US00D404373S

United States Patent [19]

Kimura

[11] **Patent Number: Des. 404,373**

[45] **Date of Patent: **Jan. 19, 1999**

[54] **FIN FOR USE IN A SEMICONDUCTOR WAFER HEAT PROCESSING APPARATUS**

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[73] Assignee: **Tokyo Electron Limited**, Tokyo-To, Japan

[**] Term: **14 Years**

[21] Appl. No.: **83,716**

[22] Filed: **Feb. 12, 1998**

[30] **Foreign Application Priority Data**

Aug. 20, 1997 [JP] Japan 9-65103

[51] **LOC (6) Cl.** **13-03**

[52] **U.S. Cl.** **D13/182**

[58] **Field of Search** D13/182, 179; D15/144, 144.1, 199; 414/935–941, 217, 147; 437/247, 946

[56] **References Cited**

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Primary Examiner—Brian N. Vinson
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[57] **CLAIM**

I claim the ornamental design for a fin for use in a semiconductor wafer heat processing apparatus, as shown and described.

DESCRIPTION

FIG. **1** a perspective view of a fin for use in a semiconductor wafer heat processing apparatus;

FIG. **2** a front elevational view thereof;

FIG. **3** a right side view thereof;

FIG. **4** a top plan view thereof;

FIG. **5** a bottom plan view therof;

FIG. **6** a cross-sectional view taken along line VI-VI in FIG. **2**; and,

FIG. **7** a cross-sectional view taken alone line VII-VII in FIG. **4**.

1 Claim, 1 Drawing Sheet

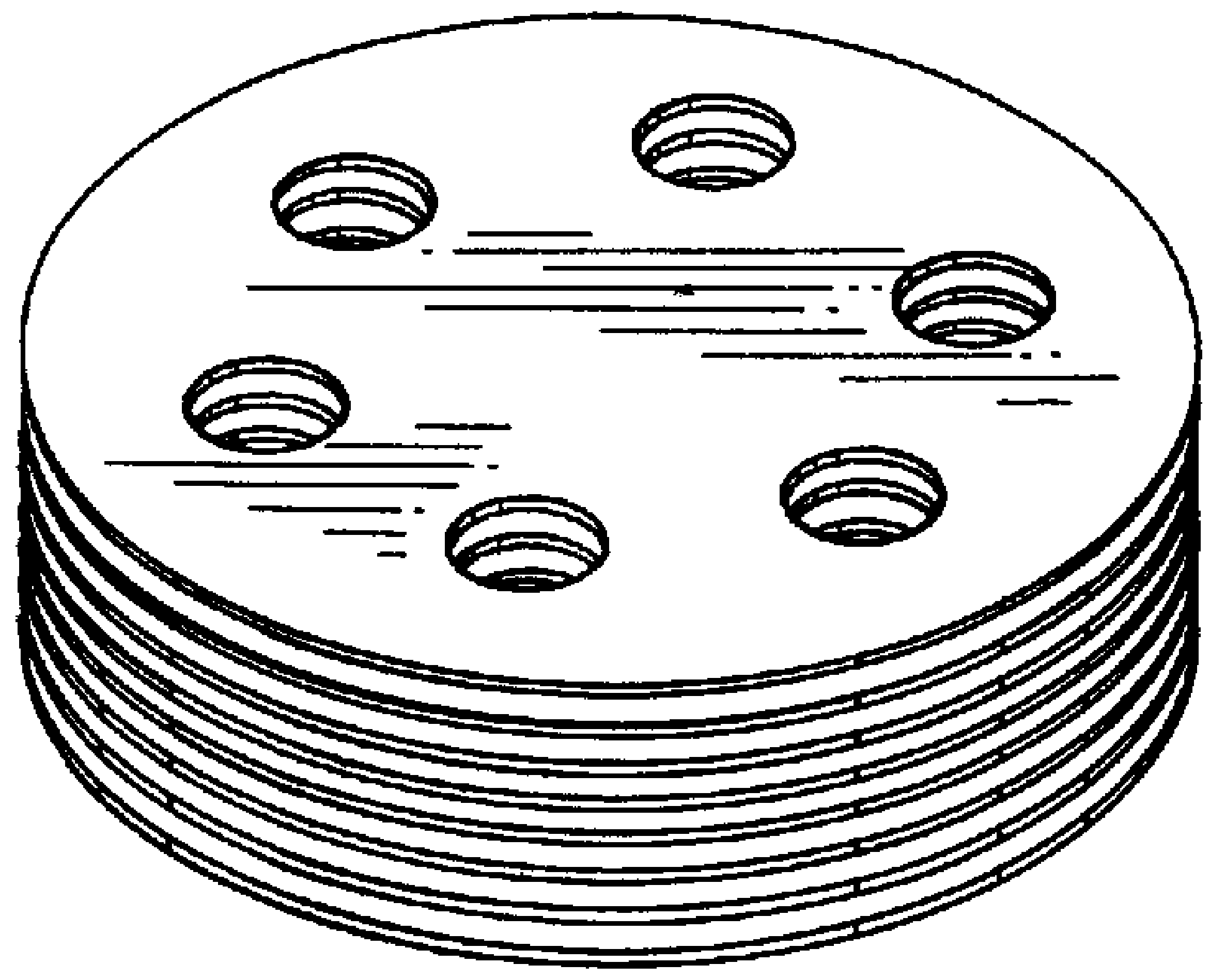

FIG. 1

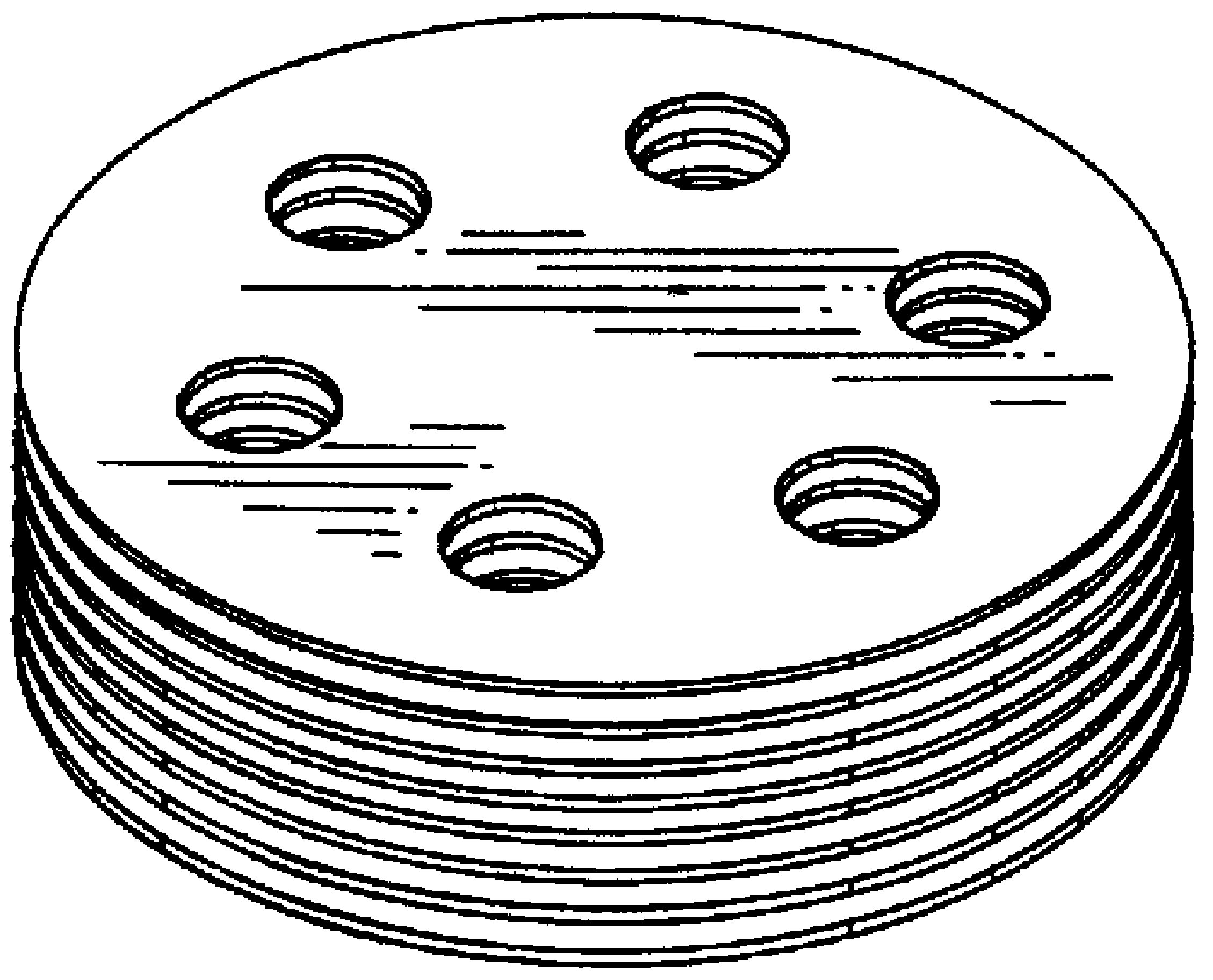

FIG. 2

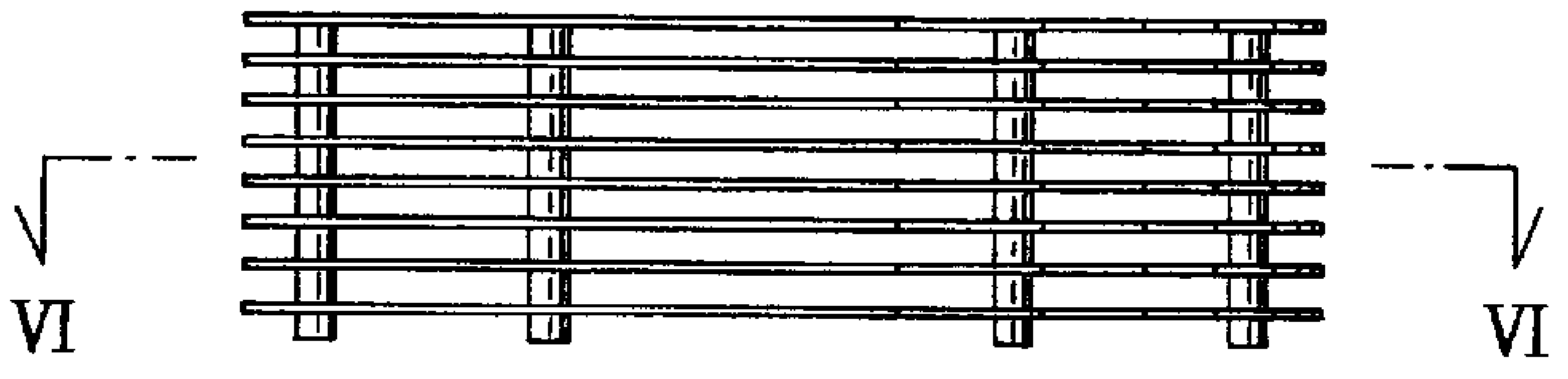

FIG. 3

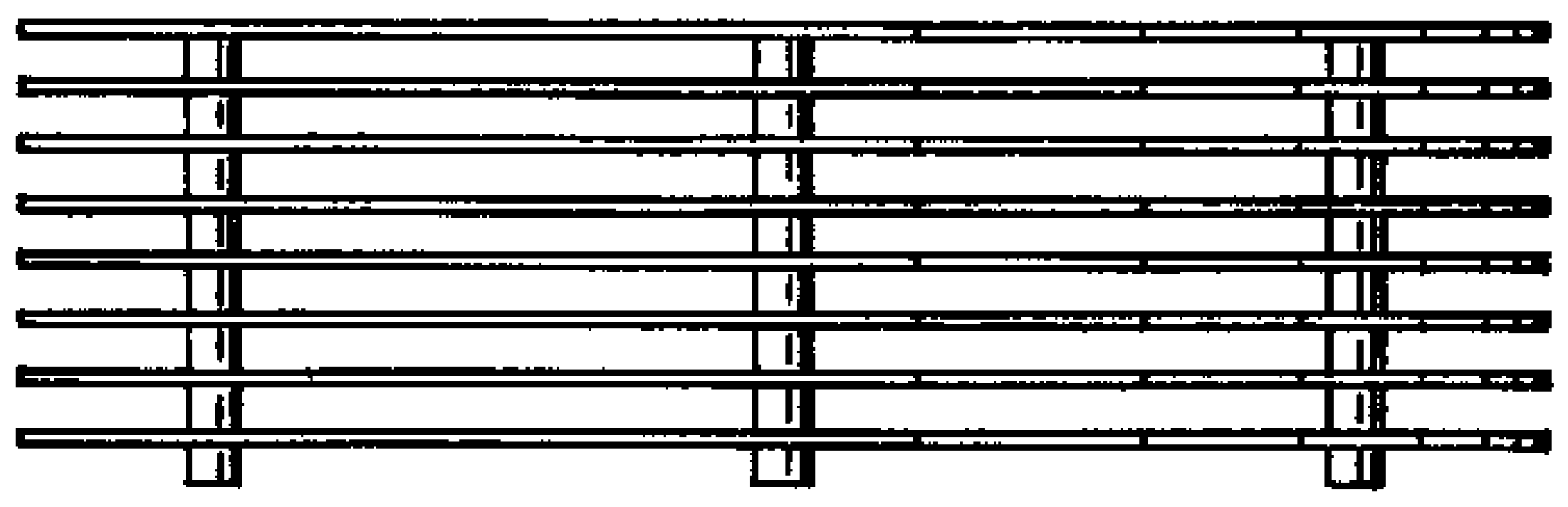

FIG. 4

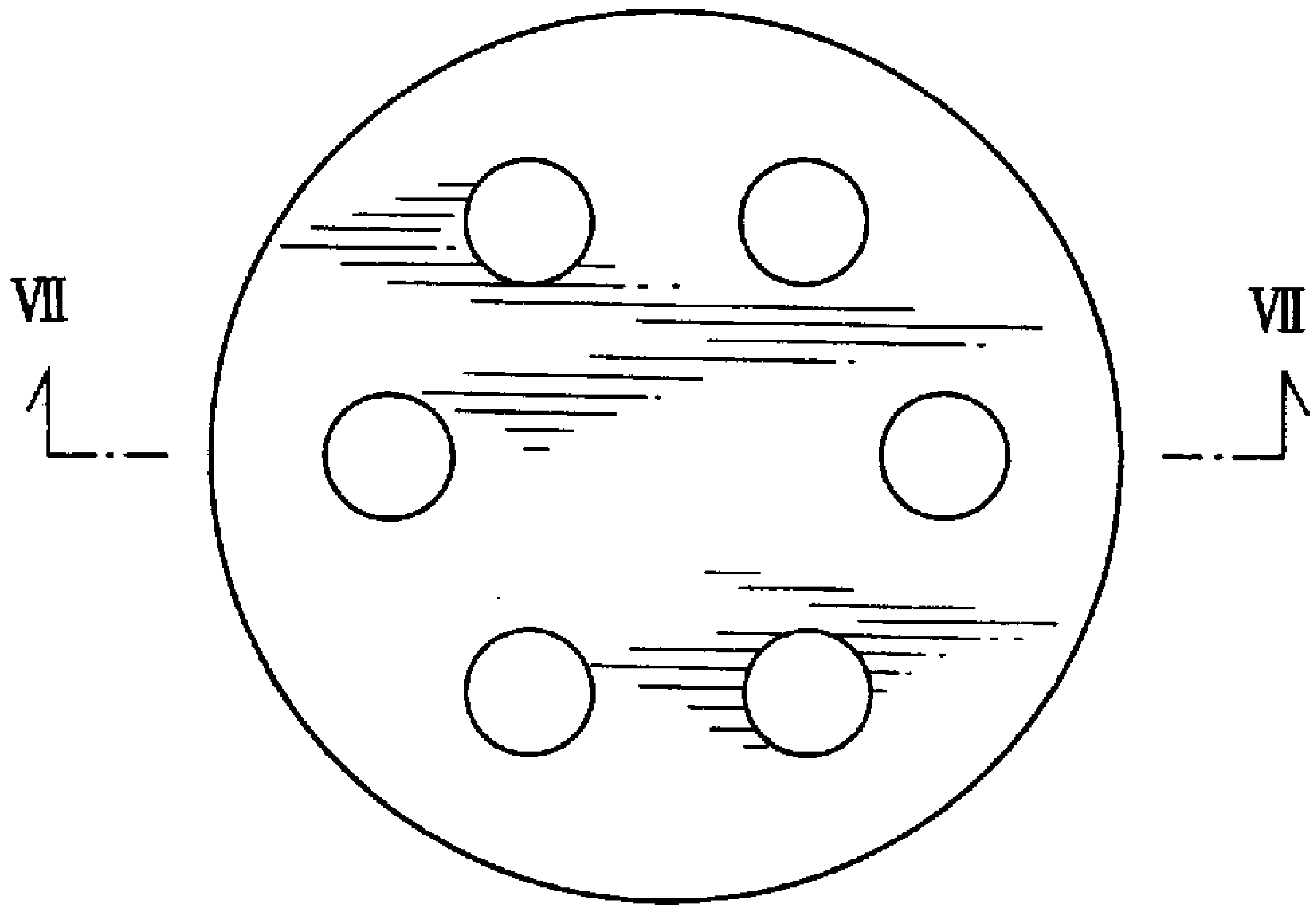

FIG. 5

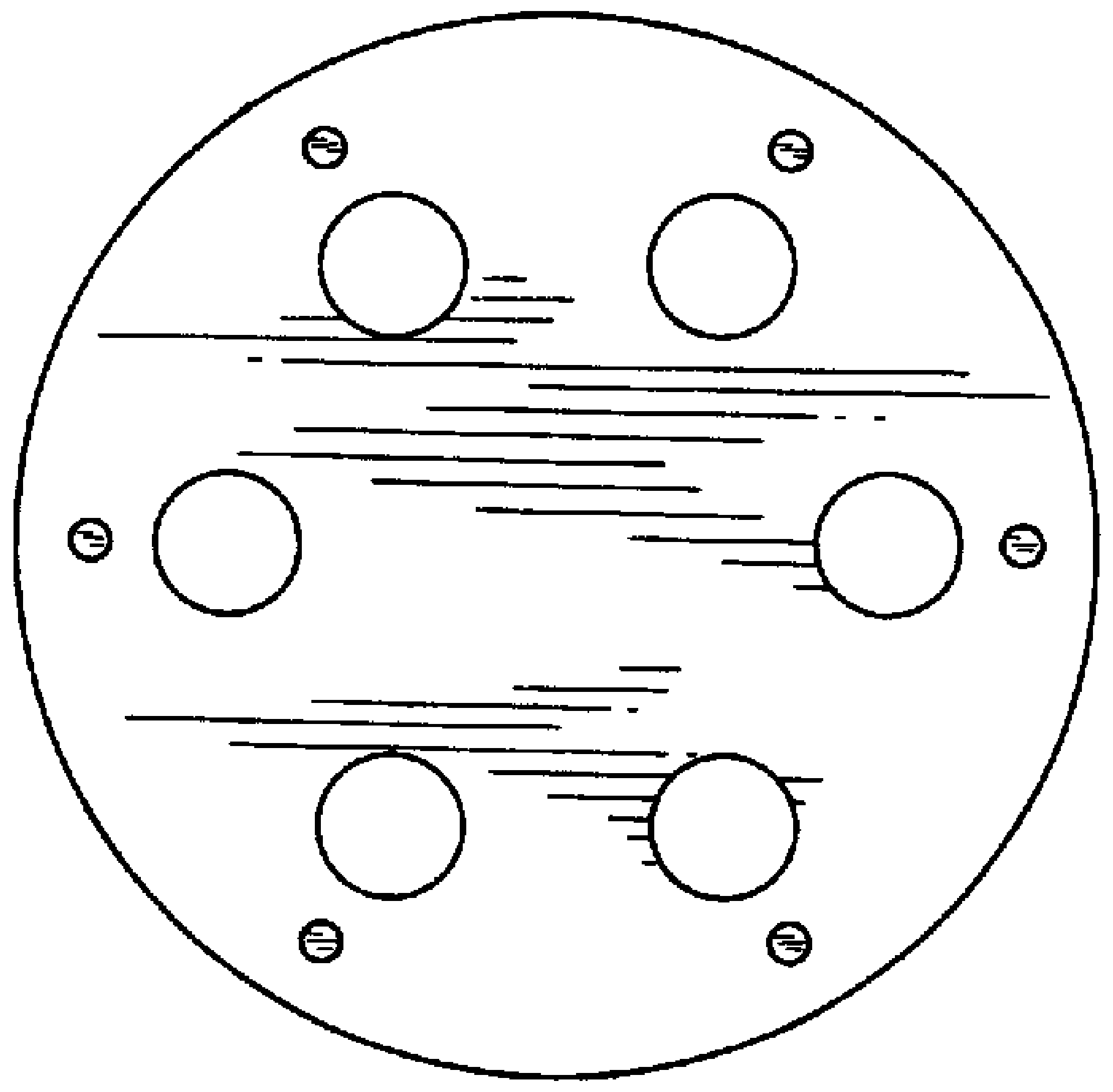

FIG. 6

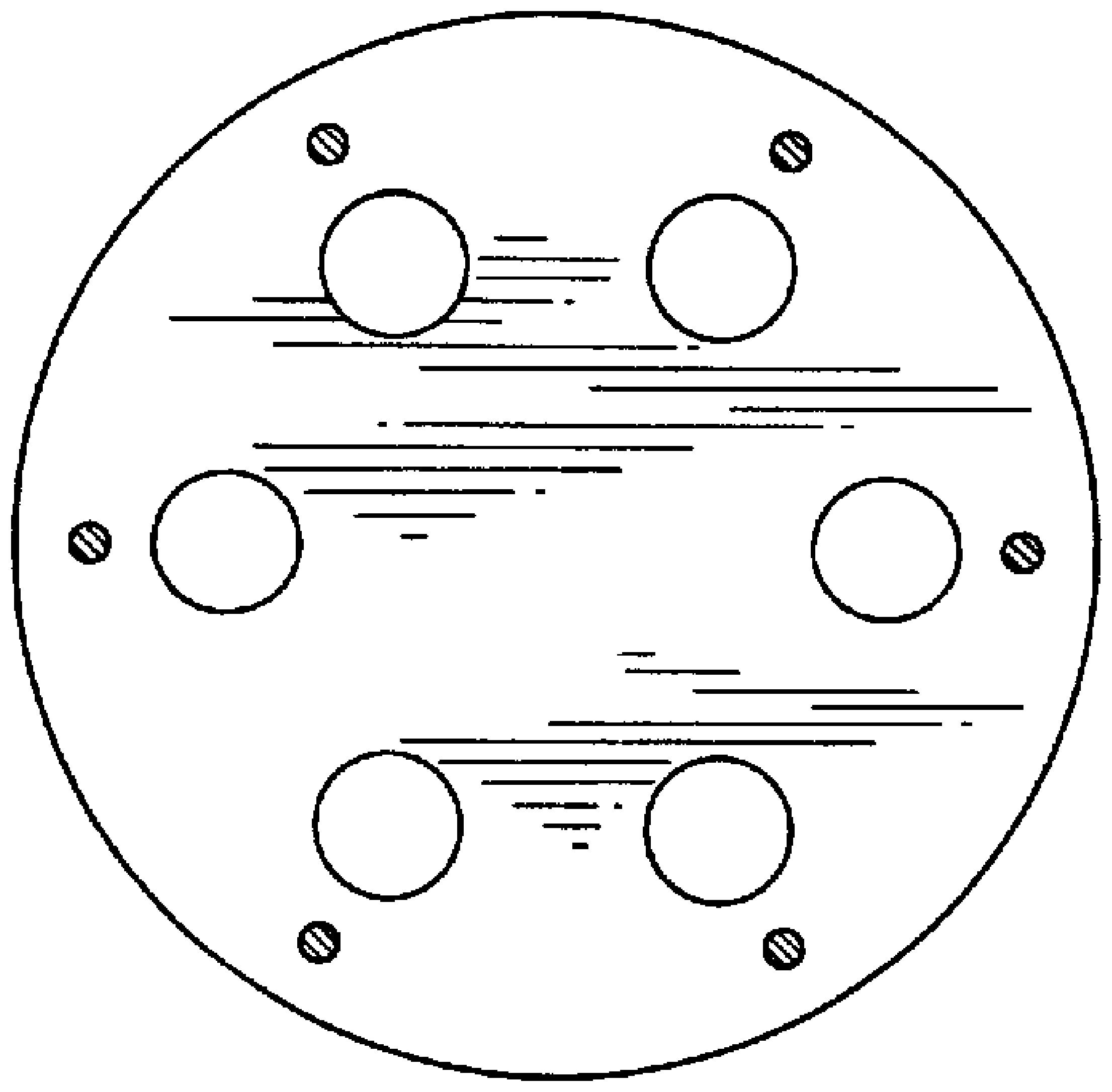

FIG. 7