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United States Patent [19]

Rosner et al.

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[45] **Date of Patent: **Dec. 8, 1998**

[54] **FAULT TOLERANT COMMUNICATION MONITOR**

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[73] Assignee: **General Electric Company**, New York, N.Y.

[**] Term: **14 Years**

[21] Appl. No.: **74,453**

[22] Filed: **Aug. 4, 1997**

[51] **LOC (6) Cl.** **14-02**

[52] **U.S. Cl.** **D14/113**; D10/46

[58] **Field of Search** D14/100, 106, D14/113, 124–129; 248/917–924; 345/104, 133, 156, 168, 173, 87, 901–905; 348/180, 184, 325, 739; D10/46, 104

[56] **References Cited**

U.S. PATENT DOCUMENTS

D. 310,210 8/1990 Ishida ..... D14/113 X

D. 317,447 6/1991 Ishida D14/113

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Attorney, Agent, or Firm—Richard A. Menelly; Carl B. Horton

[57] **CLAIM**

The ornamental design for a fault tolerant communication monitor, as shown and described.

DESCRIPTION

FIG. **1** is front view of the fault tolerant communication monitor according to the invention;

FIG. **2** is a left side view of the fault tolerant communication monitor of FIG. **1**;

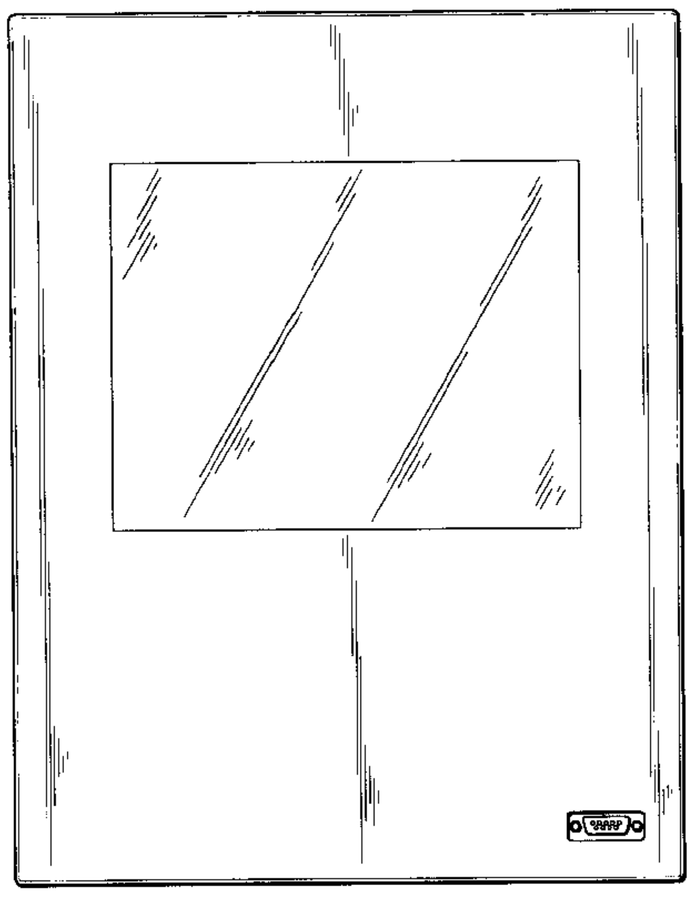
FIG. **3** is a rear view of the fault tolerant communication monitor of FIG. **1**;

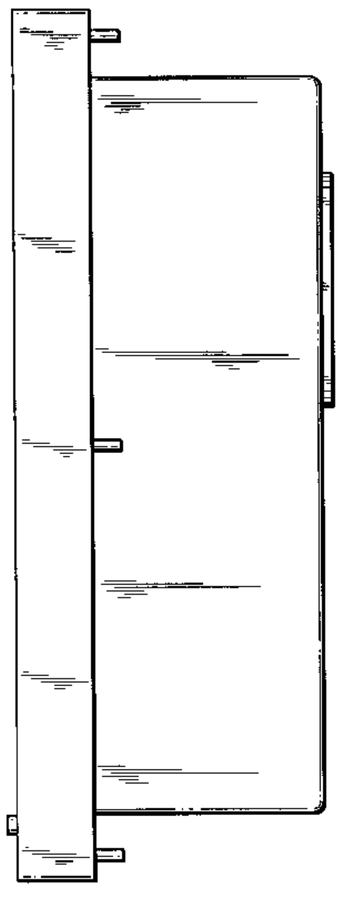
FIG. **4** is a right side view of the fault tolerant communication monitor of FIG. **1**;

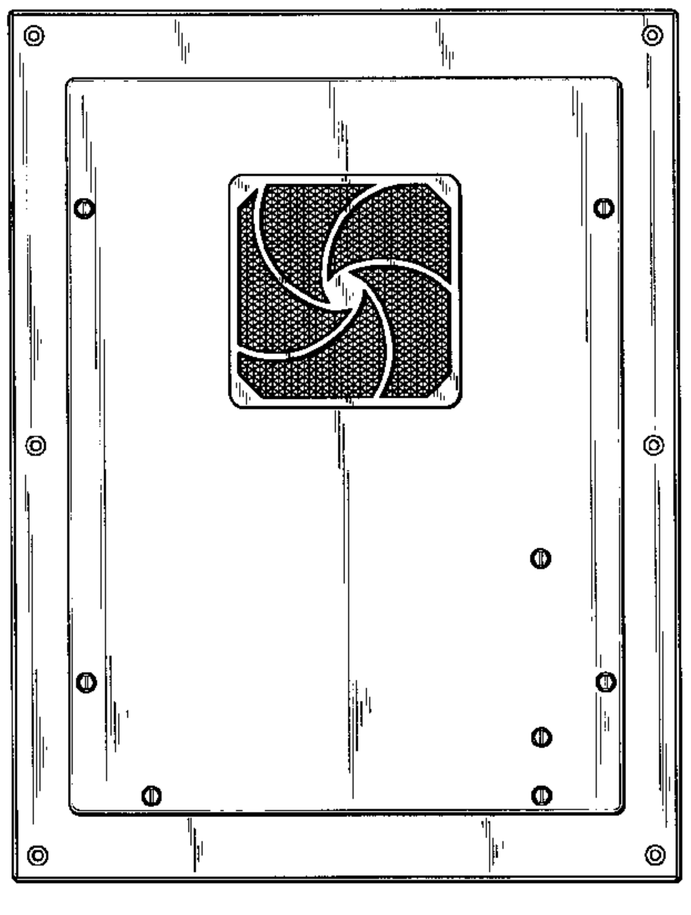
FIG. **5** is a top plan view of the fault tolerant communication monitor of FIG. **1**; and,

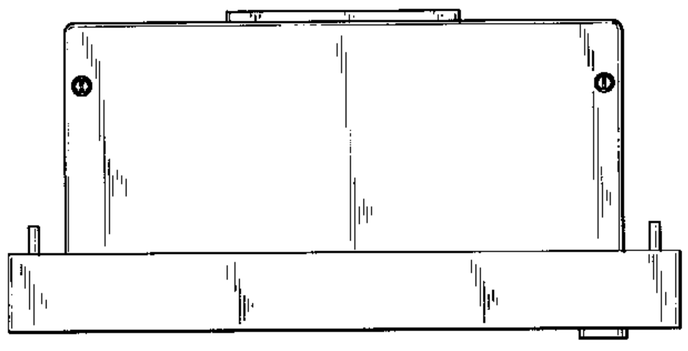
FIG. **6** is a bottom plan view of the fault tolerant communication monitor of FIG. **1**.

1 Claim, 2 Drawing Sheets









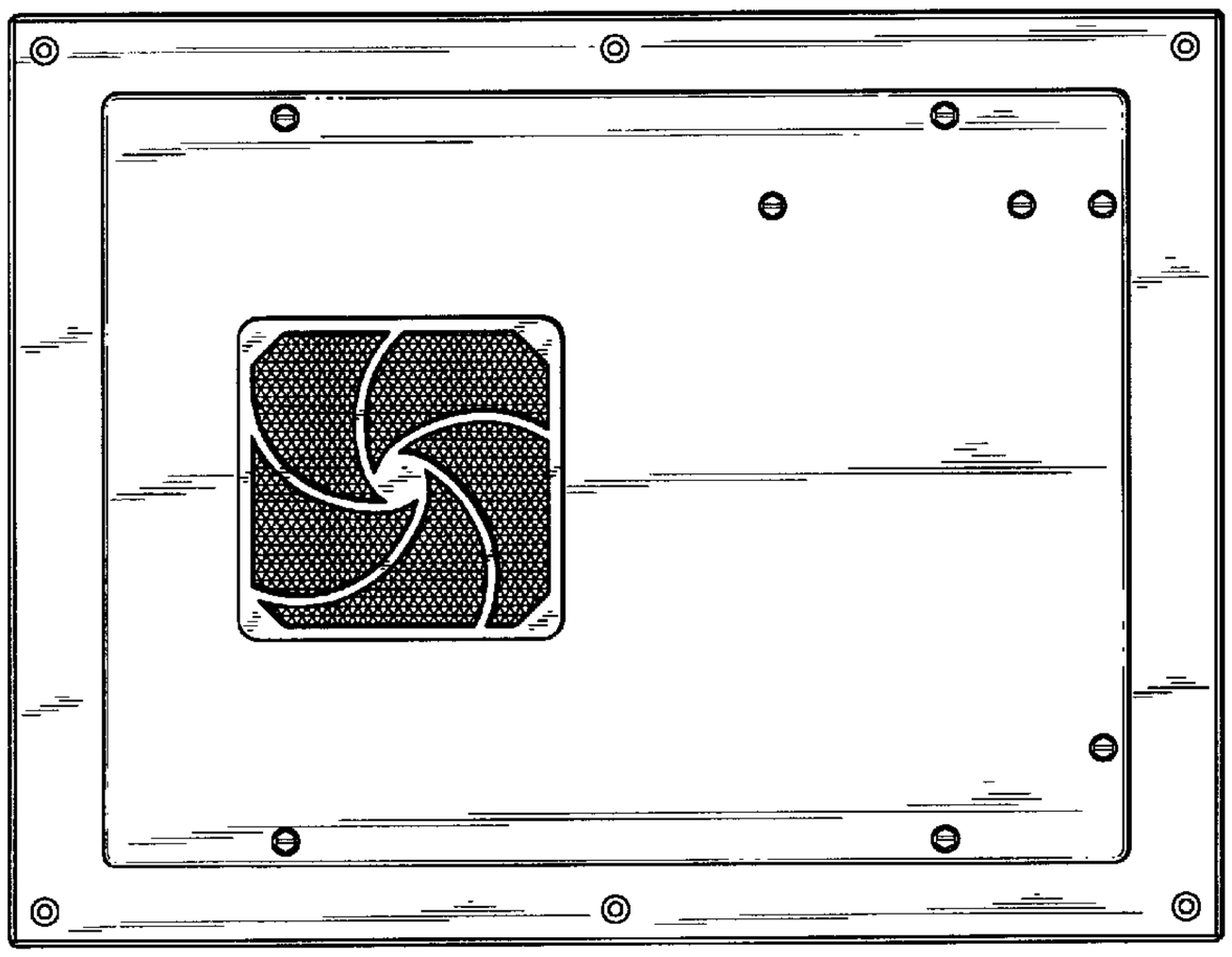


FIG. 3

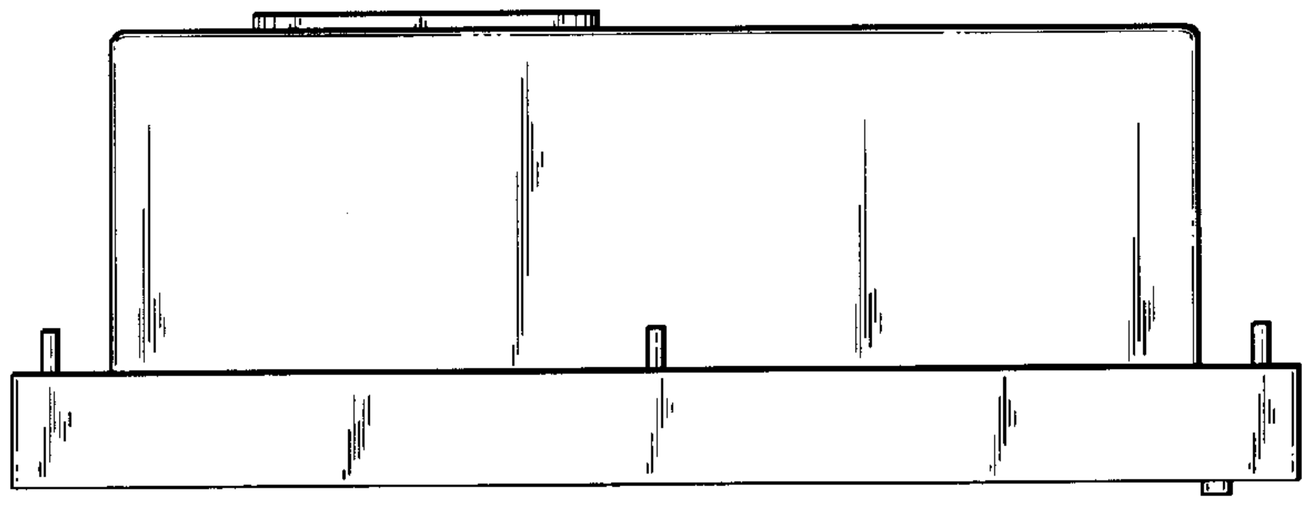


FIG. 2

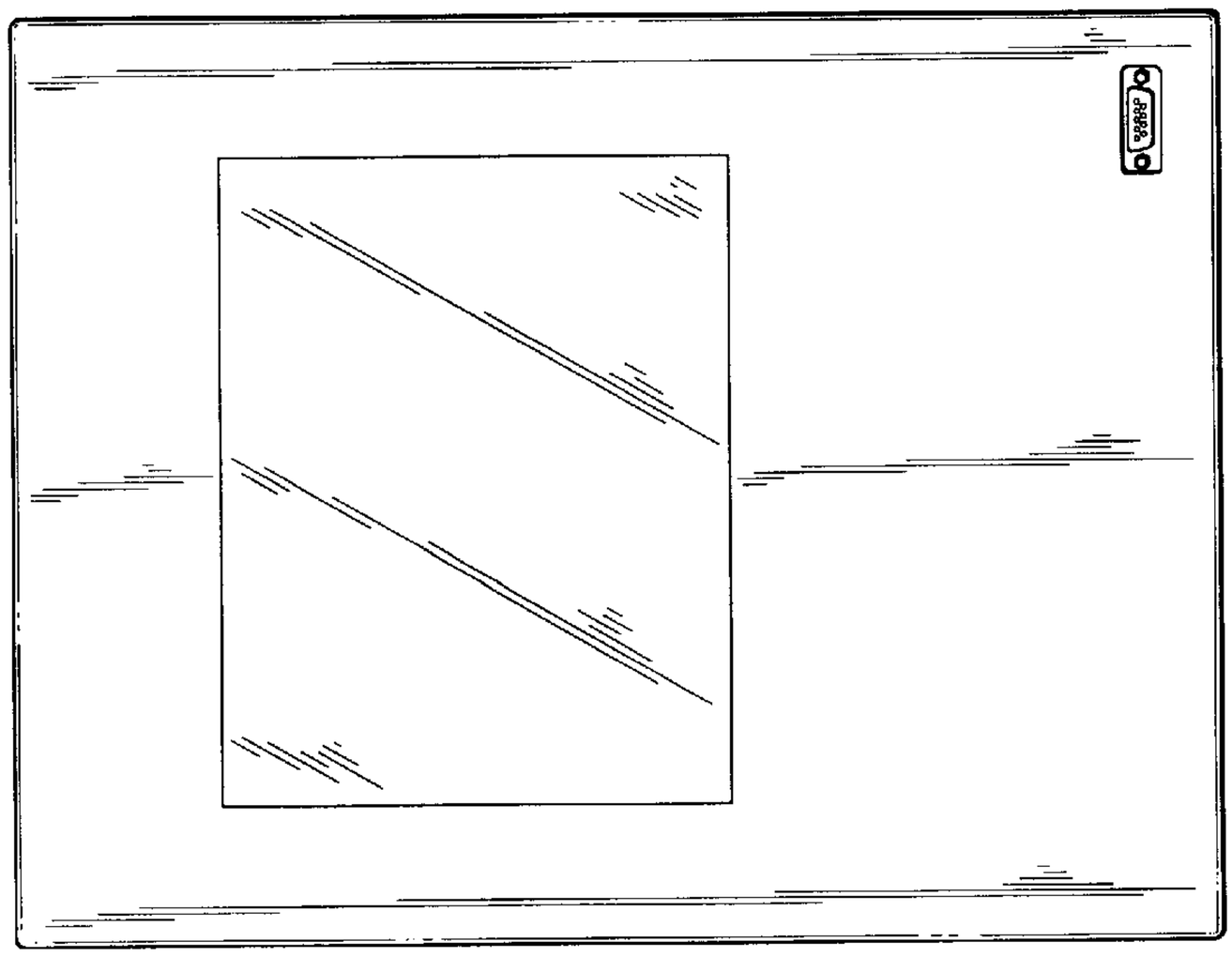


FIG. 1

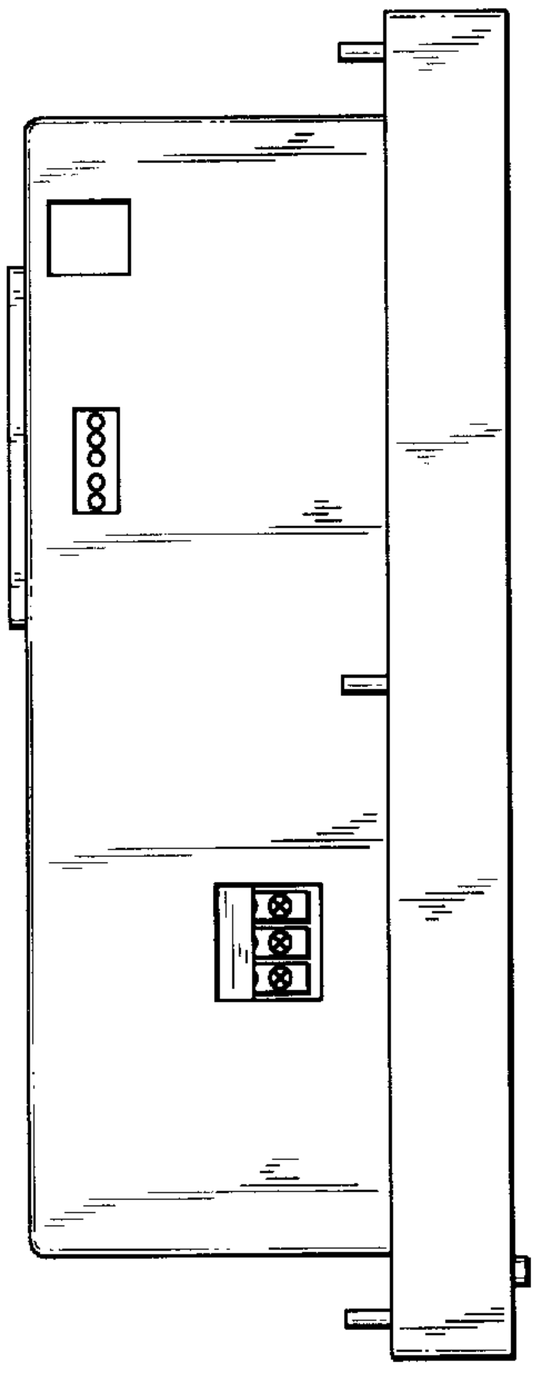


FIG. 4

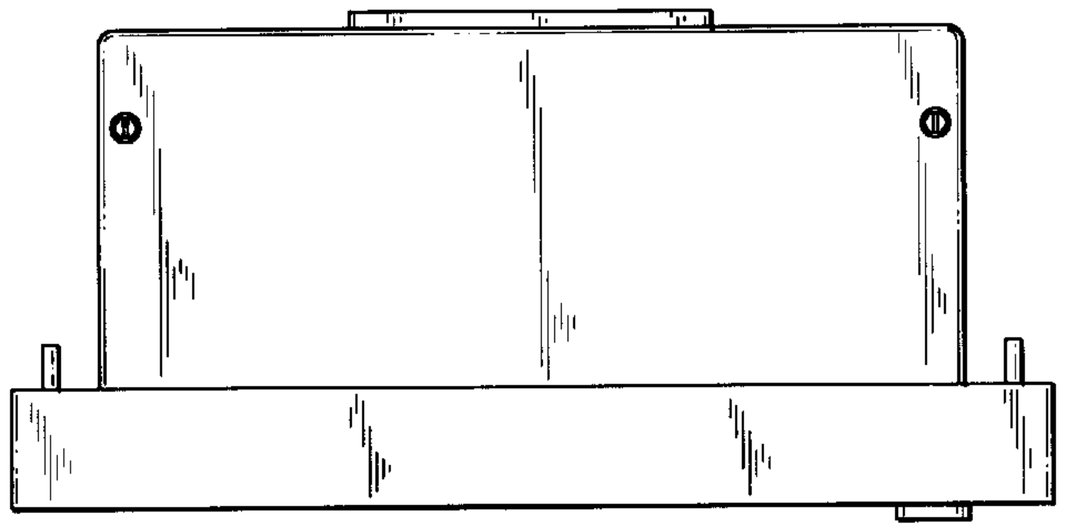


FIG. 5

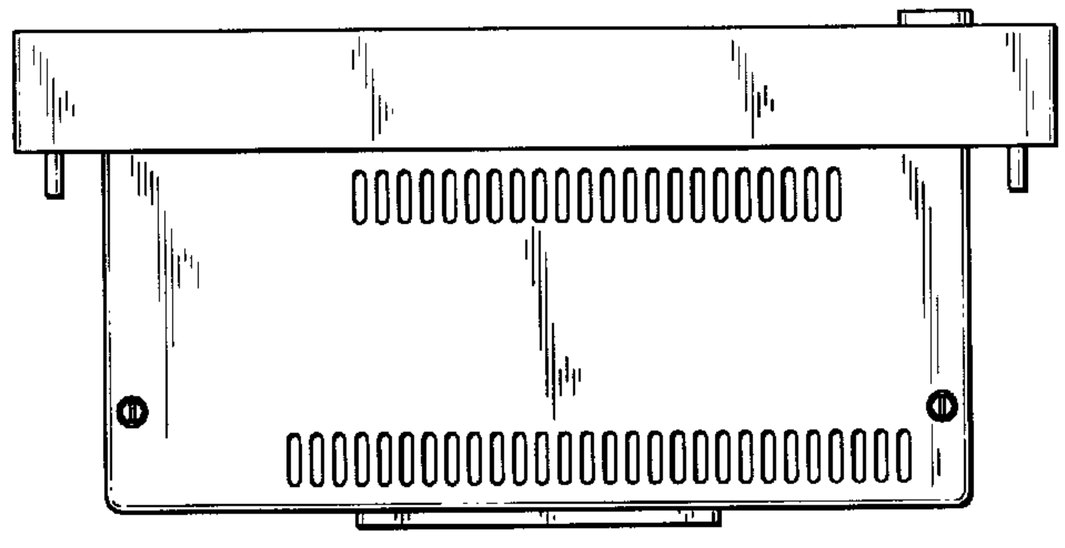


FIG. 6