



US00D402279S

# United States Patent [19]

Rosner et al.

[11] **Patent Number: Des. 402,279**

[45] **Date of Patent: \*\*Dec. 8, 1998**

[54] **FAULT TOLERANT COMMUNICATION MONITOR**

D. 317,447 6/1991 Ishida ..... D14/113

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[73] Assignee: **General Electric Company**, New York, N.Y.

[57] **CLAIM**

The ornamental design for a fault tolerant communication monitor, as shown and described.

[\*\*] Term: **14 Years**

**DESCRIPTION**

[21] Appl. No.: **74,453**

FIG. 1 is front view of the fault tolerant communication monitor according to the invention;

[22] Filed: **Aug. 4, 1997**

FIG. 2 is a left side view of the fault tolerant communication monitor of FIG. 1;

[51] **LOC (6) Cl.** ..... **14-02**

FIG. 3 is a rear view of the fault tolerant communication monitor of FIG. 1;

[52] **U.S. Cl.** ..... **D14/113; D10/46**

FIG. 4 is a right side view of the fault tolerant communication monitor of FIG. 1;

[58] **Field of Search** ..... D14/100, 106, D14/113, 124-129; 248/917-924; 345/104, 133, 156, 168, 173, 87, 901-905; 348/180, 184, 325, 739; D10/46, 104

FIG. 5 is a top plan view of the fault tolerant communication monitor of FIG. 1; and,

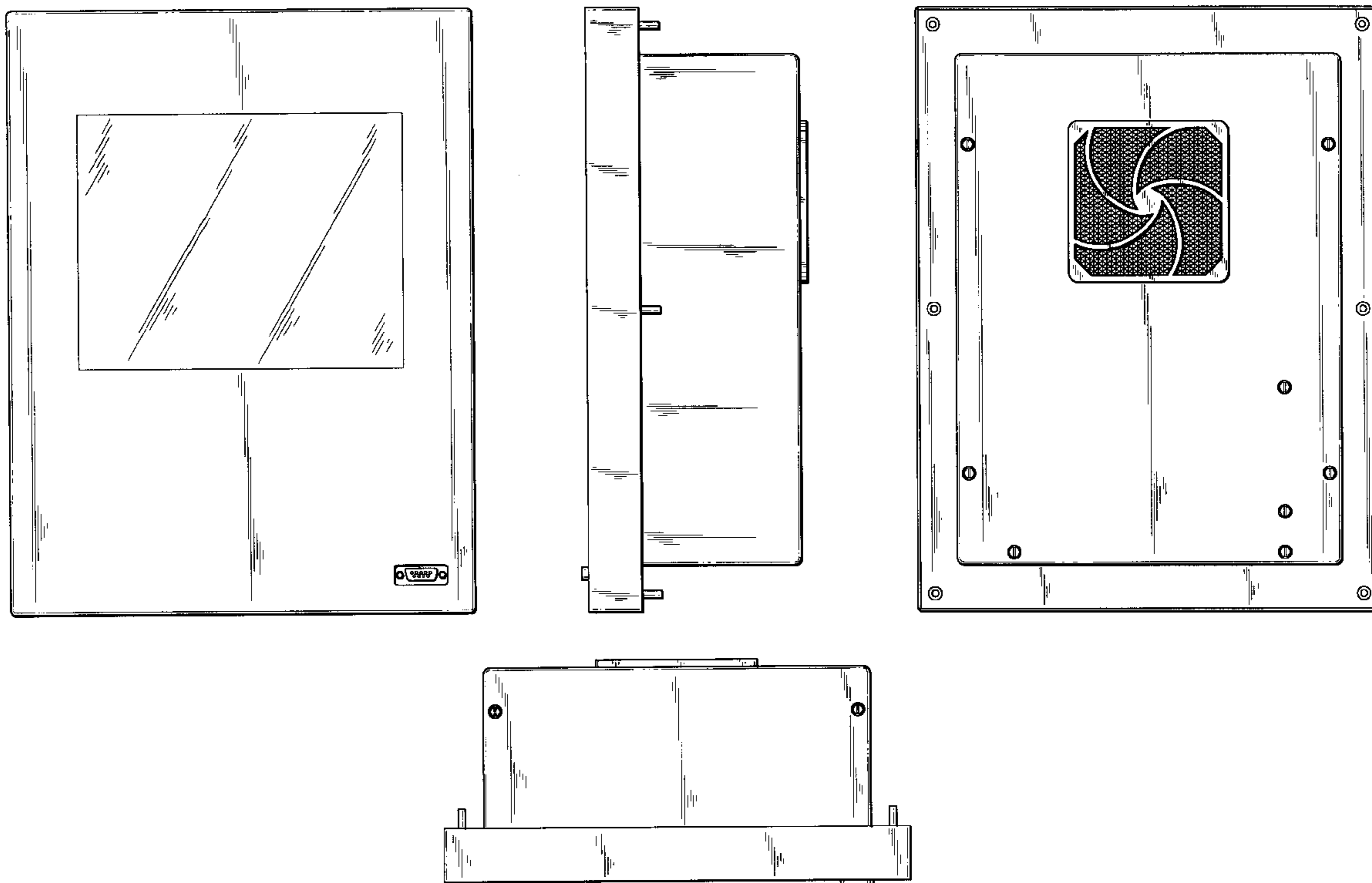
FIG. 6 is a bottom plan view of the fault tolerant communication monitor of FIG. 1.

[56] **References Cited**

**U.S. PATENT DOCUMENTS**

D. 310,210 8/1990 Ishida ..... D14/113 X

**1 Claim, 2 Drawing Sheets**



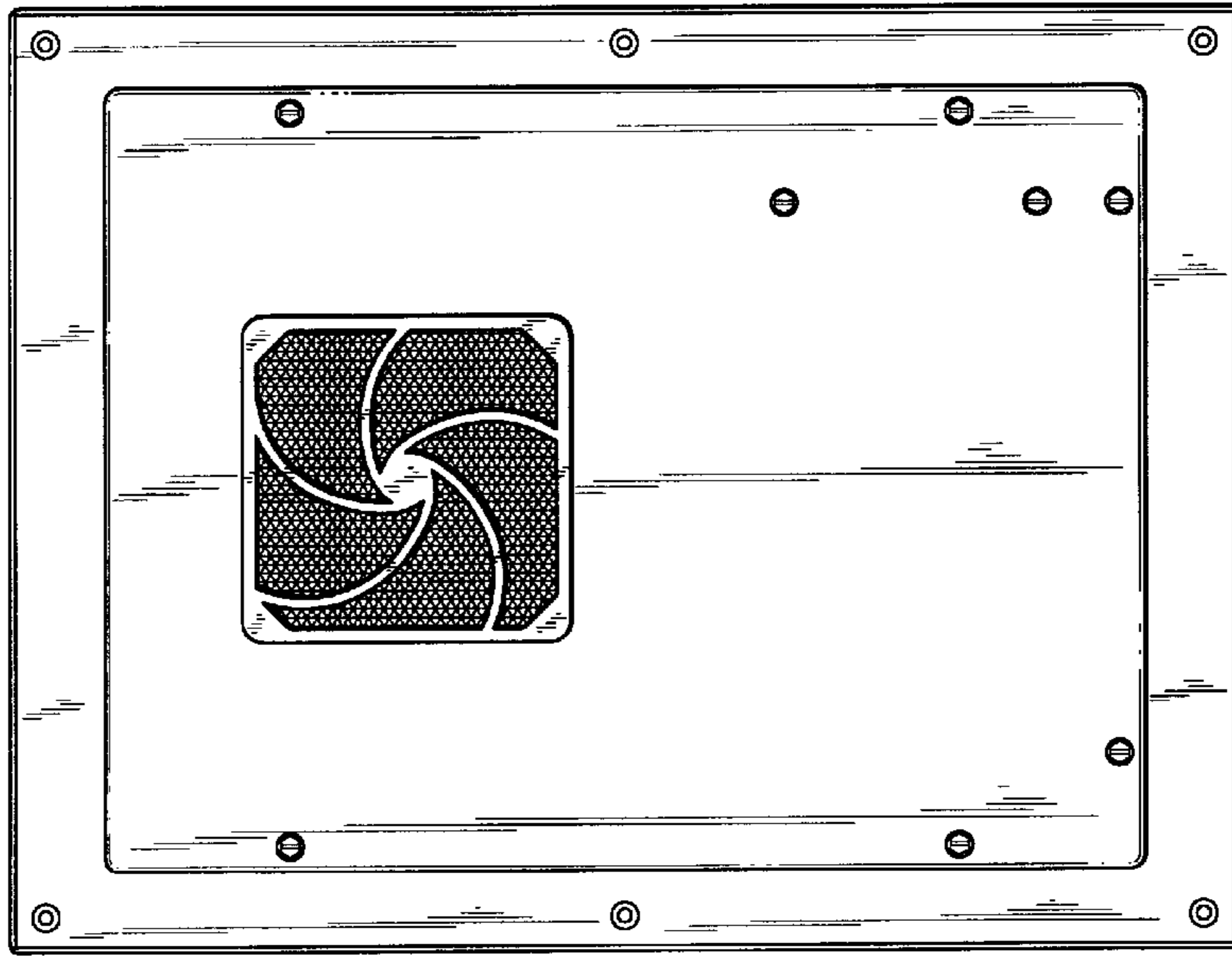


FIG. 3

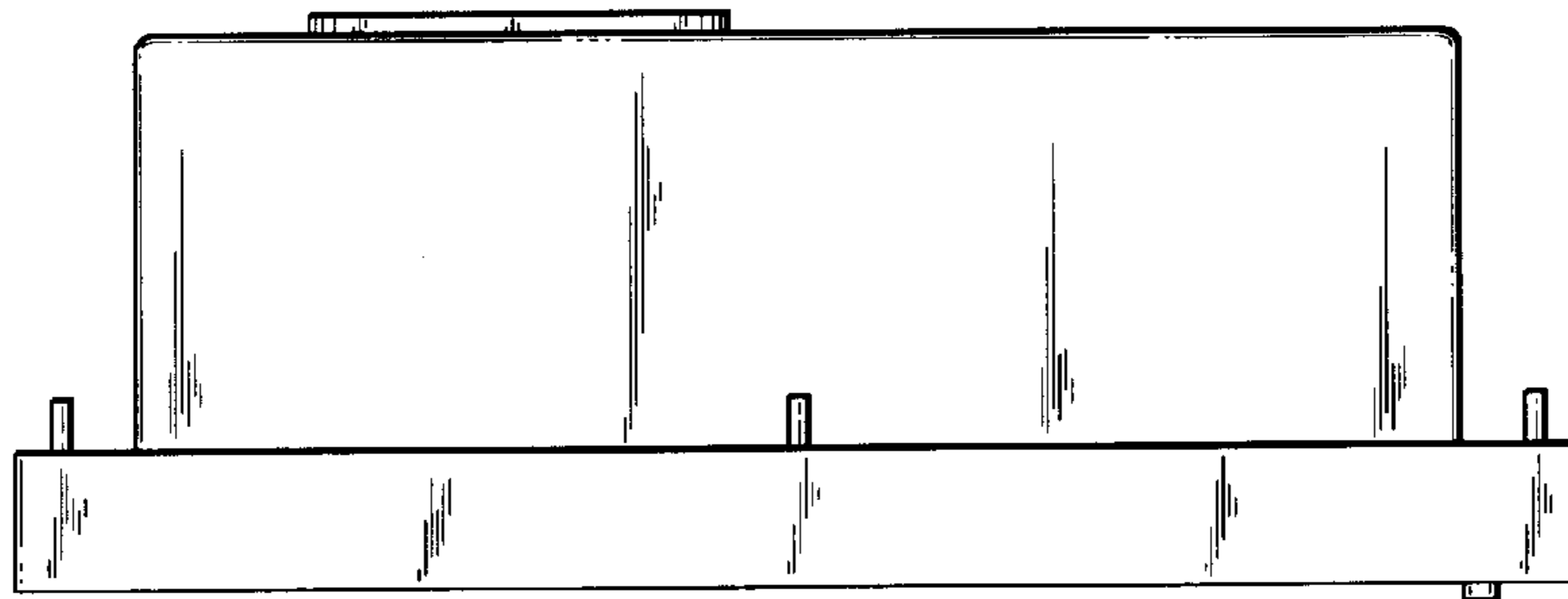


FIG. 2

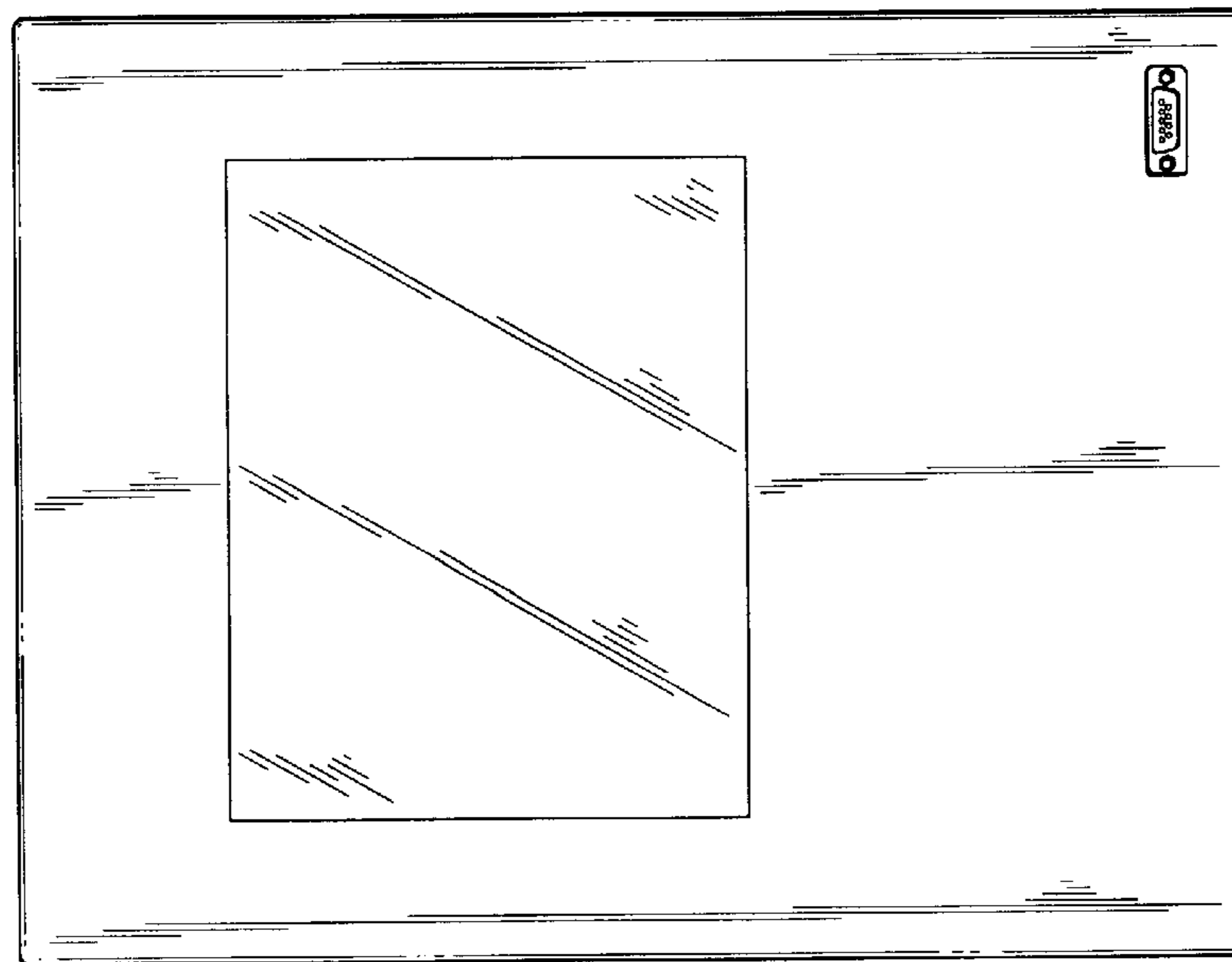


FIG. 1

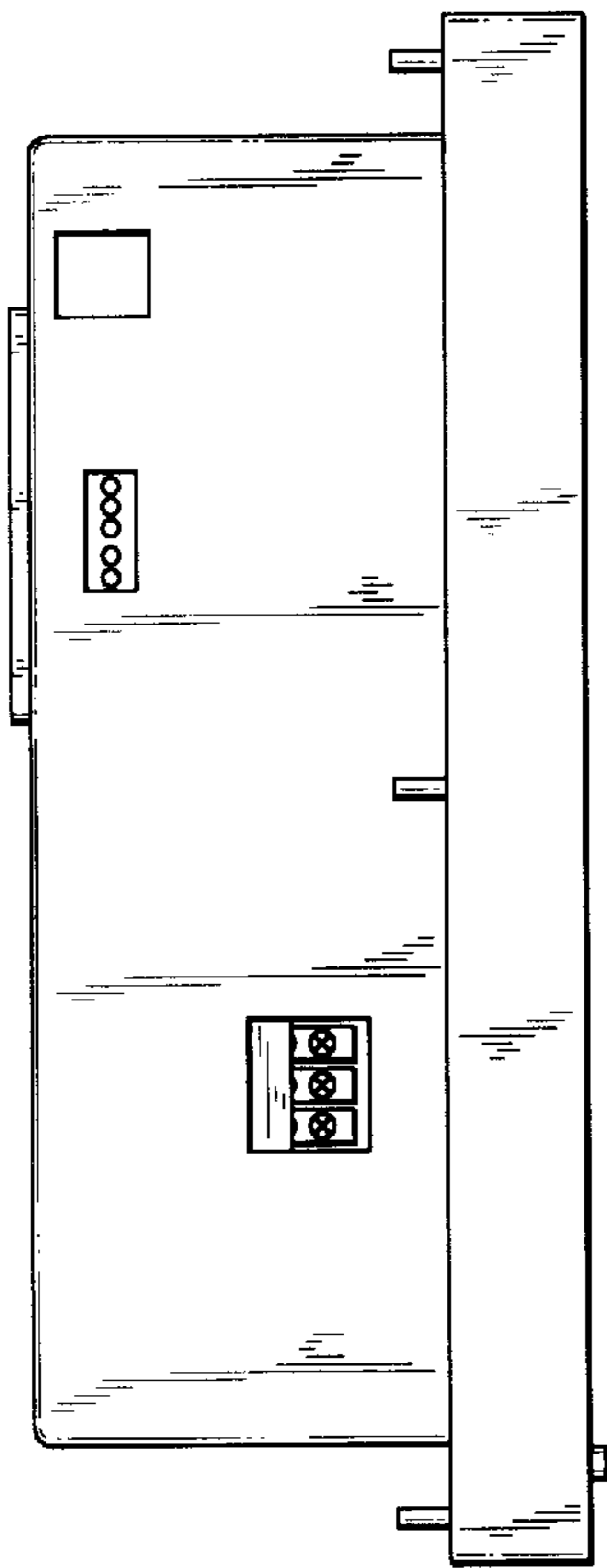


FIG. 4

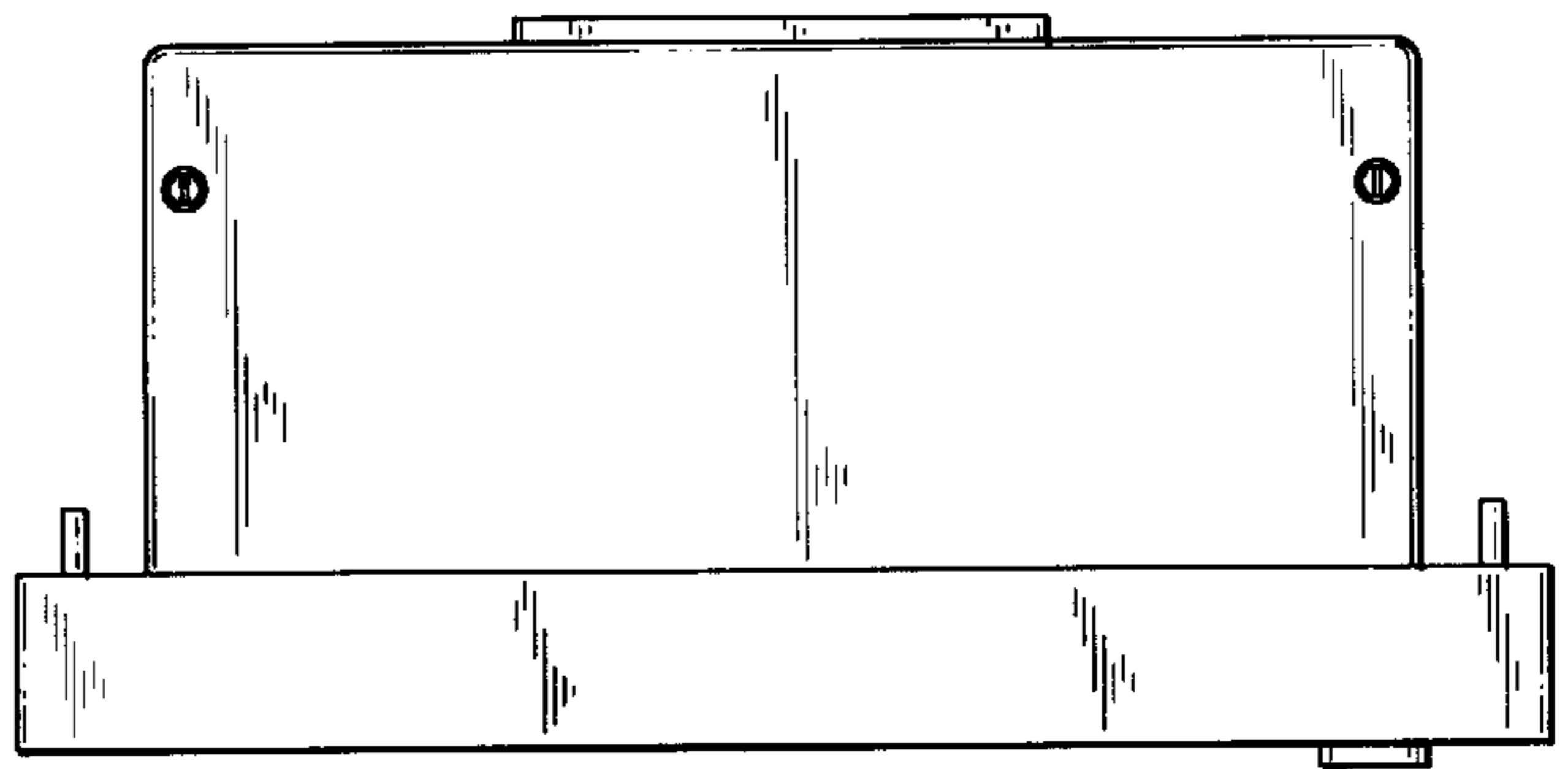


FIG. 5

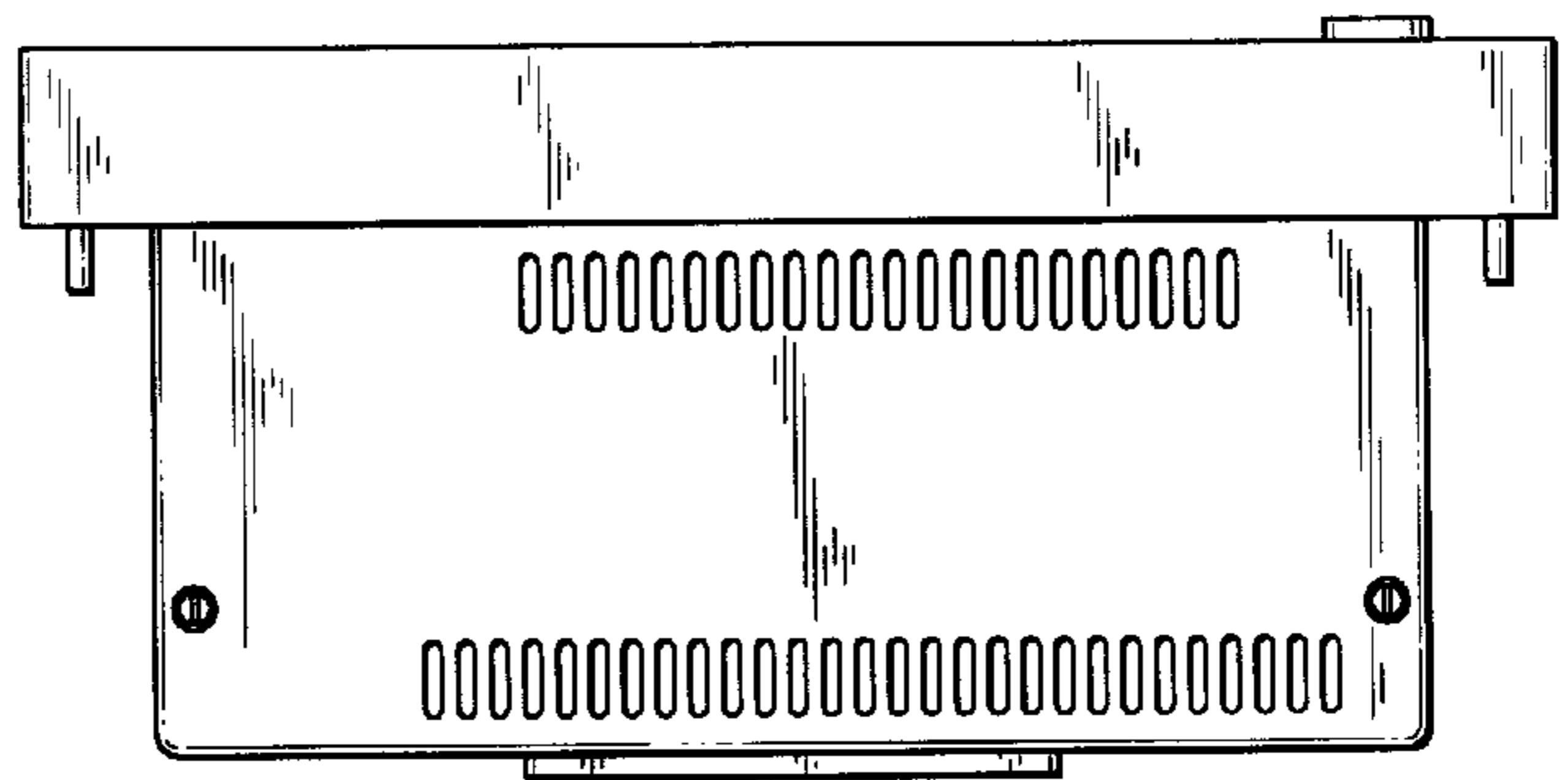


FIG. 6