



US00D401560S

United States Patent [19]
Nakamura

[11] **Patent Number: Des. 401,560**

[45] **Date of Patent: **Nov. 24, 1998**

[54] **INSULATING COVER FOR A TERMINAL**

[75] Inventor: **Toshinobu Nakamura**, Tokyo, Japan

[73] Assignee: **Shinagawa Shoko Co., Ltd.**, Tokyo, Japan

[**] Term: **14 Years**

[21] Appl. No.: **77,130**

[22] Filed: **Sep. 29, 1997**

[51] **LOC (6) Cl.** **13-03**

[52] **U.S. Cl.** **D13/156**

[58] **Field of Search** D13/156, 154;
174/74 A, 135, 138 F; 439/133, 134, 135,
136, 149, 606, 687, 731, 906

[56] **References Cited**

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Primary Examiner—Joel Sincavage
Attorney, Agent, or Firm—Banner & Witcoff, Ltd.

[57] **CLAIM**

The ornamental design for an insulating cover for a terminal, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of an insulating cover for a terminal embodying the present design;

FIG. 2 is a rear perspective view thereof;

FIG. 3 is a front view thereof;

FIG. 4 is a rear view thereof;

FIG. 5 is a top plan view thereof;

FIG. 6 is a bottom view thereof;

FIG. 7 is a left side view thereof;

FIG. 8 is a right side view thereof;

FIG. 9 is a front perspective view thereof in an opened condition;

FIG. 10 is a rear perspective view thereof in an opened condition;

FIG. 11 is a front view thereof in an opened condition;

FIG. 12 is a rear view thereof in an opened condition;

FIG. 13 is a top plan view thereof in an opened condition;

FIG. 14 is a bottom view thereof in an opened condition;

FIG. 15 is a left side view thereof in an opened condition;

FIG. 16 is a right side view thereof in an opened condition;

and,

FIG. 17 is a typical referential view thereof when it is used.

The broken line showing of the conductor and terminal in FIG. 17 is included for the purpose of illustrating the environment of the insulated cover only and forms no part of the claimed design.

1 Claim, 6 Drawing Sheets

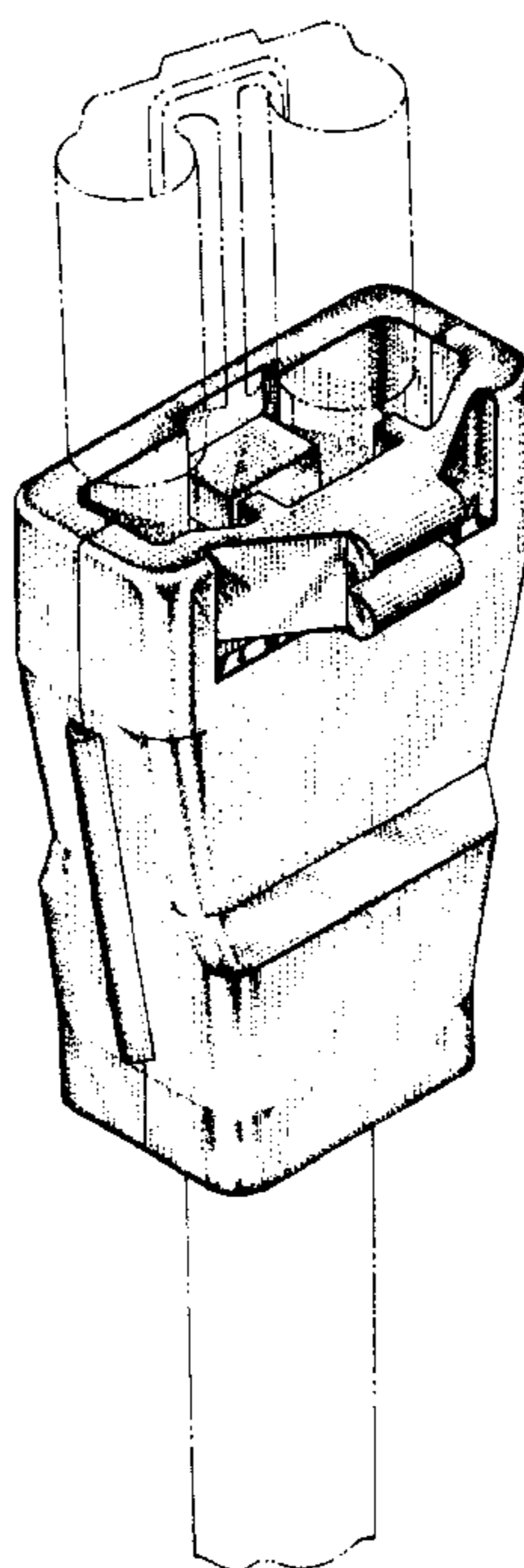


FIG. 1

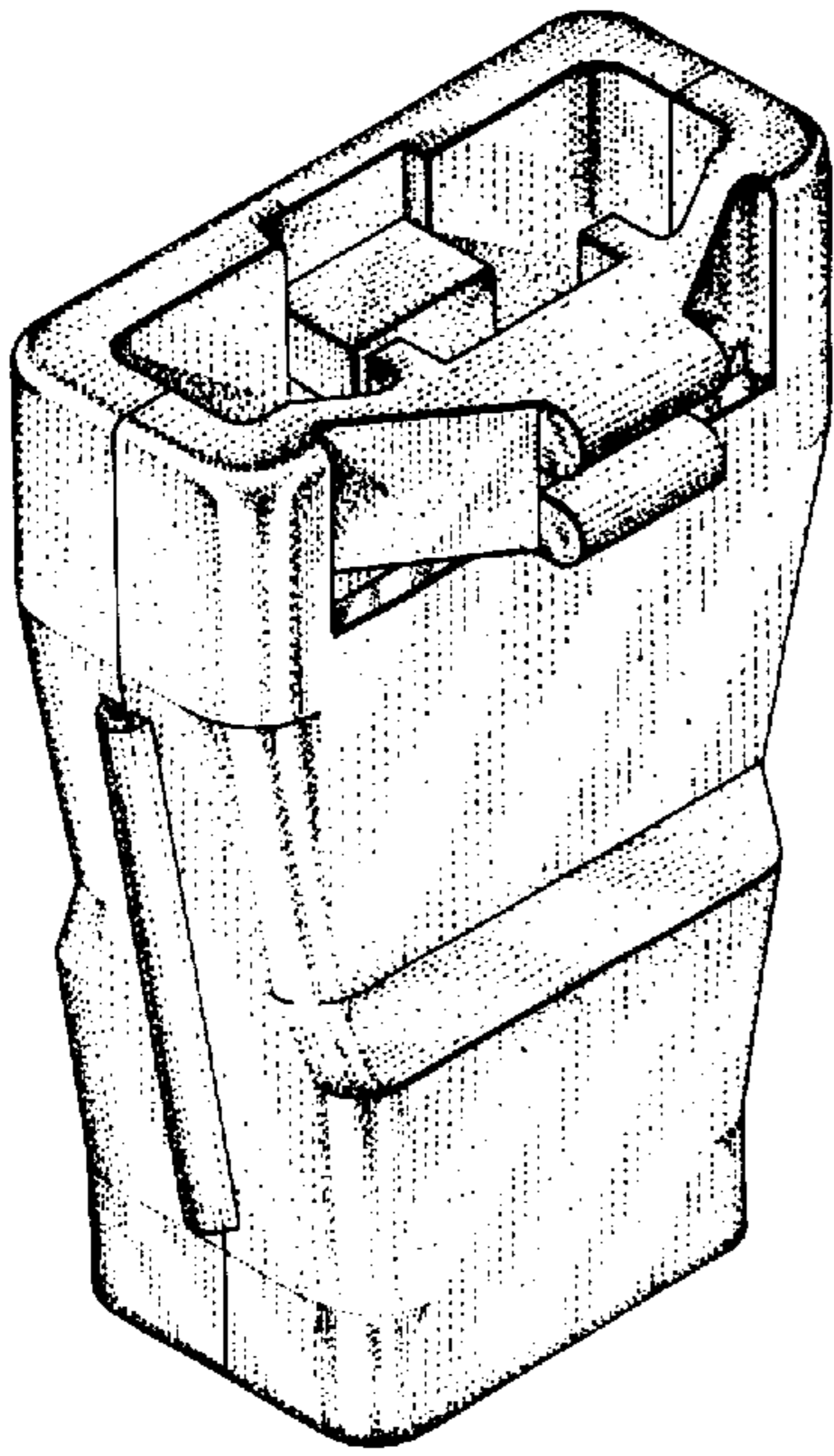


FIG. 2

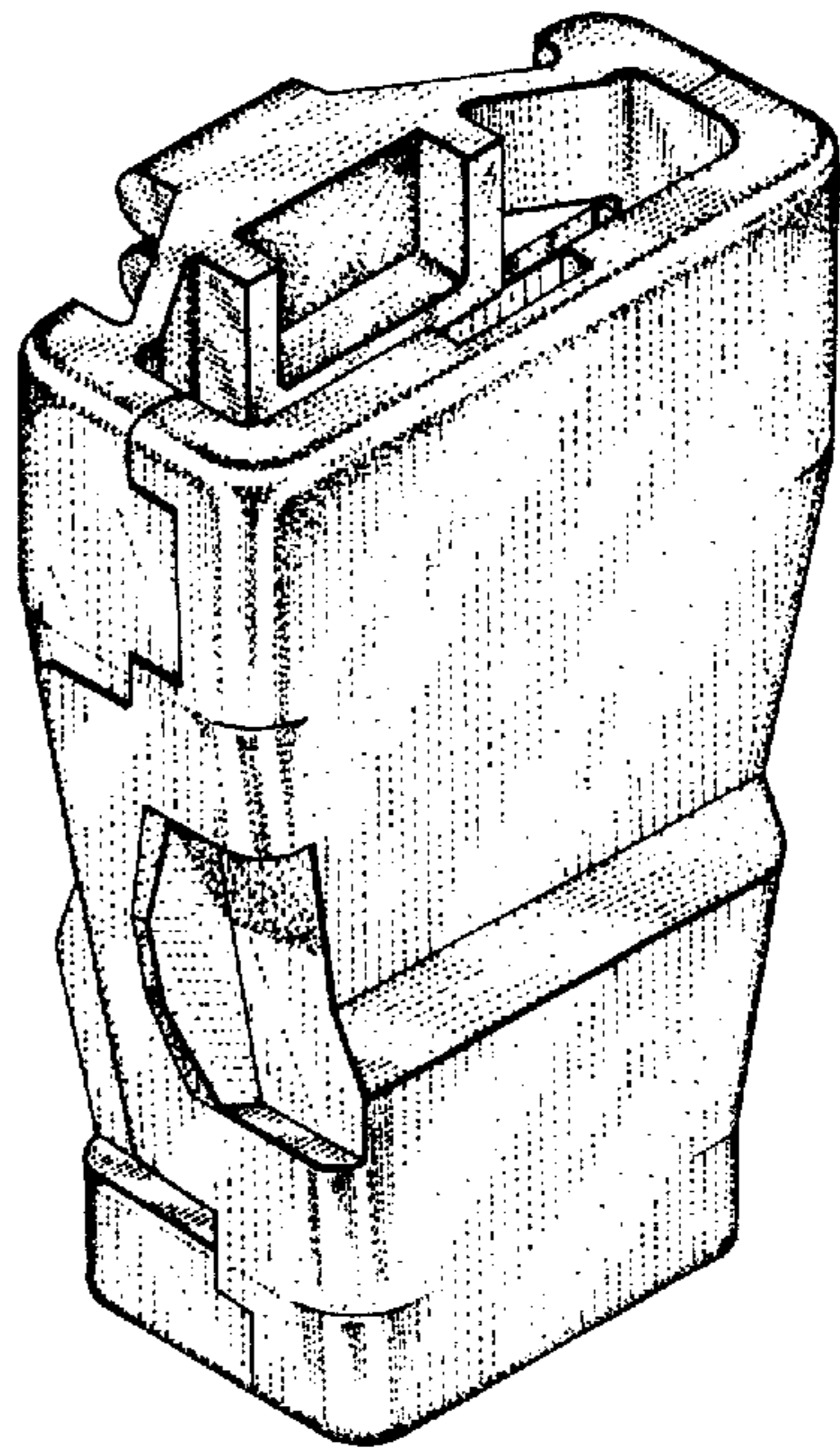


FIG. 3

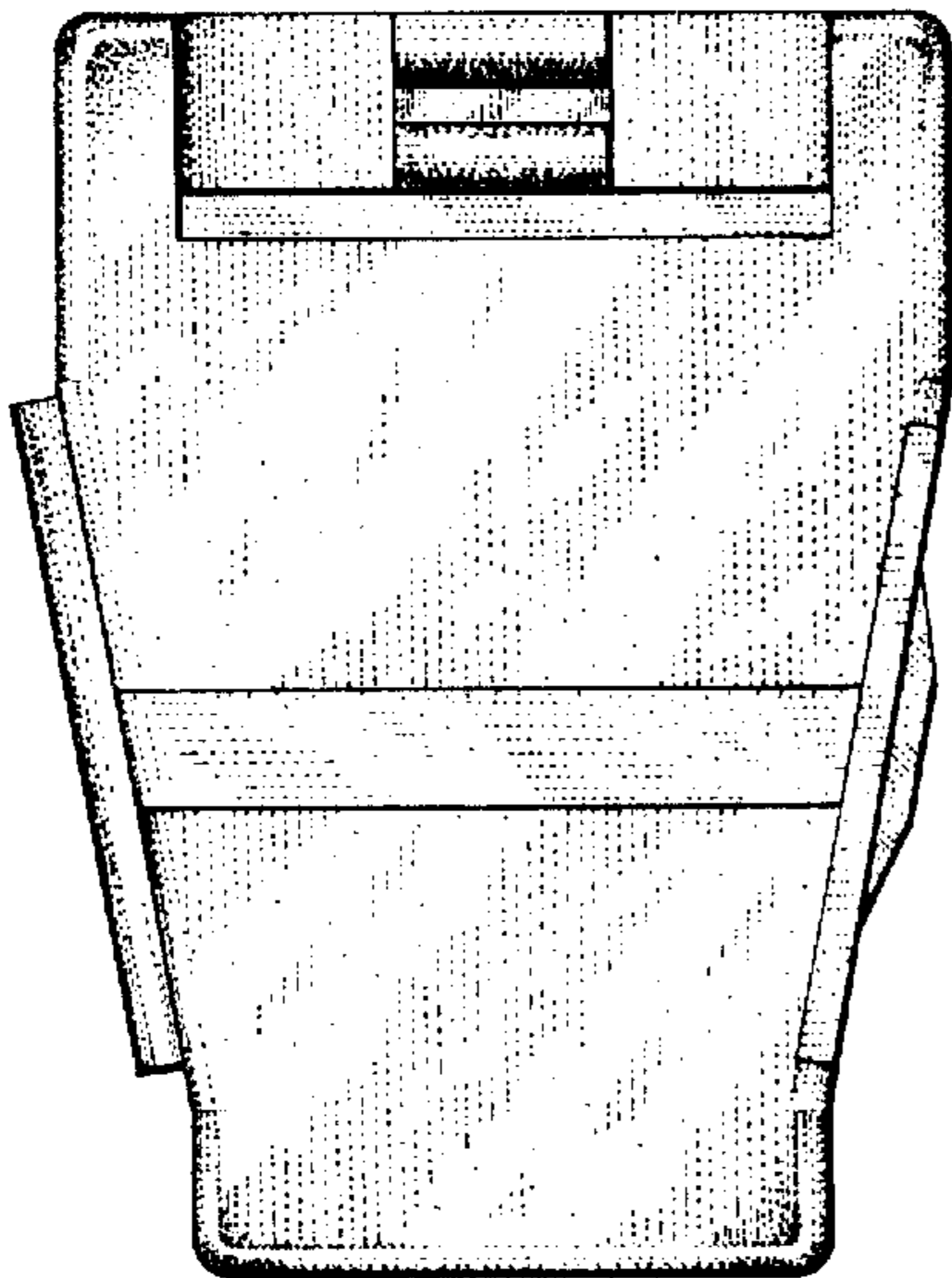


FIG. 4

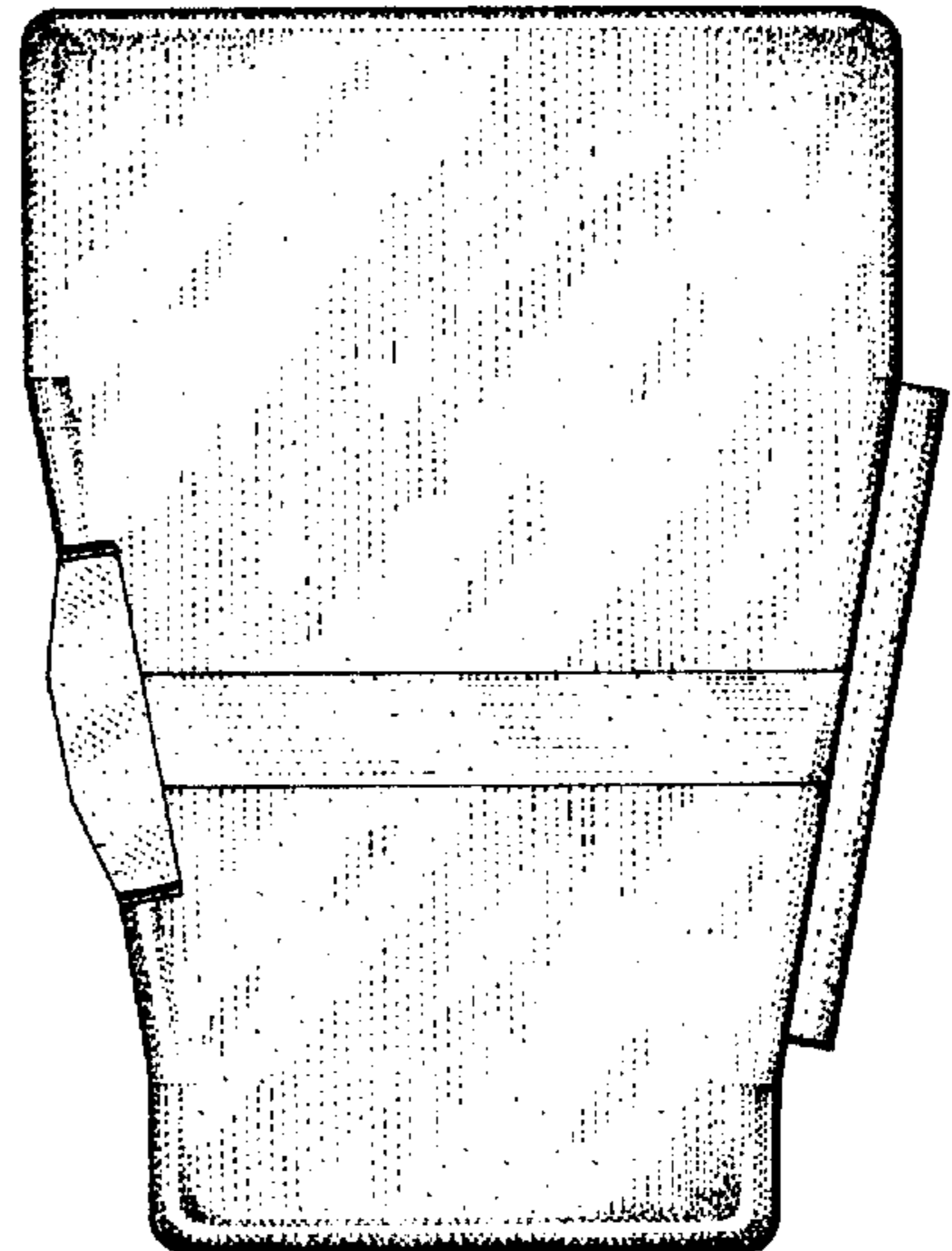


FIG. 5

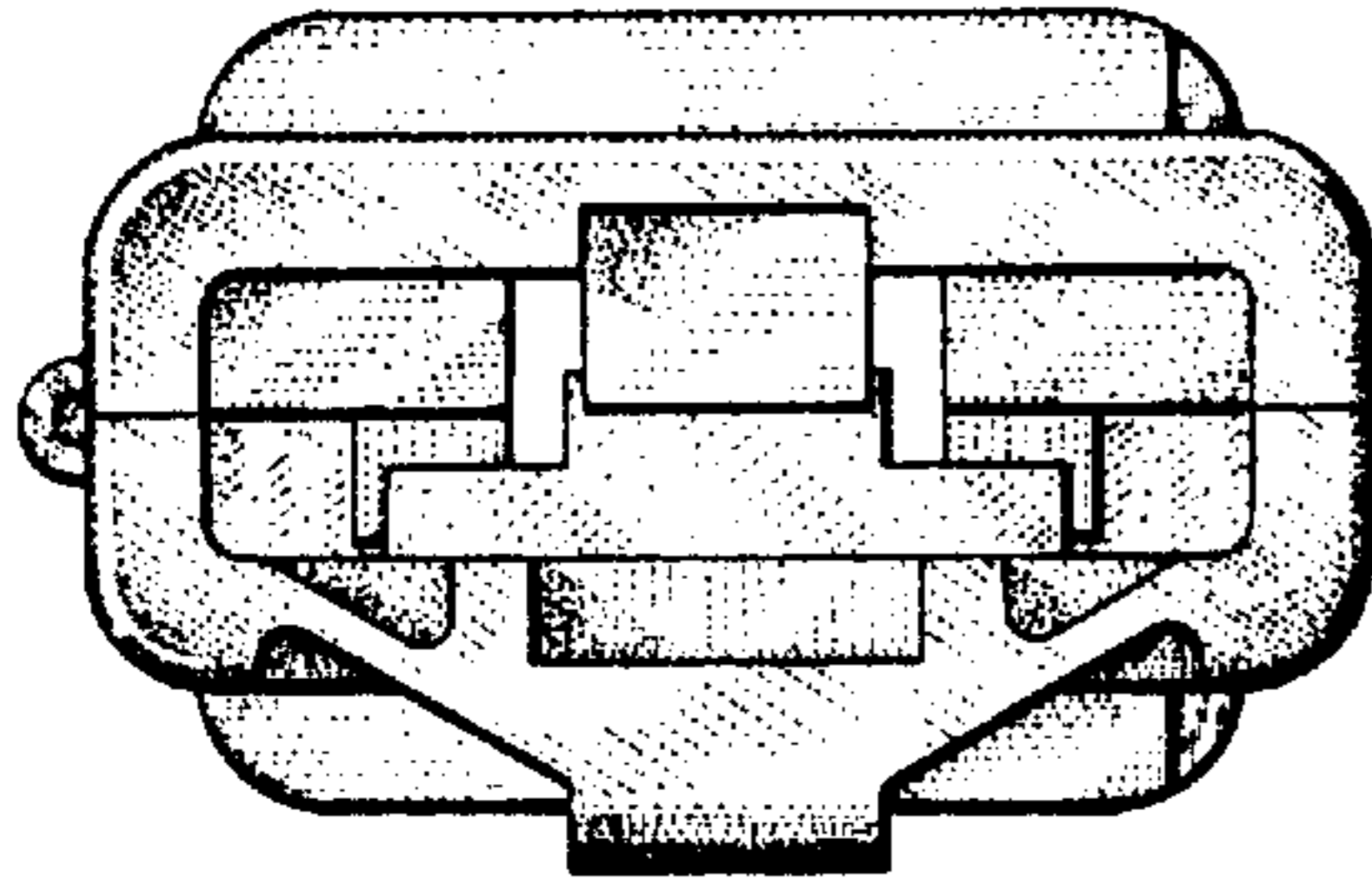


FIG. 6

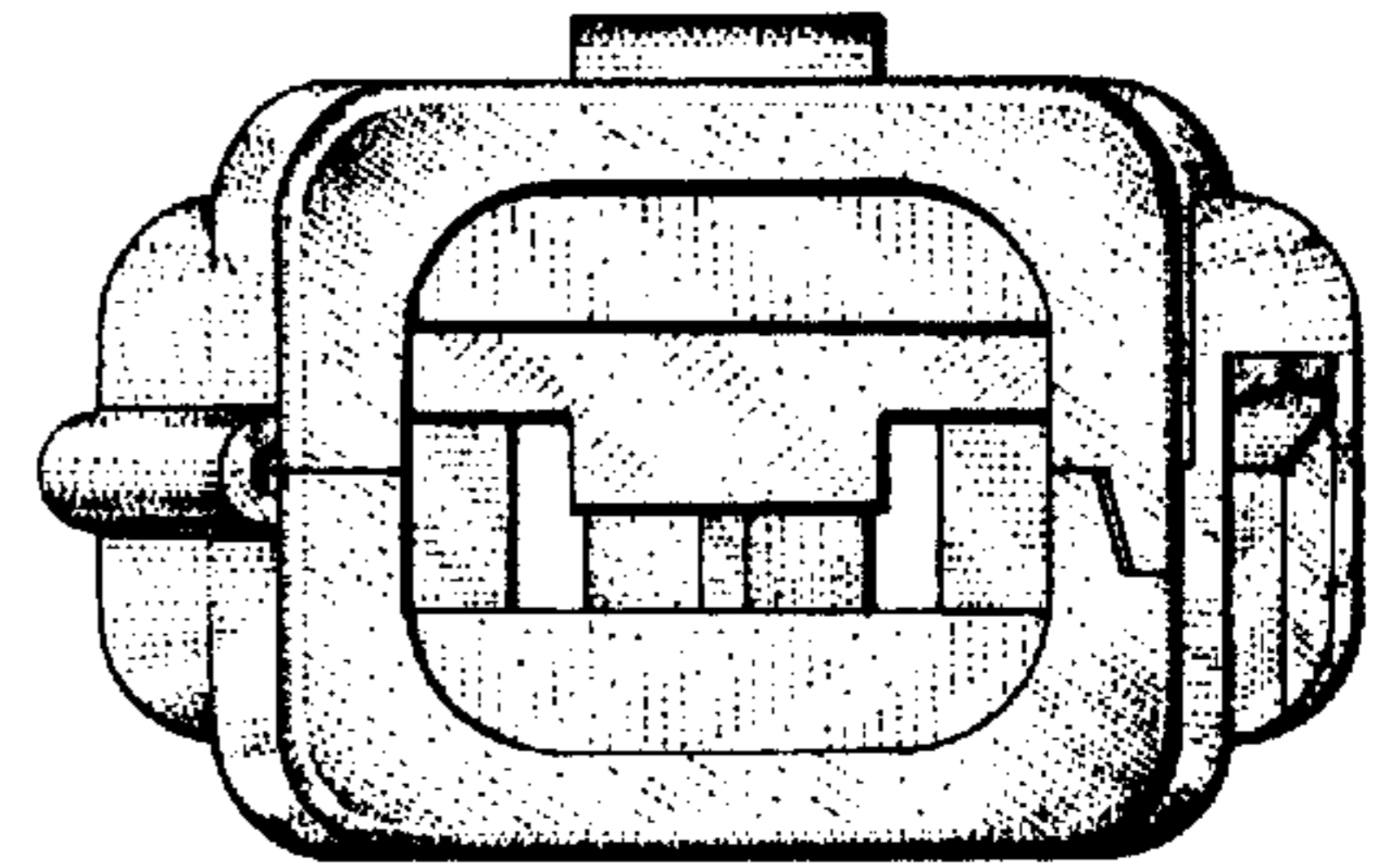


FIG. 7

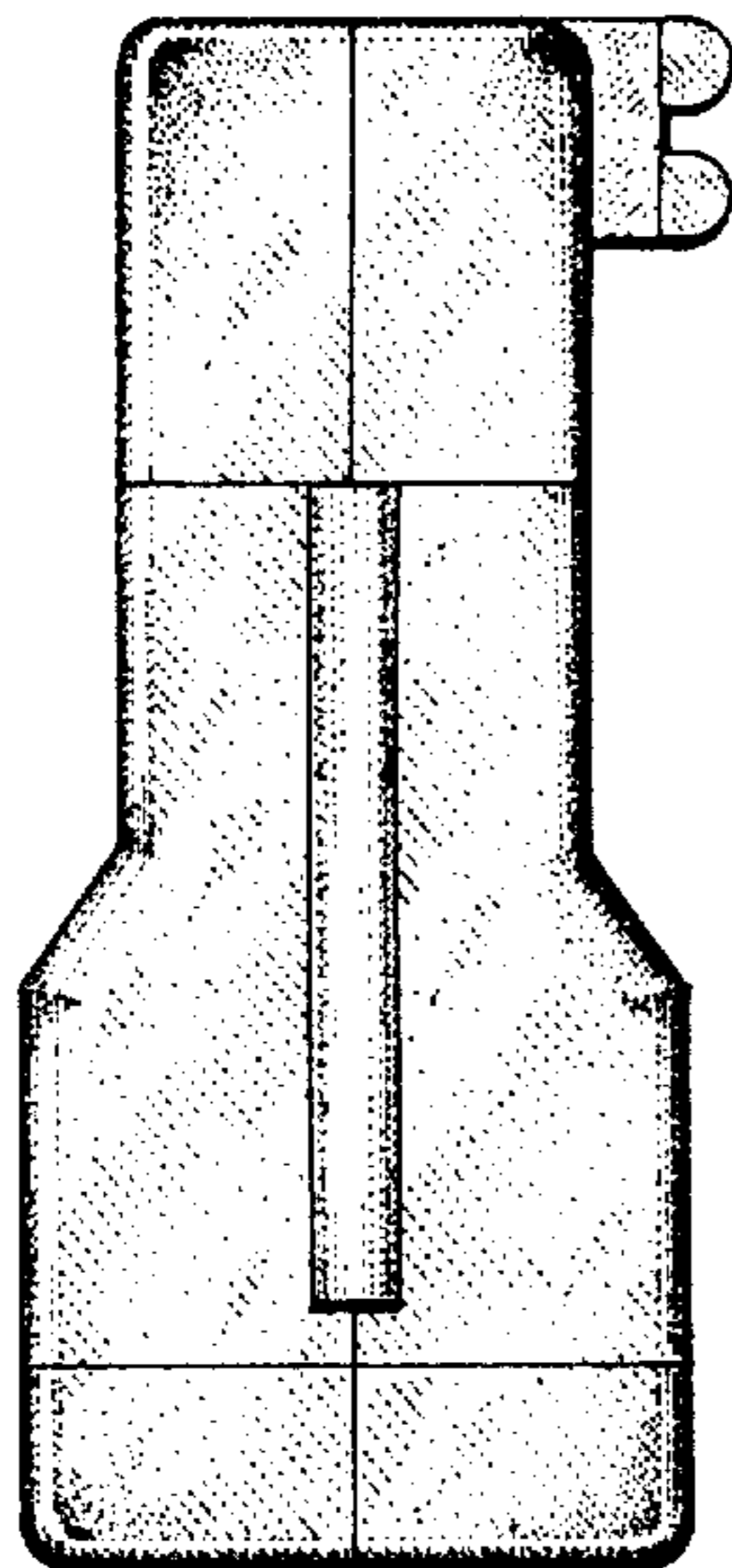


FIG. 8

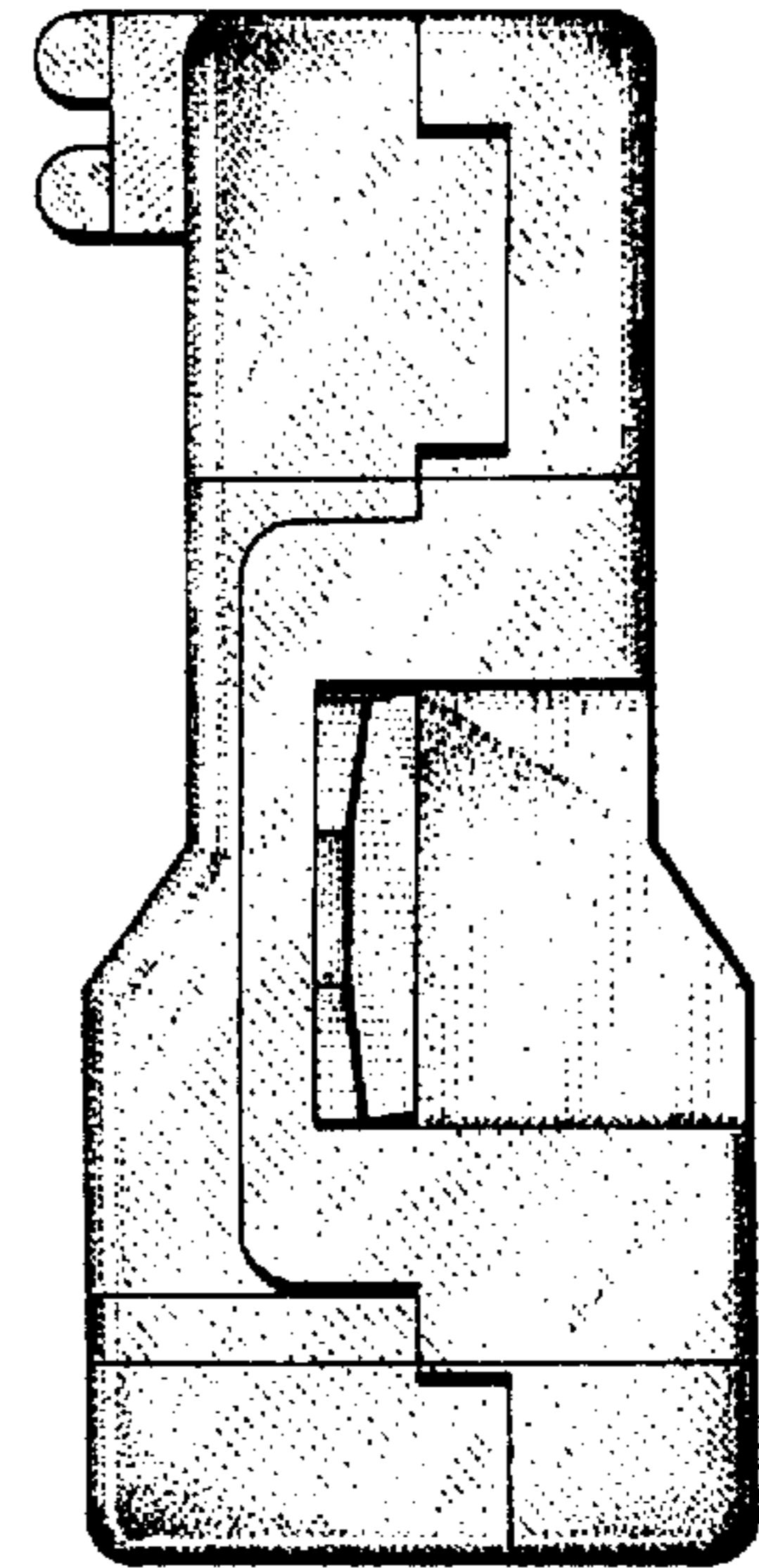


FIG. 9

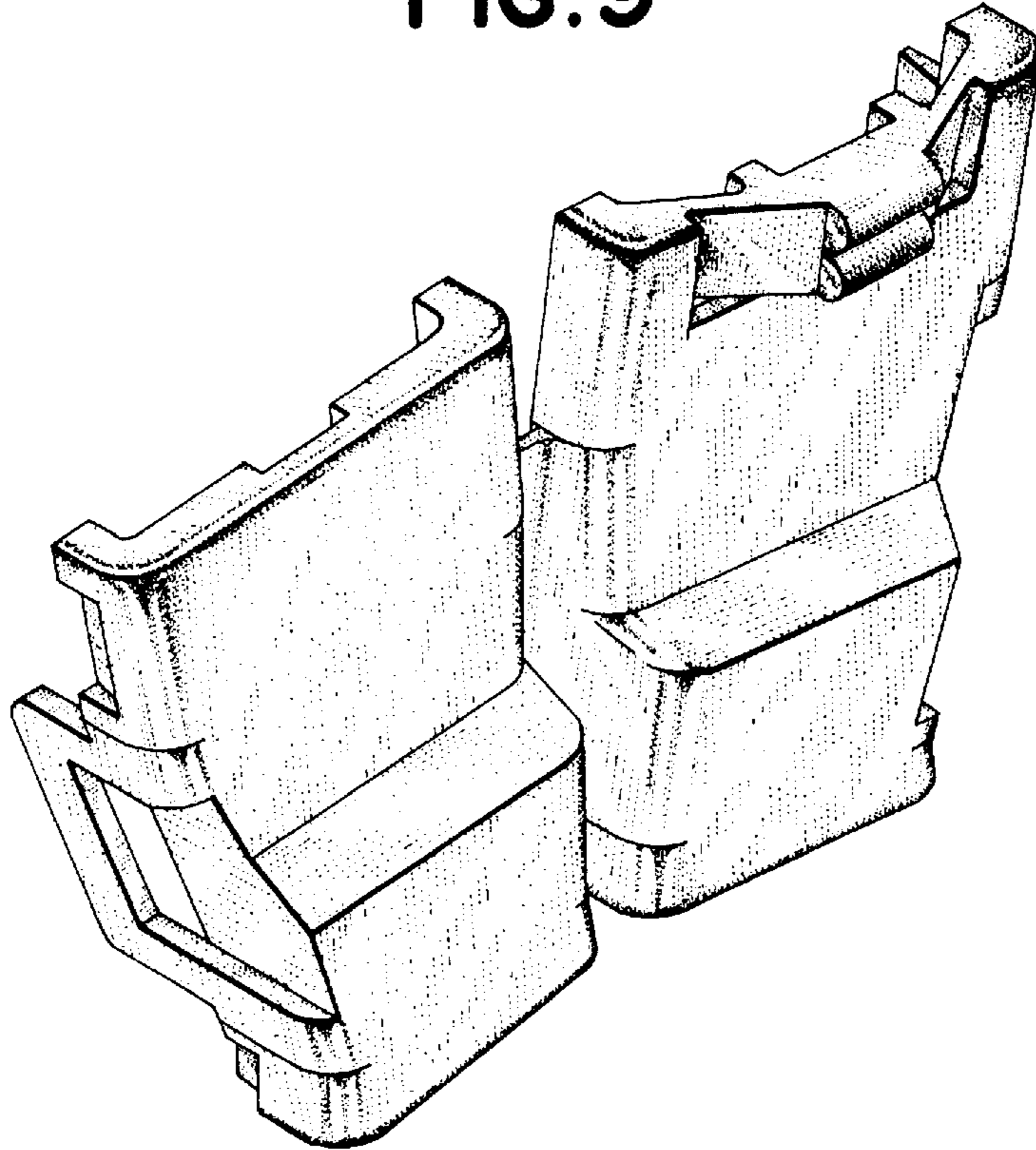


FIG. 10

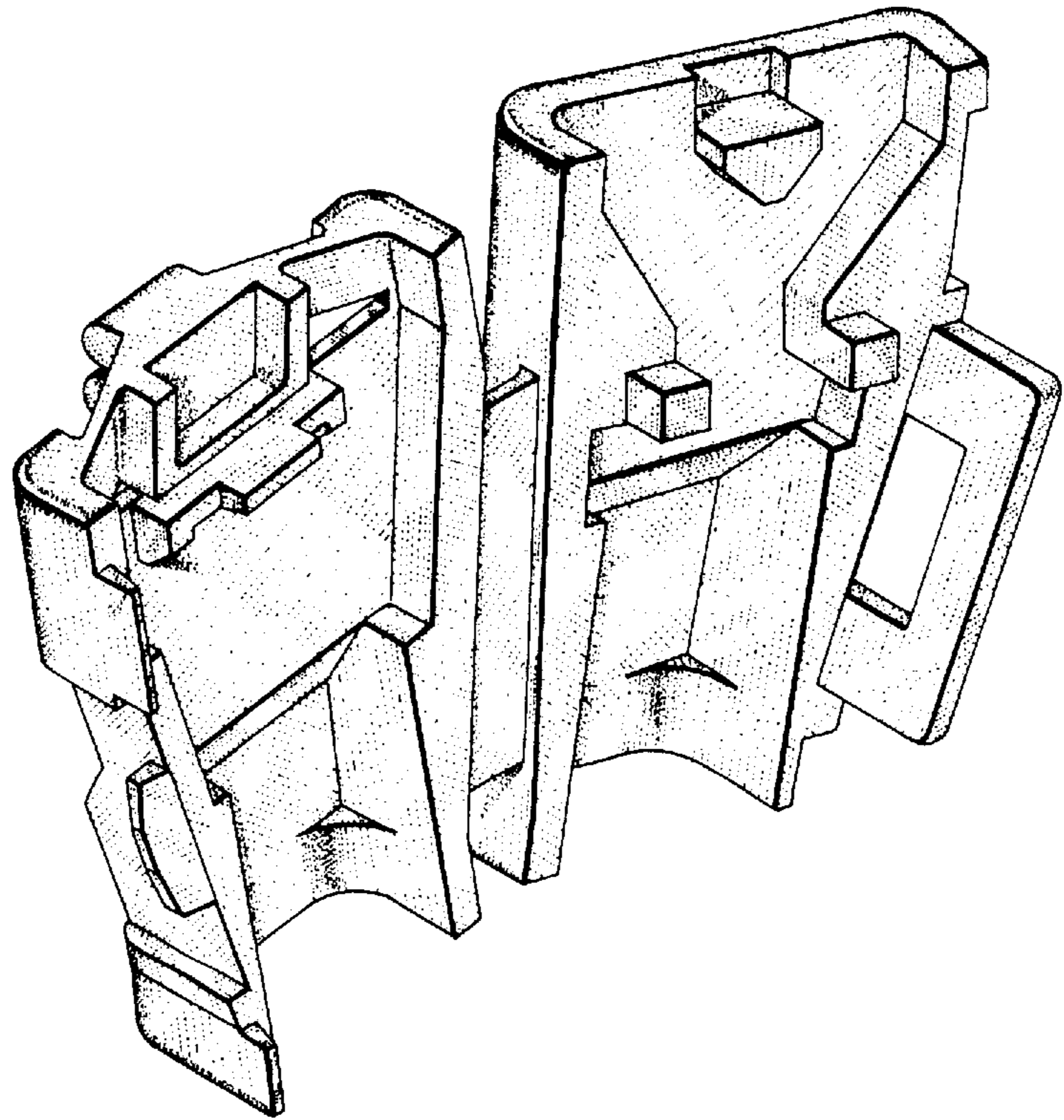


FIG. 11

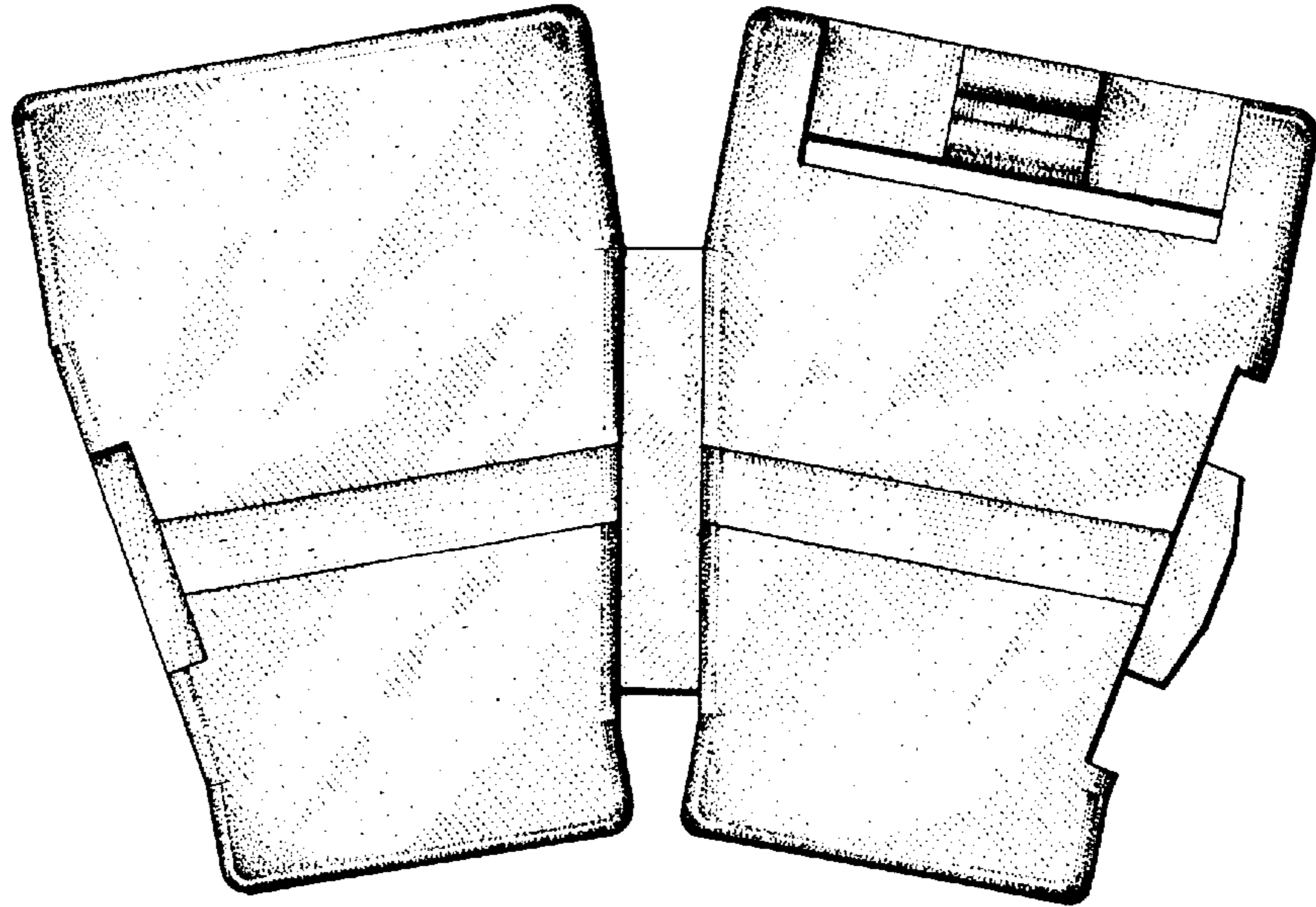


FIG. 12

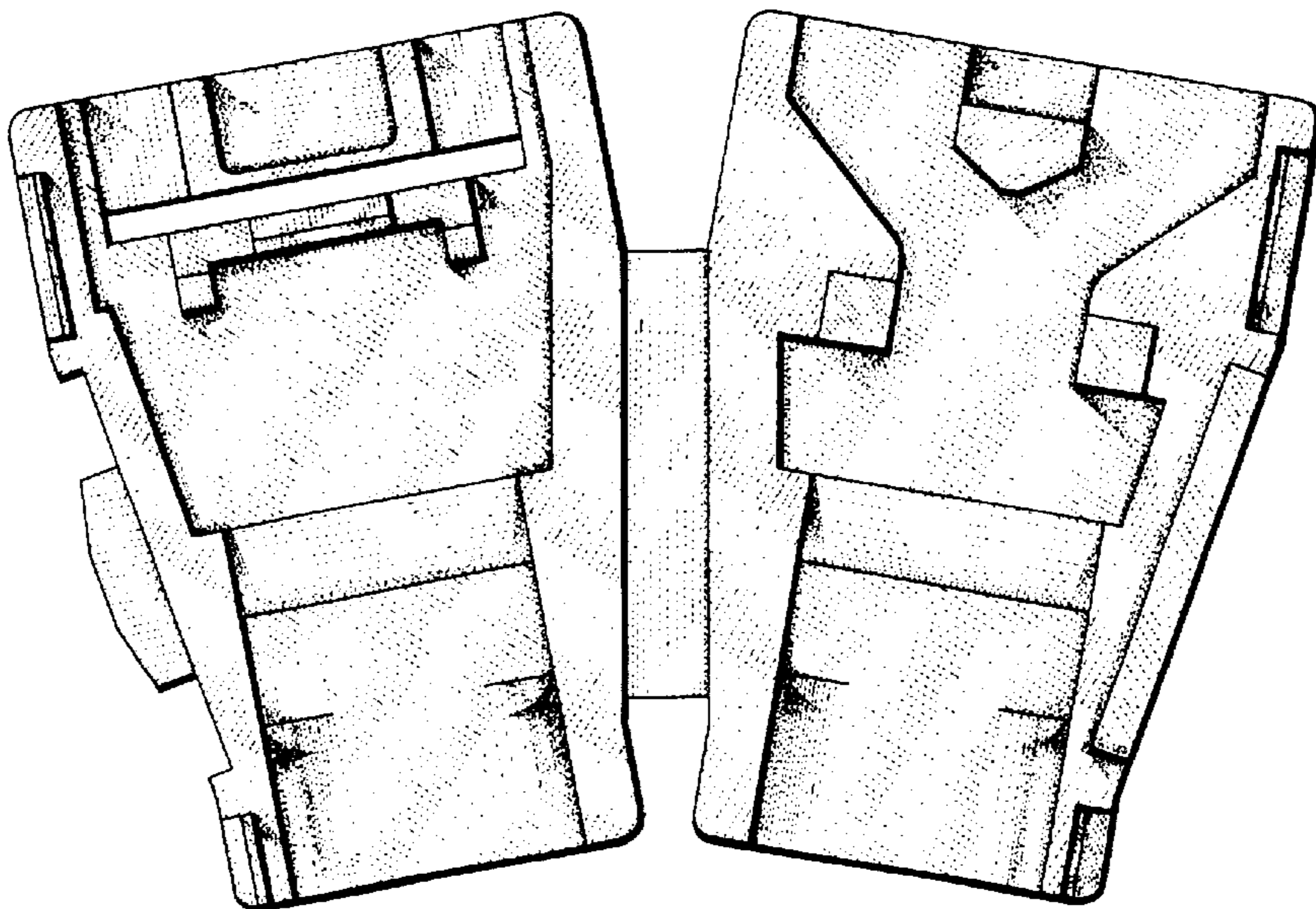


FIG.13

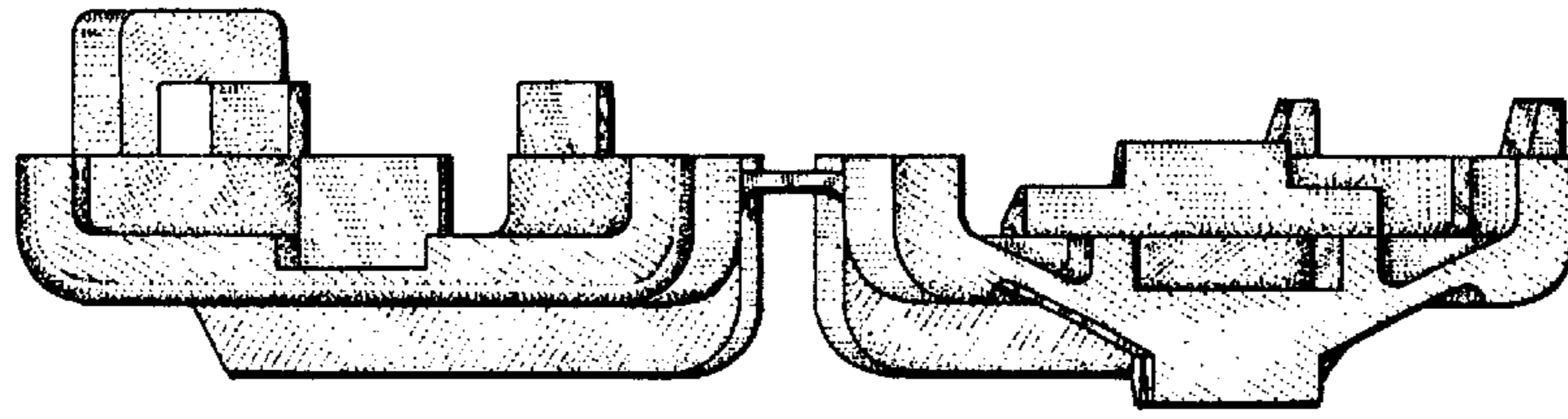


FIG.14

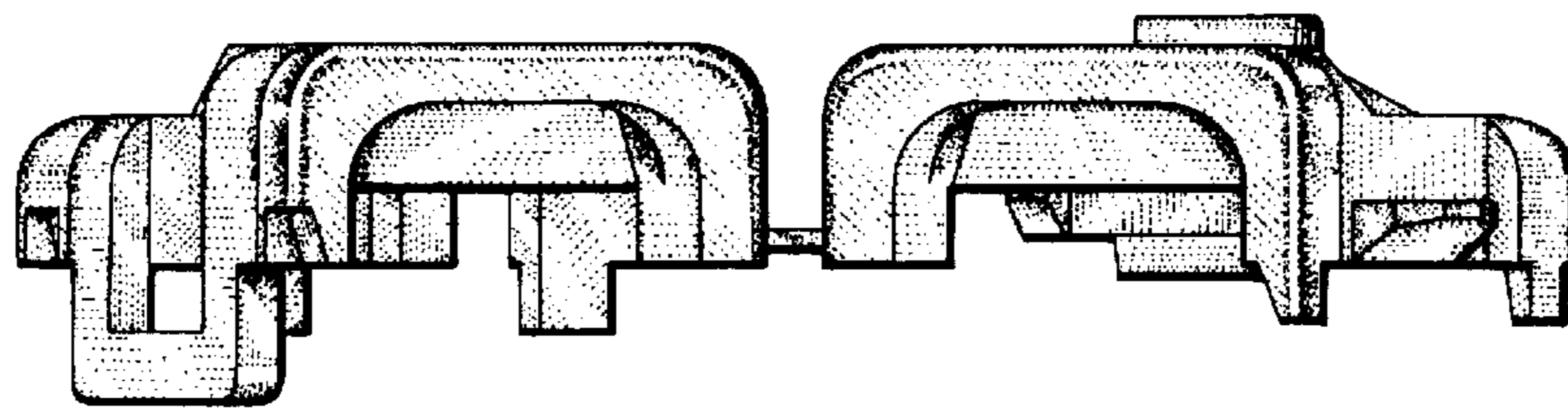


FIG.15

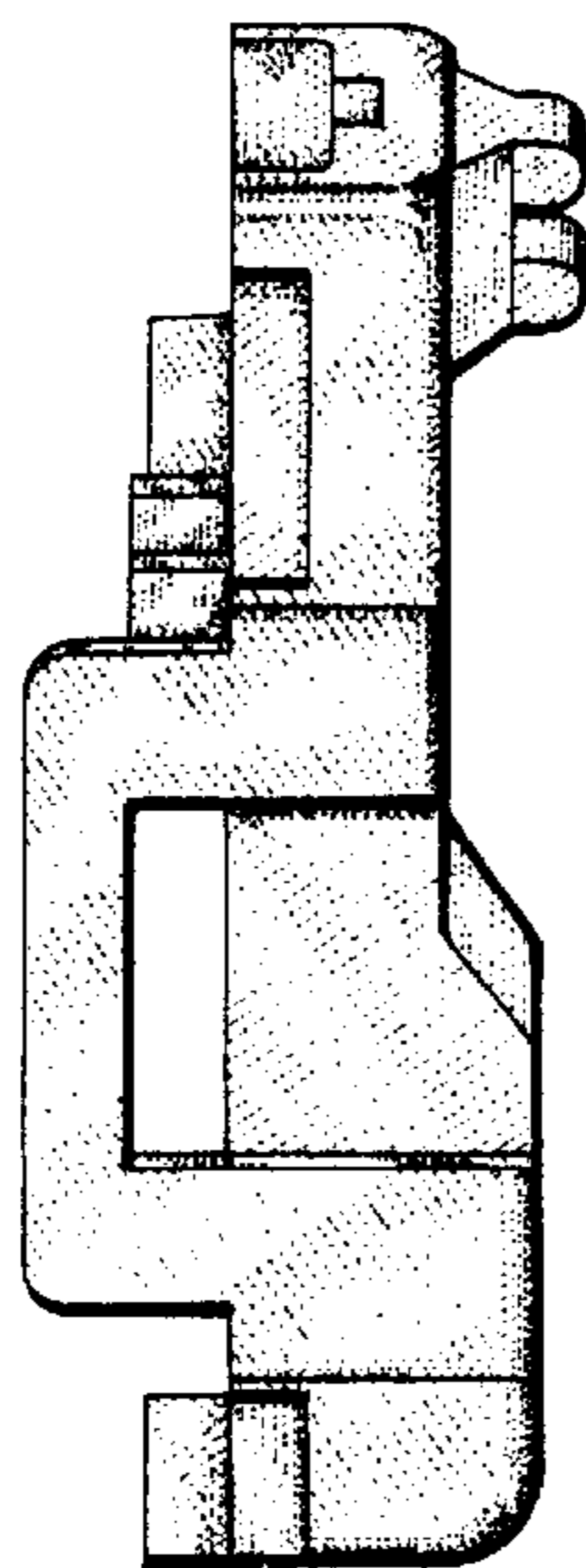


FIG.16

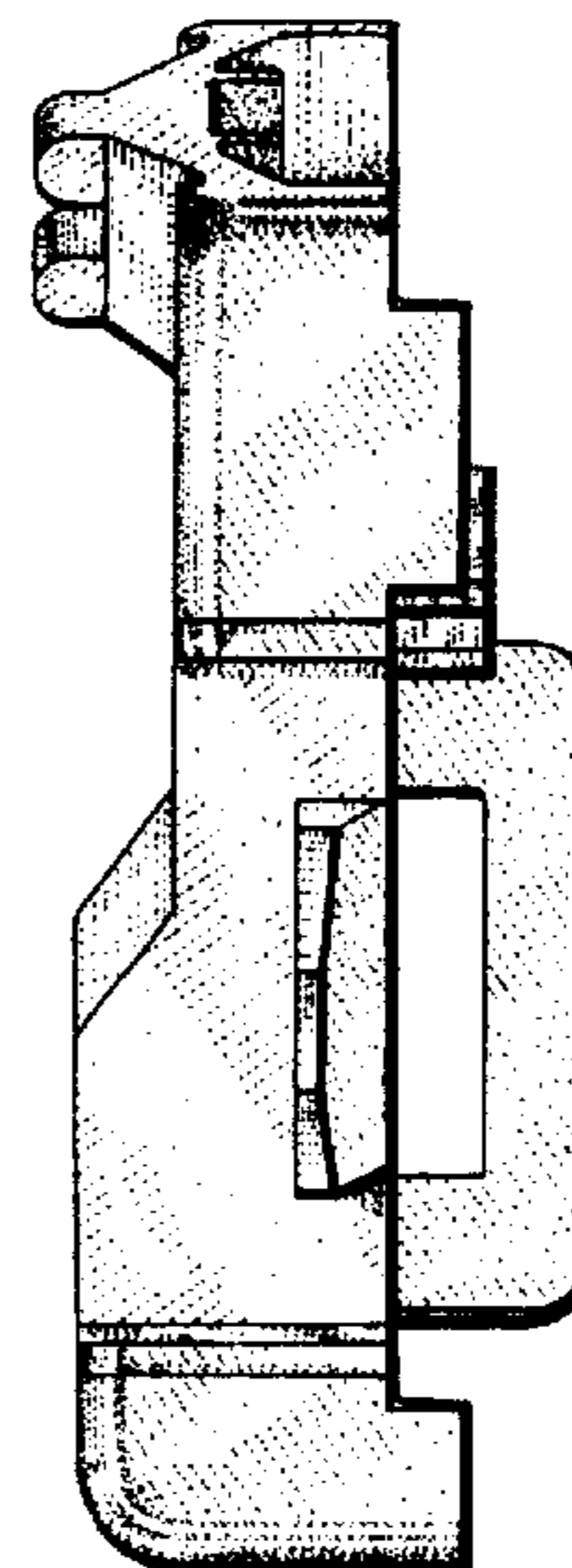
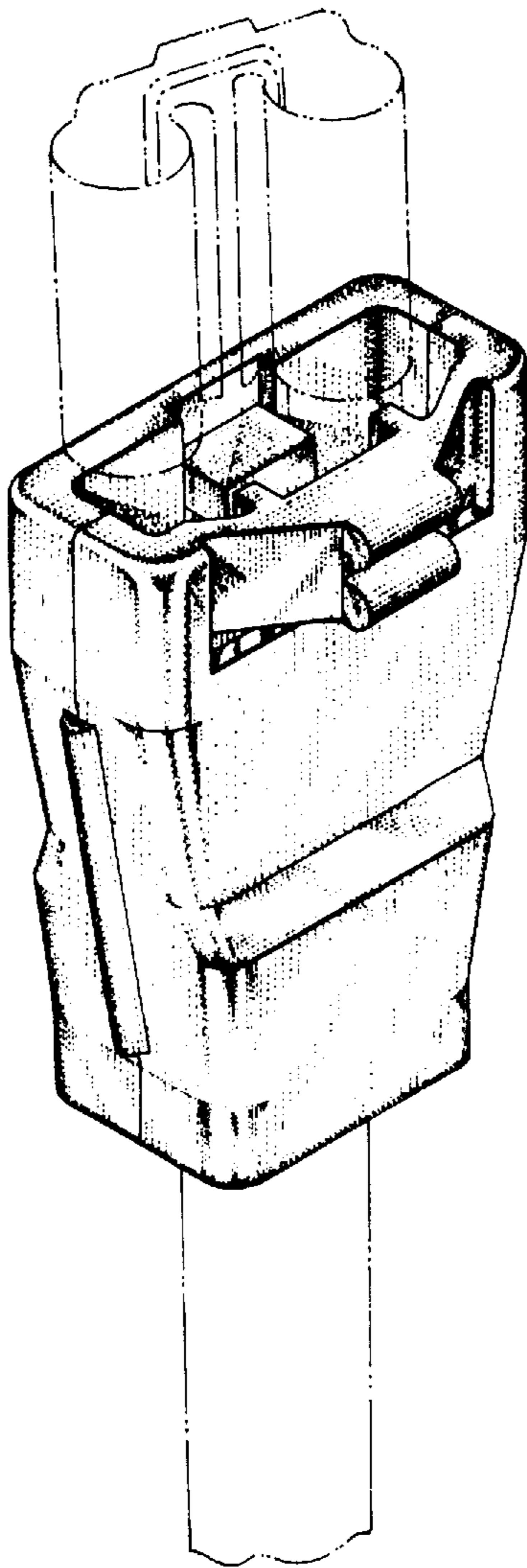


FIG.17



UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.: Des. 401,560
DATED: November 24, 1998
INVENTOR(S): Toshinobu NAKAMURA

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page,

[22] after "Filed: Jan. 30, 1998" please add the following:

-- Foreign Application Priority Data

Jun. 30, 1997 [JP] Japan Des 9-59539 --

Signed and Sealed this
Twenty-ninth Day of June, 1999

Attest:



Q. TODD DICKINSON

Attesting Officer

Acting Commissioner of Patents and Trademarks