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# United States Patent [19] Klinker

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[54] **PROCESSOR CARD ASSEMBLY**

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[73] Assignee: **Intel Corporation**, Santa Clara, Calif.

[\*\*] Term: **14 Years**

[21] Appl. No.: **61,739**

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[51] **LOC (6) Cl.** ..... **14-02**

[52] **U.S. Cl.** ..... **D14/117; D14/114**

[58] **Field of Search** ..... D14/100, 114, D14/117, 121; D13/147, 182; 235/380-382, 382.5, 379, 486, 487, 492, 441; 359/152; 396/211; 439/607, 946.2; D18/12; 395/310, 393, 376, 325, 568, 652, 670, 675, 672, 800; 361/816, 737, 753

D. 344,725	3/1994	Barr et al. .	
D. 355,414	2/1995	Inoue et al. .	
D. 362,846	10/1995	Inoue et al. .	
D. 365,556	12/1995	Inoue et al. .	
D. 369,352	4/1996	Iwakami .	
D. 371,353	7/1996	Ashida et al. .	
D. 374,442	10/1996	Ozaki .	
D. 374,665	10/1996	Miyaki .	
D. 376,795	12/1996	Ashida .	
D. 377,488	1/1997	Ashida .	
D. 379,977	6/1997	Ueda .	
5,184,169	2/1993	Nishitani .....	396/211
5,547,401	8/1996	Aldous et al. ....	439/946.2 X
5,563,400	10/1996	Le Roux .....	439/946.2 X

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### [57] CLAIM

The ornamental design for a processor card assembly, as shown and described.

### DESCRIPTION

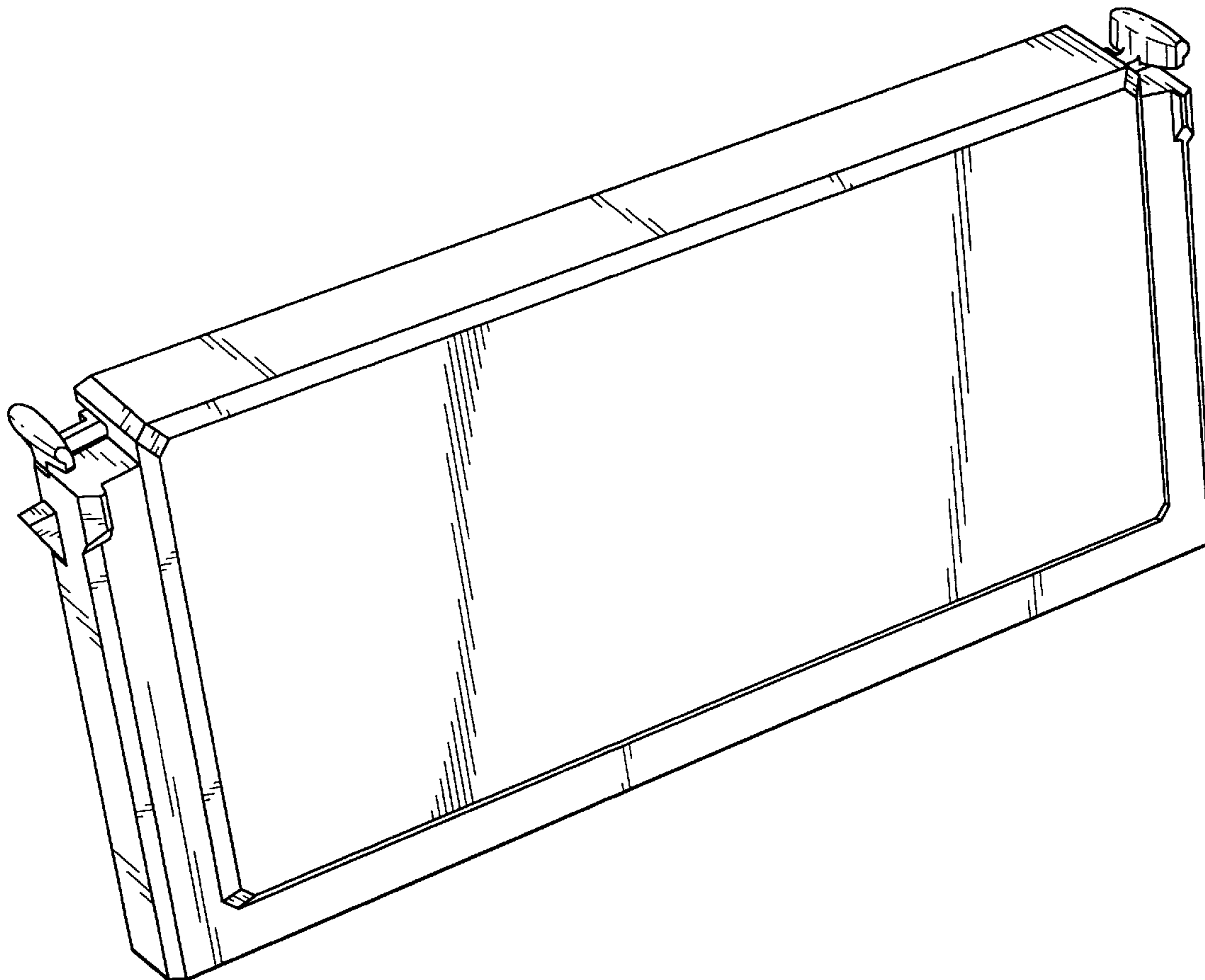
FIG. 1 is a perspective view of a processor card assembly; FIGS. 2-3 are the front and left side views respectively thereof; and, FIG. 4-7 are the back, right side, top, and bottom views respectively thereof.

**1 Claim, 3 Drawing Sheets**

### [56] References Cited

#### U.S. PATENT DOCUMENTS

D. 305,886	2/1990	Banjo et al. ....	D14/117
D. 313,229	12/1990	Audebert et al. ....	D14/117
D. 320,203	9/1991	Ashida .	
D. 321,429	11/1991	Kojo .	
D. 330,022	10/1992	Chen .....	D14/117
D. 343,833	2/1994	Barr et al. .	
D. 344,504	2/1994	Barr et al. .	



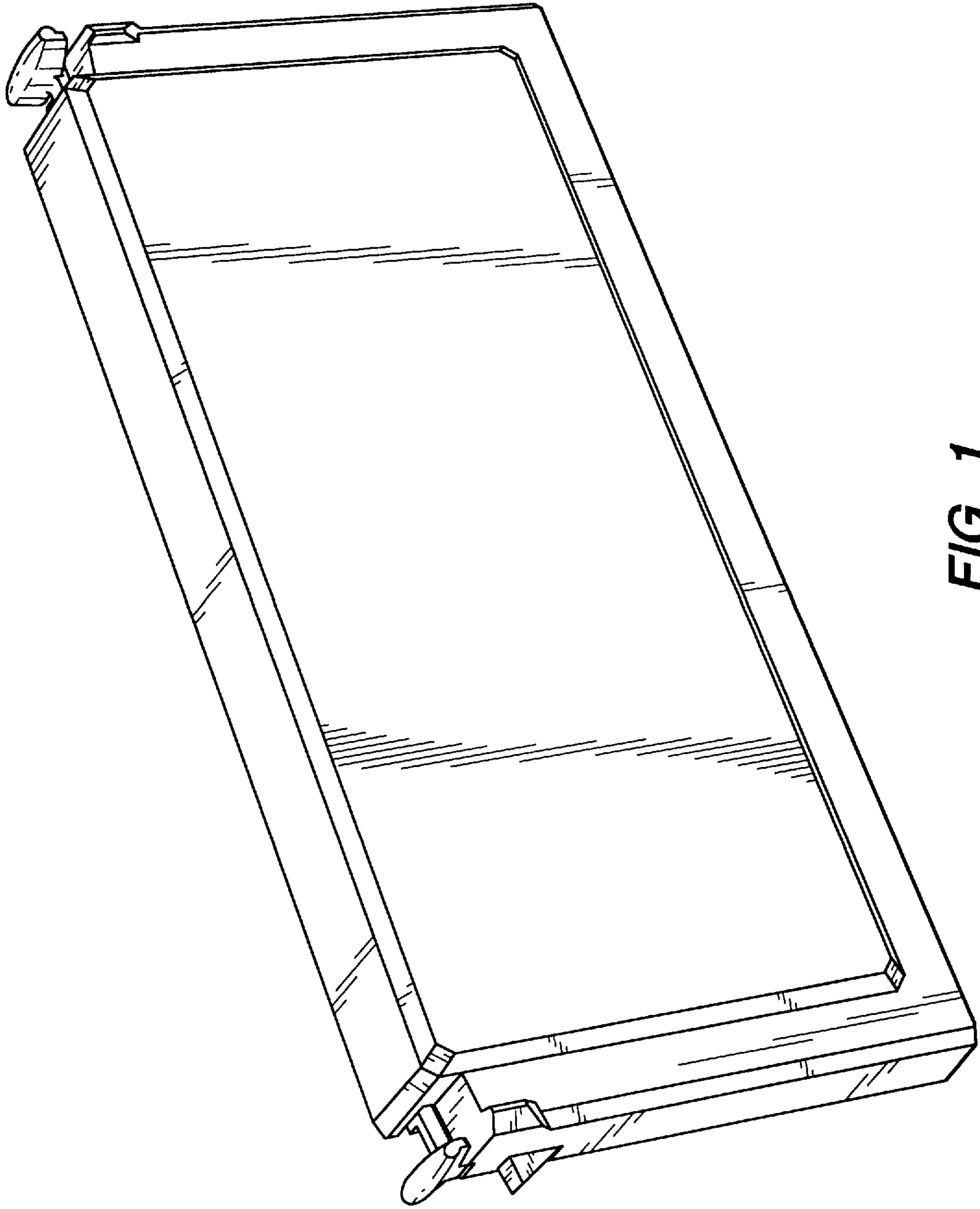
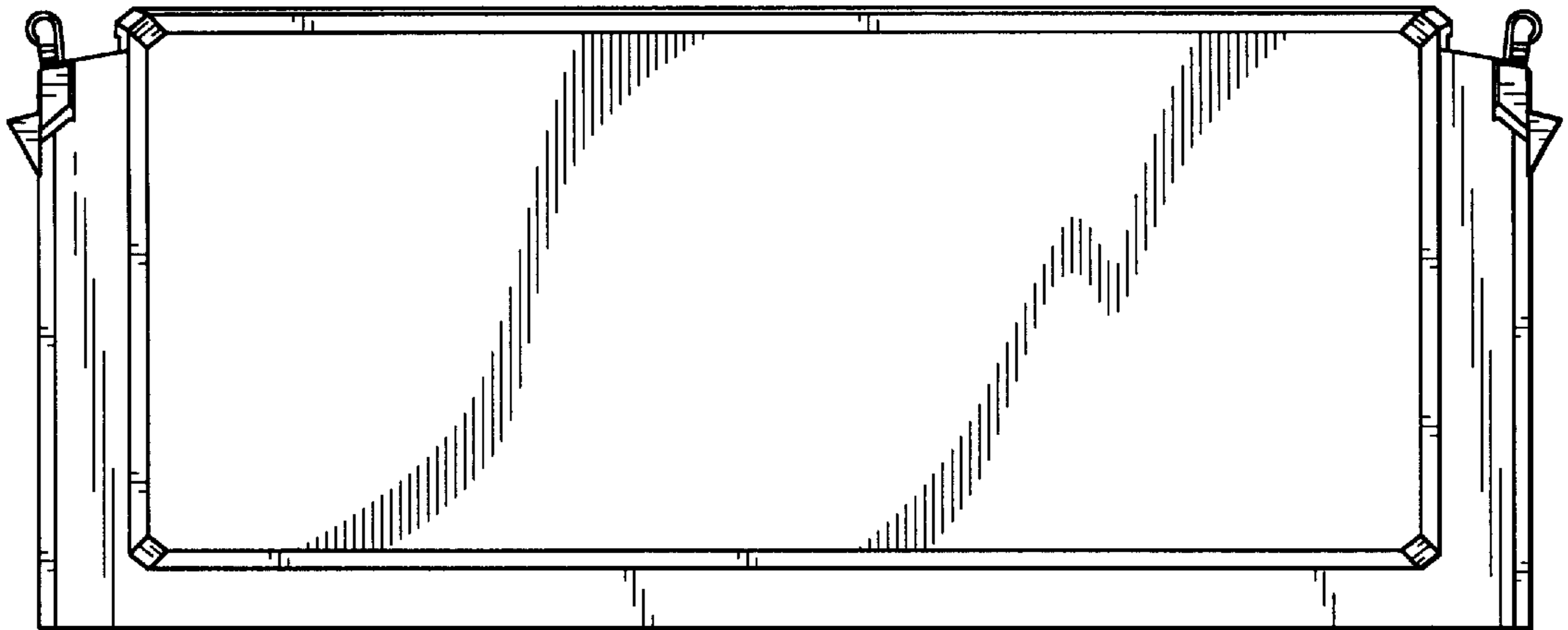
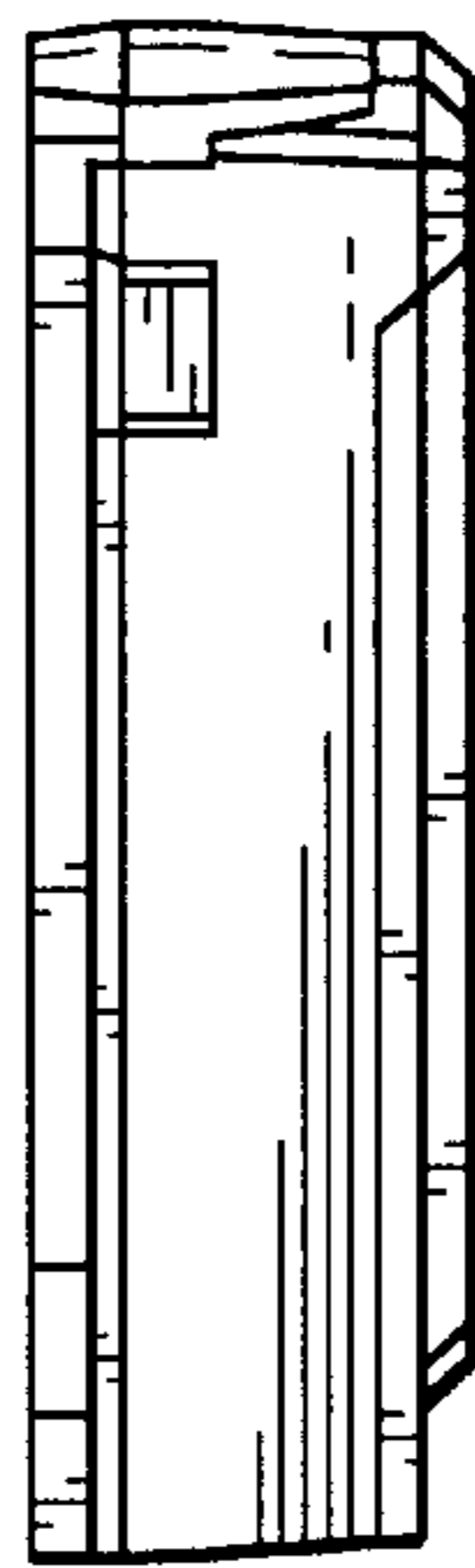


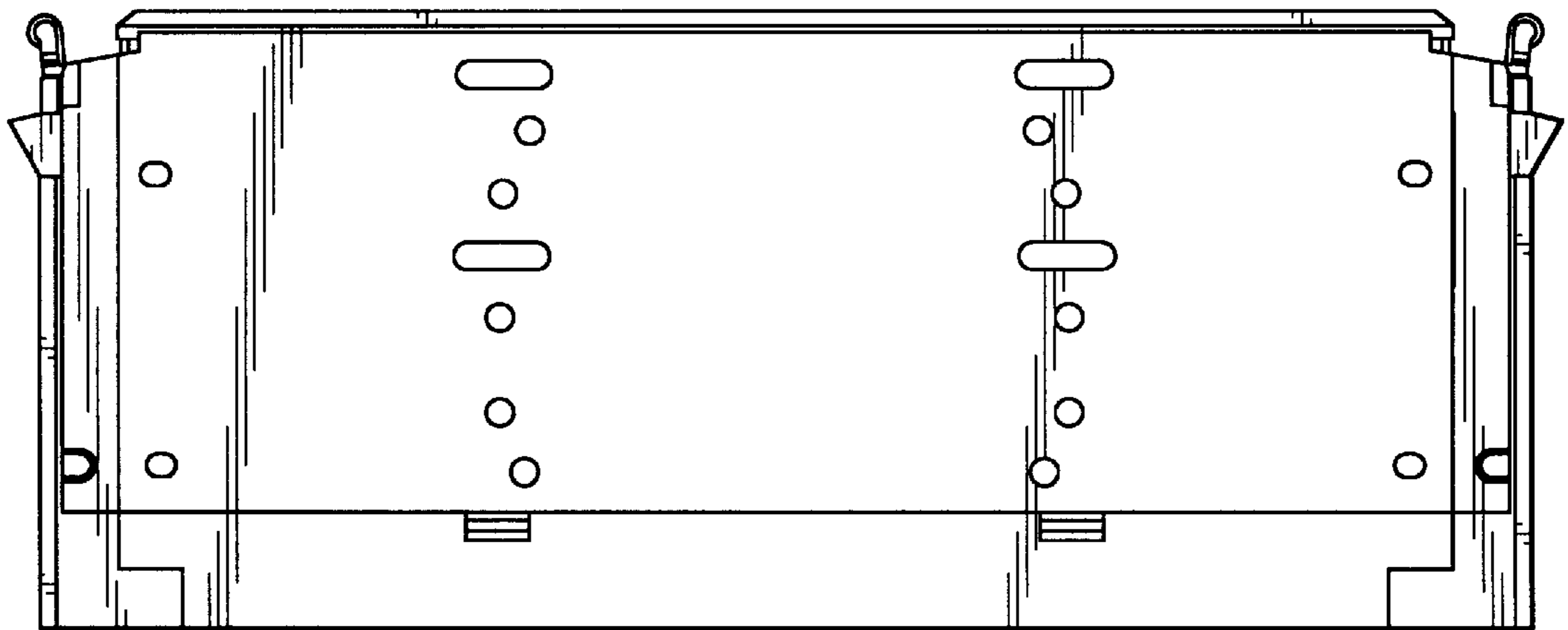
FIG. 1



**FIG. 2**



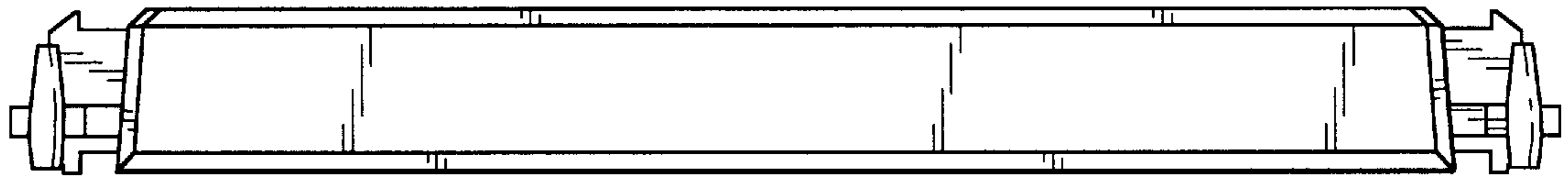
**FIG. 3**



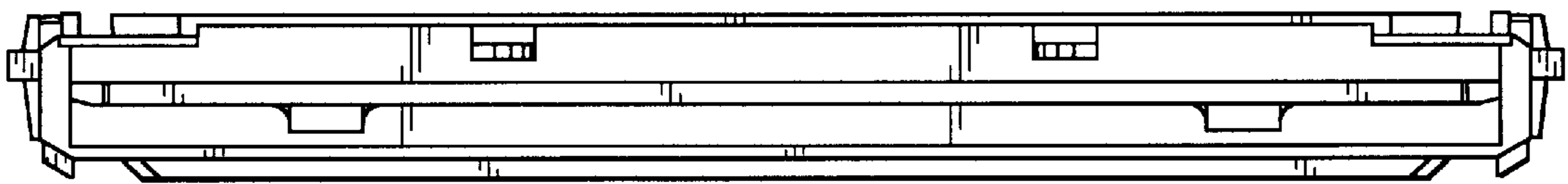
**FIG. 4**



**FIG. 5**



**FIG. 6**



**FIG. 7**