



US00D397673S

**United States Patent** [19]  
**Heberling et al.**

[11] **Patent Number:** **Des. 397,673**  
[45] **Date of Patent:** **\*\*Sep. 1, 1998**

[54] **MULTIPLE POSITION ENCLOSURE FOR ELECTRONICS DEVICE**

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*Attorney, Agent, or Firm—Jones & Askew*

[75] **Inventors:** **James R. Heberling**, Lawrenceville; **J. Stephen Wells**, Buford, both of Ga.

[57] **CLAIM**

[73] **Assignee:** **Hayes Microcomputer Products, Inc.**, Norcross, Ga.

The ornamental design for a multiple position enclosure for electronics device, as shown.

[\*\*] **Term:** **14 Years**

**DESCRIPTION**

[21] **Appl. No.:** **70,383**

FIG. 1 is a perspective view of a multiple position enclosure for electronics device embodying our new design;

[22] **Filed:** **Apr. 29, 1997**

FIG. 2 is a front elevational view thereof;

[51] **LOC (6) Cl.** ..... **14-02**

FIG. 3 is a rear elevational view thereof;

[52] **U.S. Cl.** ..... **D14/102; D14/107**

FIG. 4 is a left side elevational view thereof;

[58] **Field of Search** ..... **D14/100, 102, D14/107-109; D13/162, 184, 199; D6/432, 436, 445, 448; 312/208.1; 360/99.01, 99.12; 361/694, 696, 683**

FIG. 5 is a right side elevational view thereof;

FIG. 6 is a top plan view thereof; and,

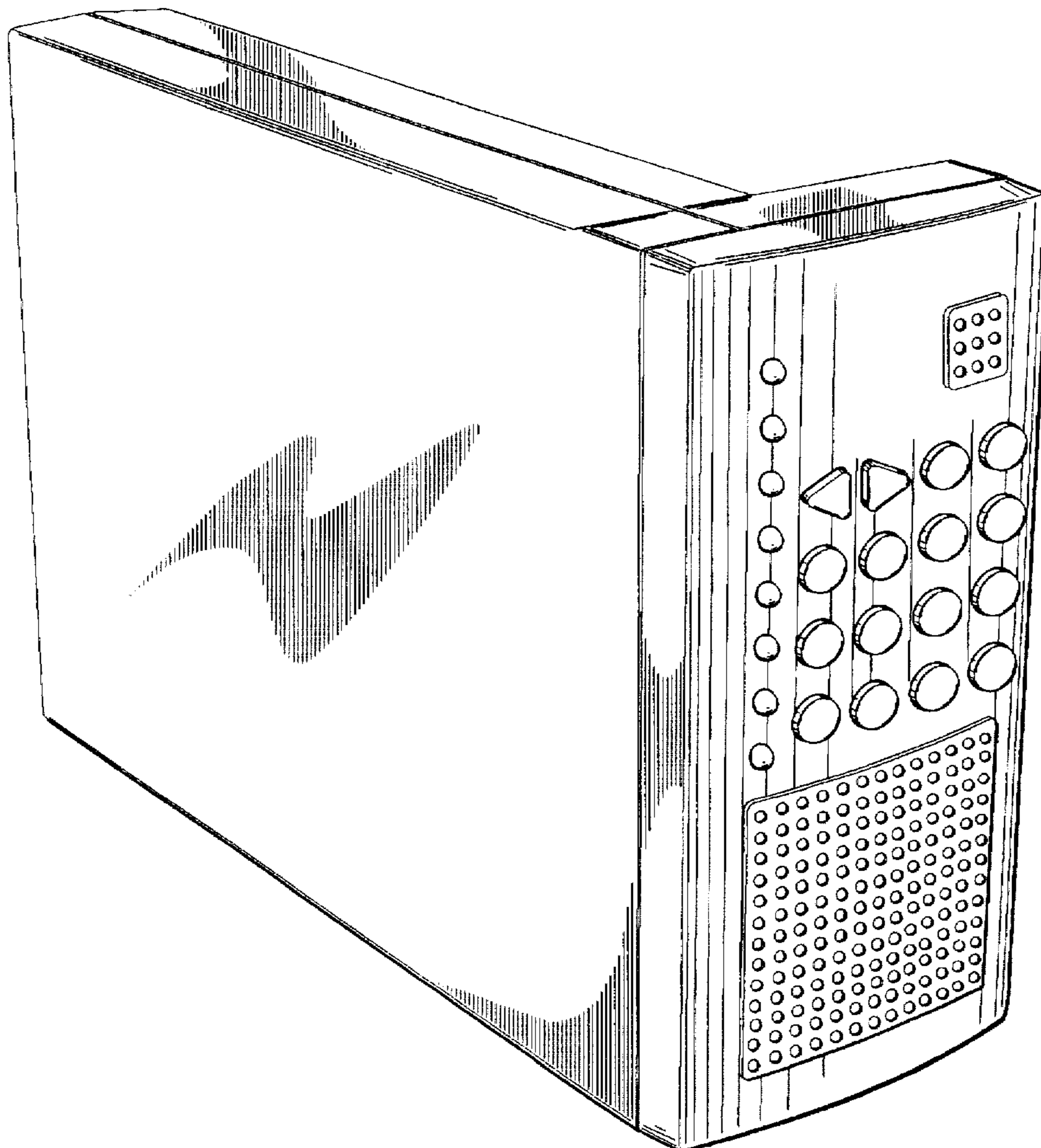
FIG. 7 is a bottom plan view thereof.

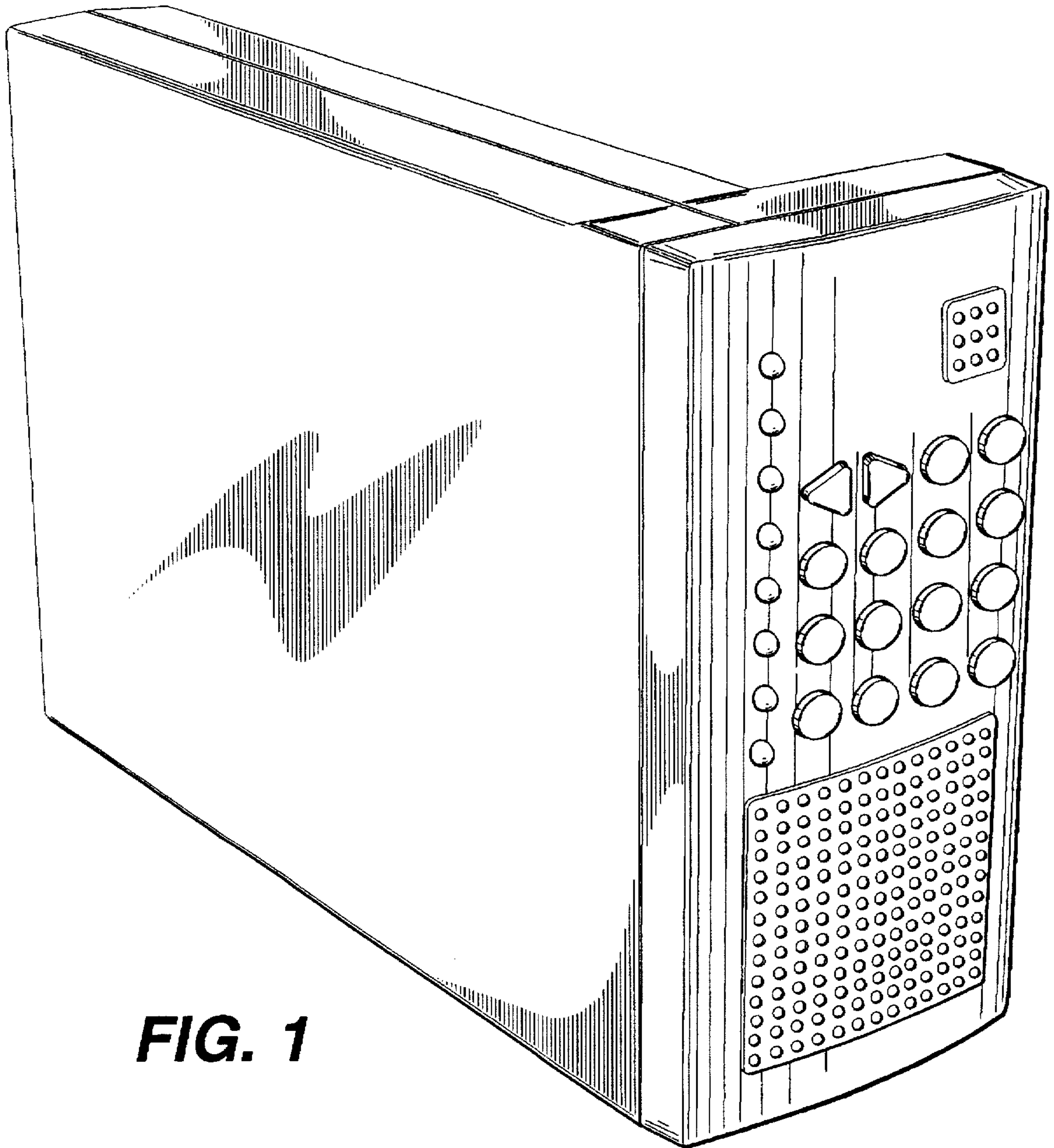
[56] **References Cited**

**U.S. PATENT DOCUMENTS**

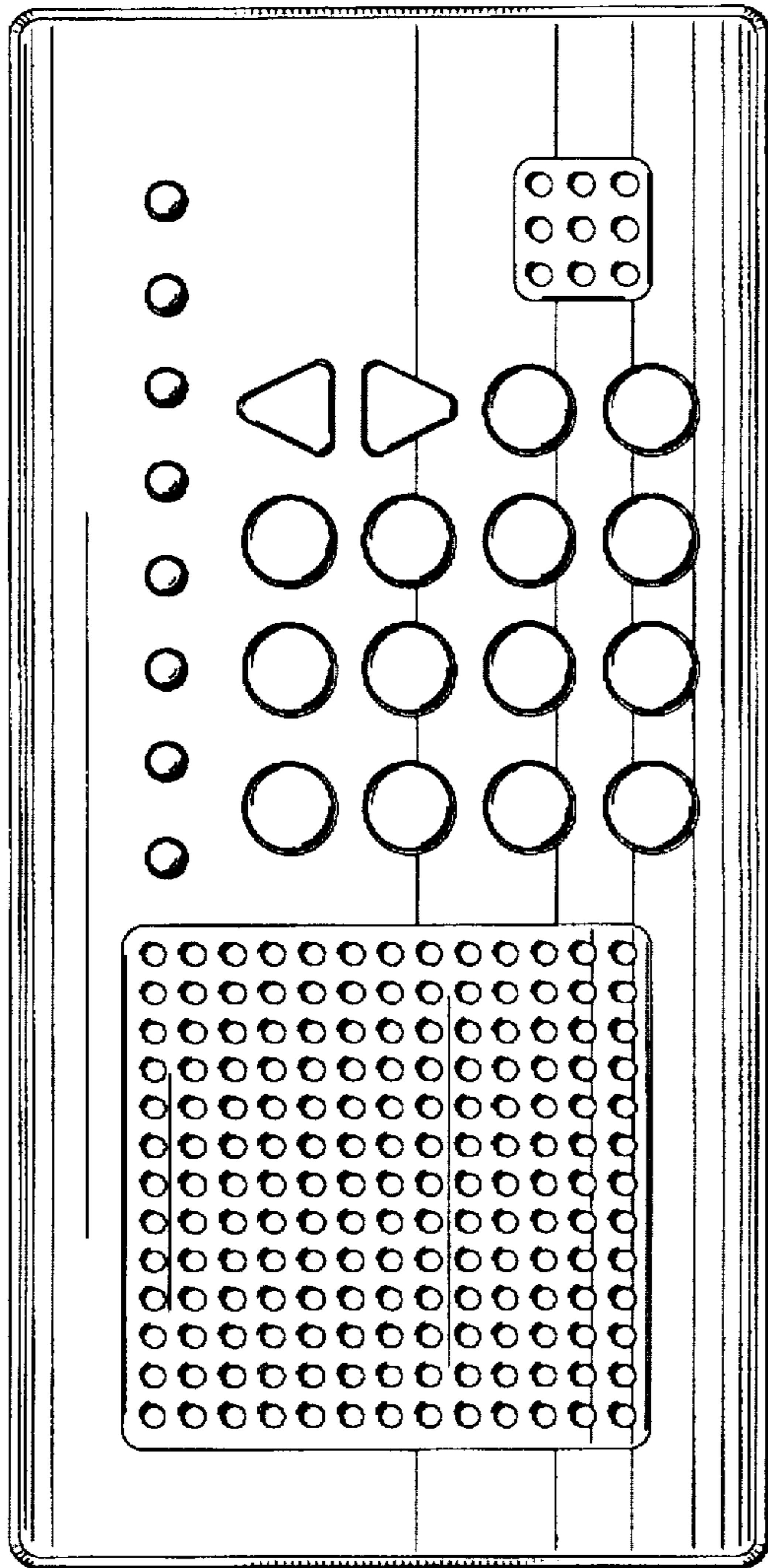
D. 370,211 5/1996 Lee ..... D14/102

**1 Claim, 5 Drawing Sheets**

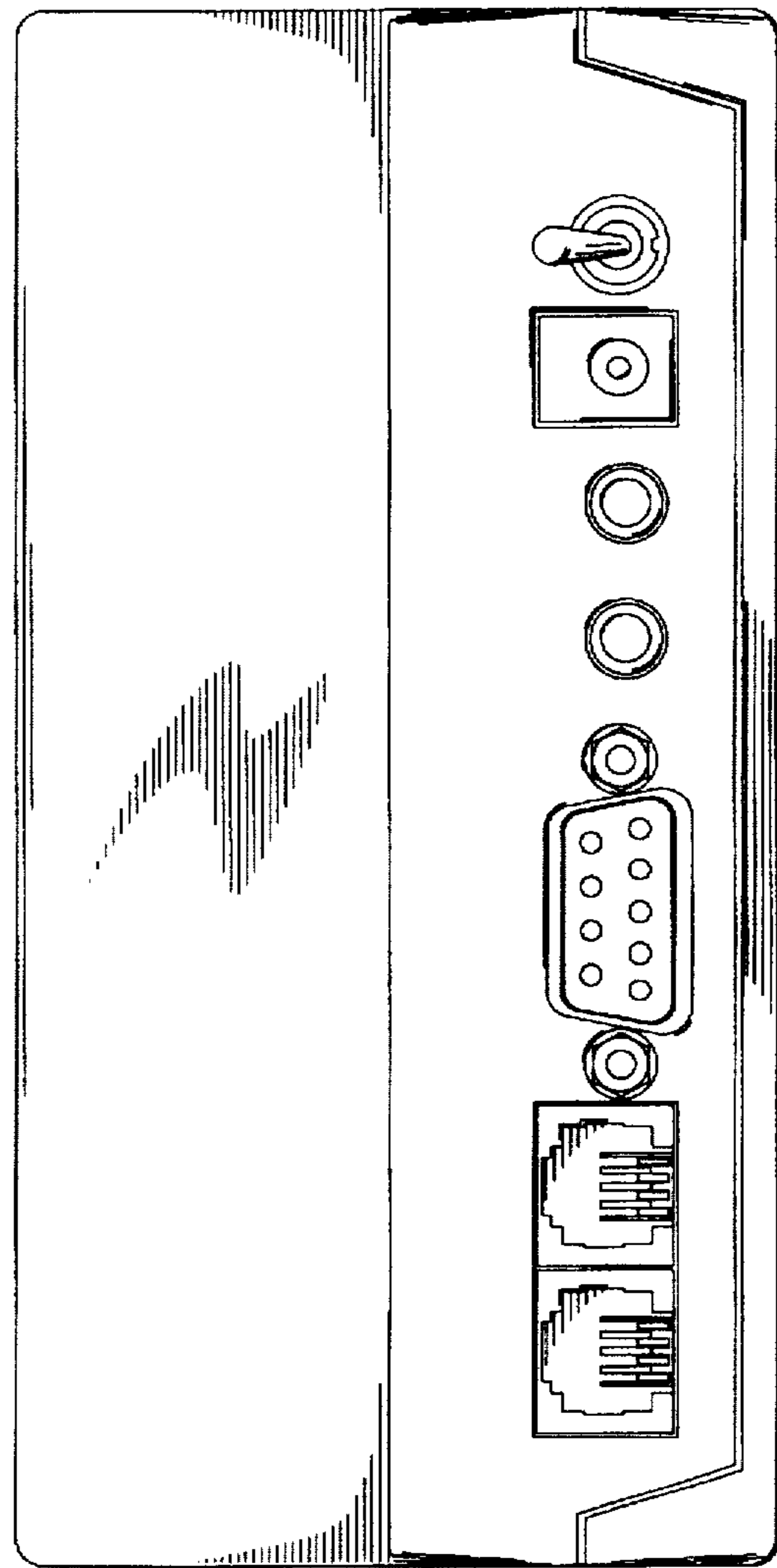




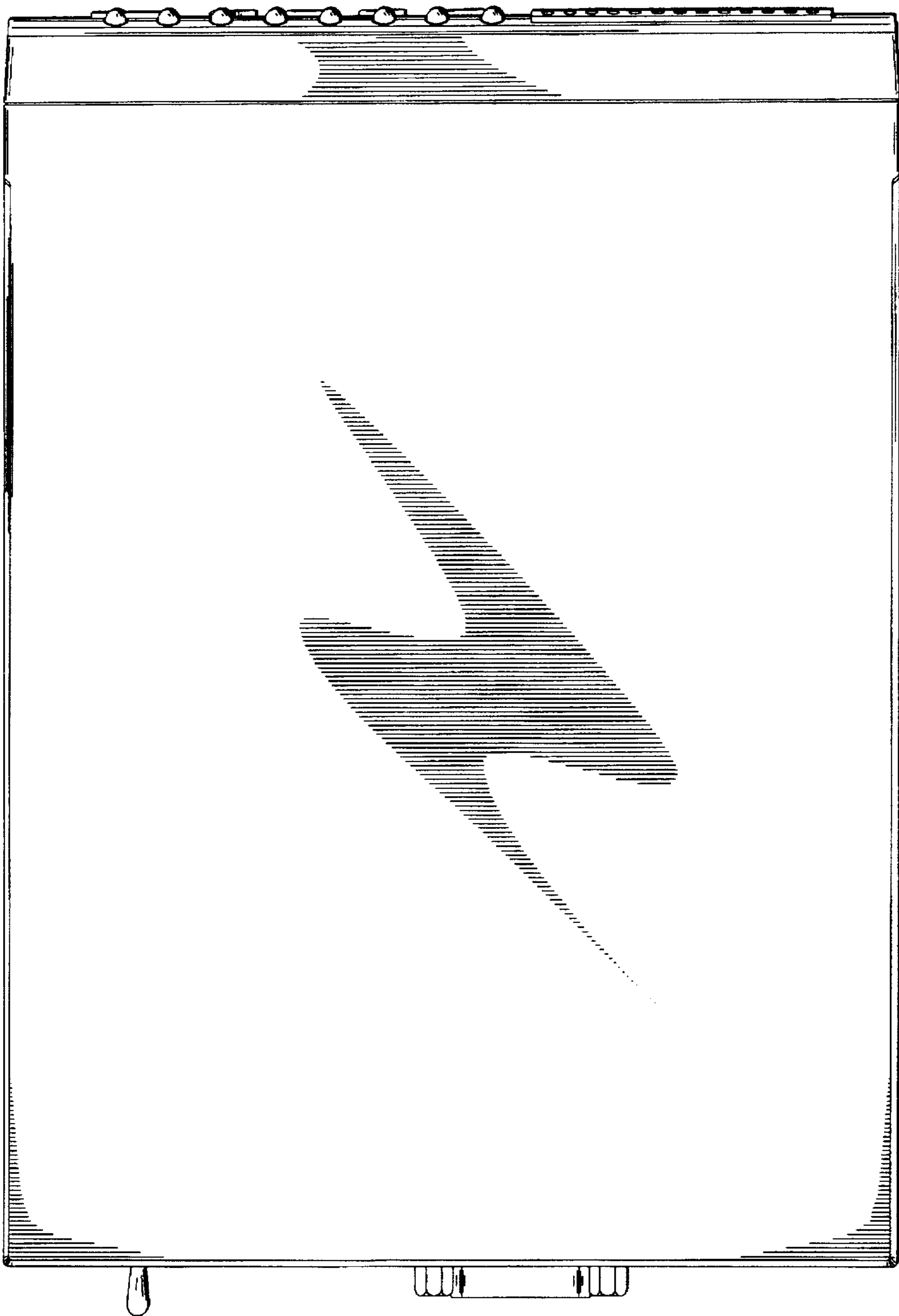
**FIG. 1**



**FIG. 2**



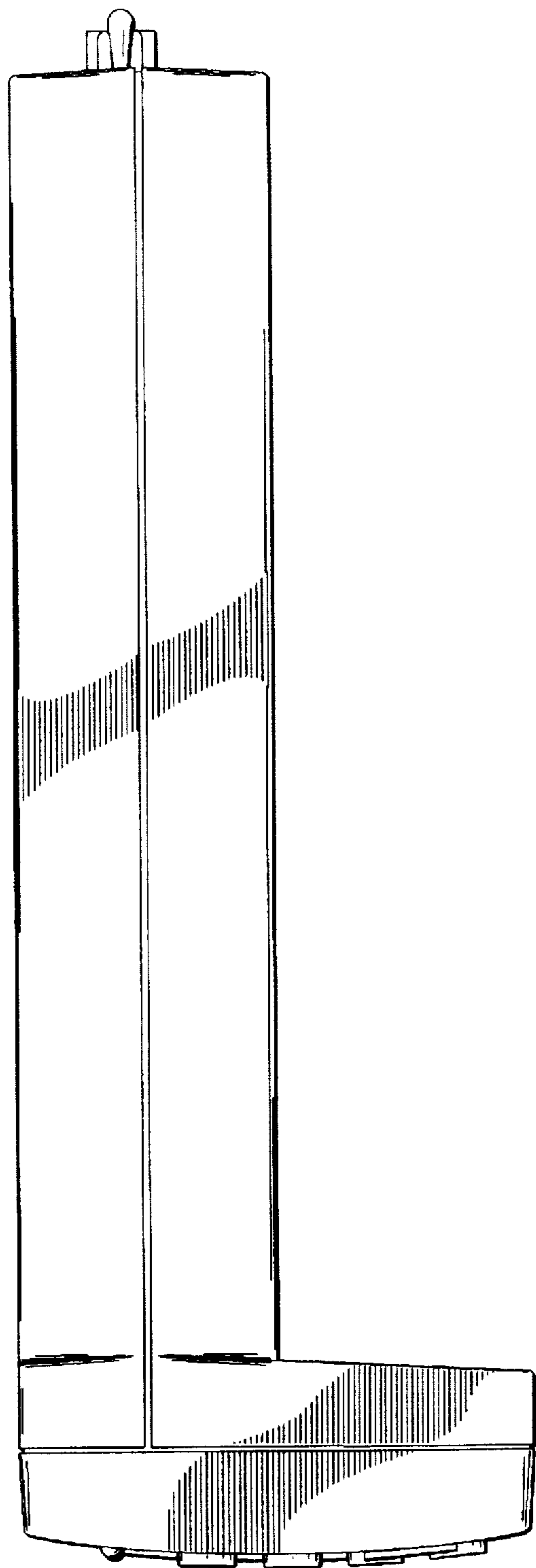
**FIG. 3**



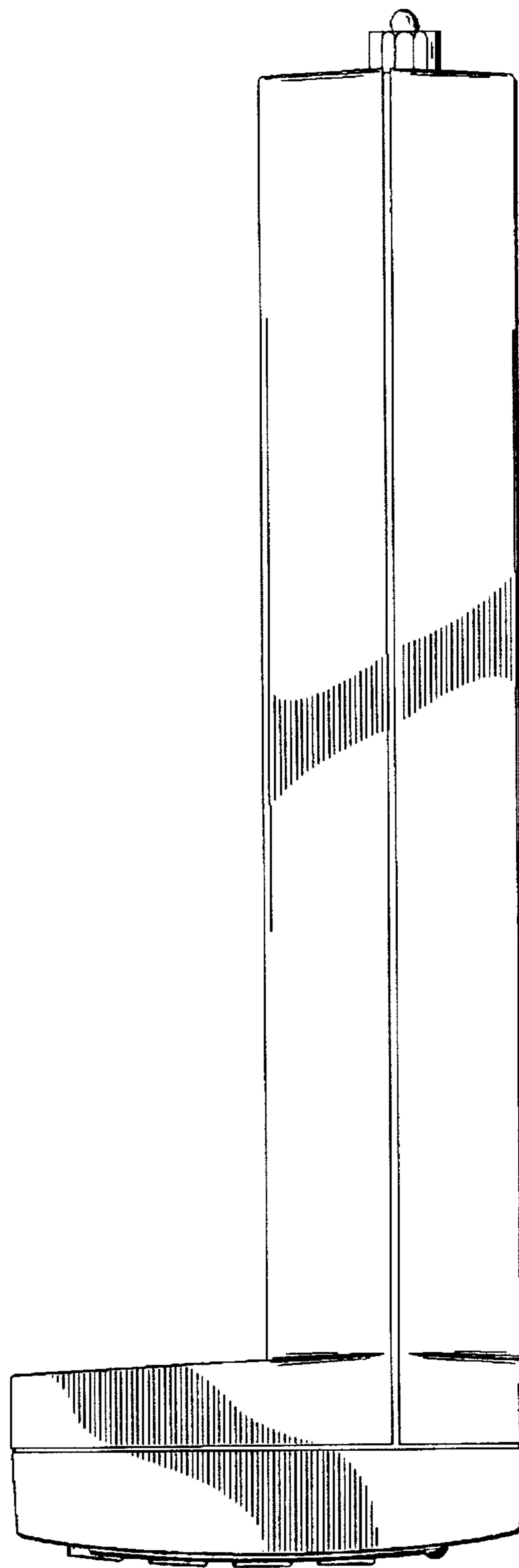
**FIG. 4**



**FIG. 5**



**FIG. 6**



**FIG. 7**