

US00D397093S

United States Patent [19]

Kim

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[54]		D CIRCUIT BOARD WITH ED COMPONENTS		
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[**]	Term:	14 Years		

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[21]	Appl. No	o.: 74,329	
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[52]	U.S. Cl.	************	D13/182
[58]	Field of	Search	D13/182; 361/380
—			361/384, 392, 395, 397, 399

56	[56] Reference	References Cited					
	U.S. PATENT	DOCUMENTS					
	D. 363,920 11/1995 Robert	ts et al D13					

D. 374,866	10/1996	Roberts et al. Johnston Yamada et al. Heung	D13/182
D. 389,808	1/1998		D13/182
•		Sutrina D	

Primary Examiner—Brian N. Vinson Attorney, Agent, or Firm—Price, Gess & Ubell

[57]

CLAIM

The ornamental design for the printed circuit board with mounted components, as shown and described.

DESCRIPTION

FIG. 1 is a top plan view of the printed circuit board with mounted components;

FIG. 2 is a bottom plan view of the printed circuit board with mounted components;

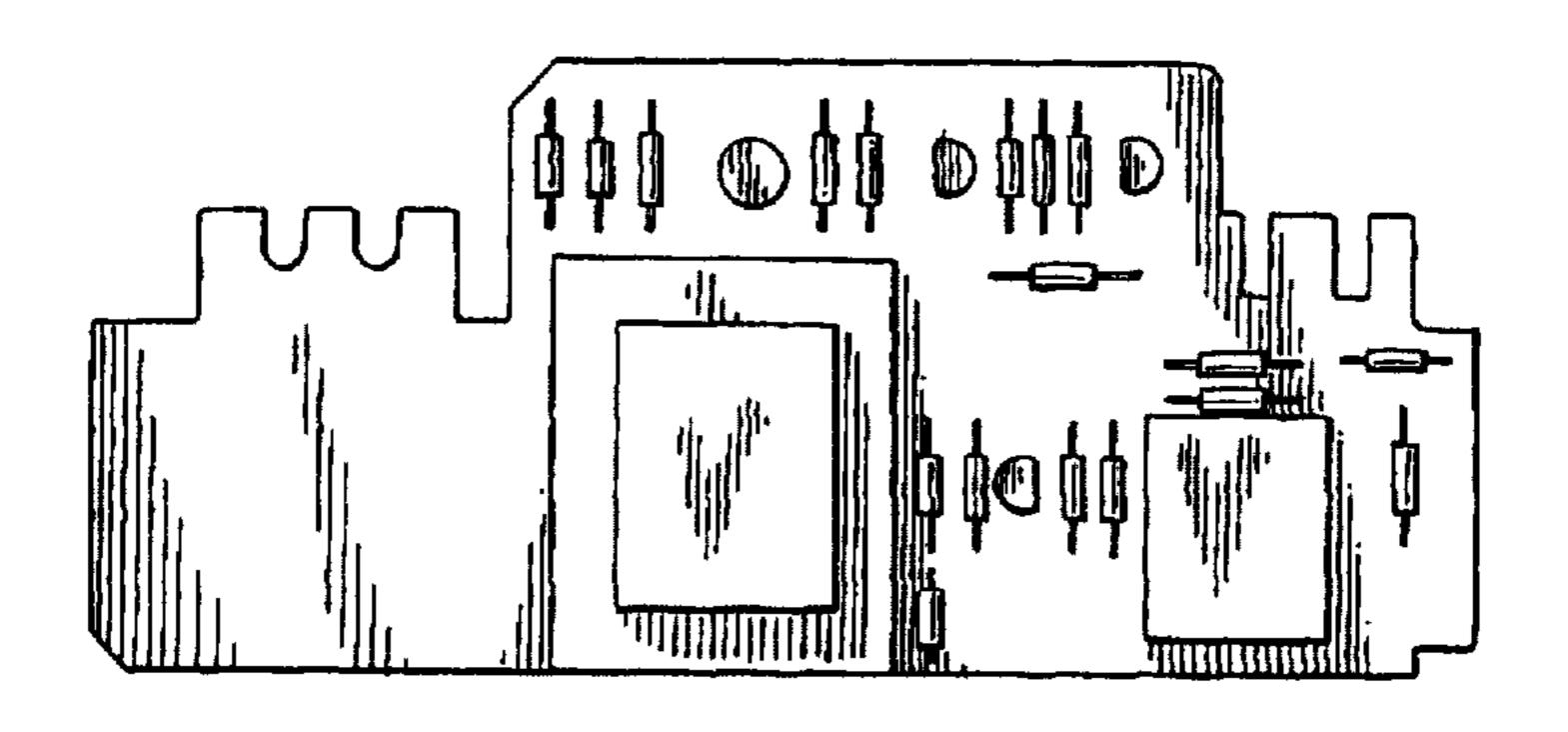
FIG. 3 is a front elevational view of the printed circuit board with mounted components;

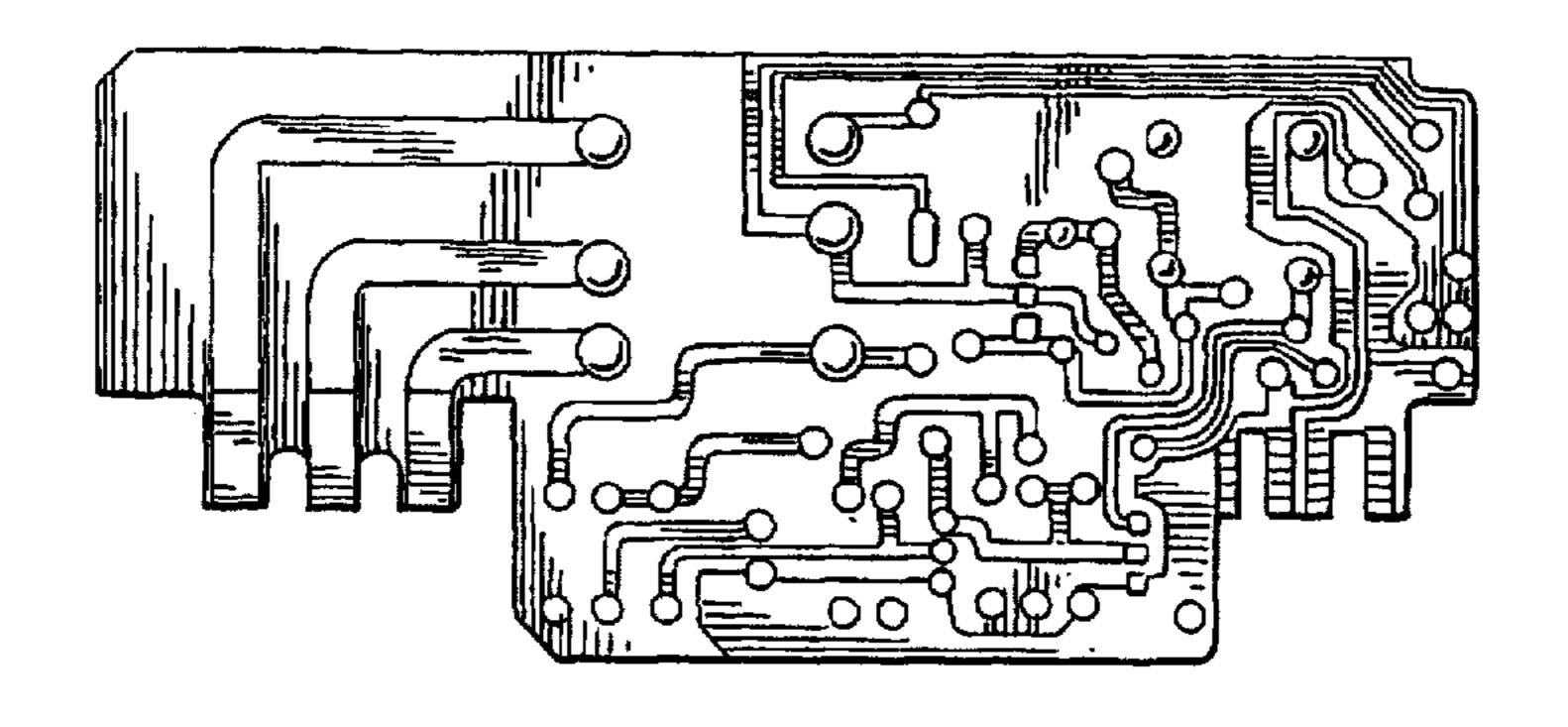
FIG. 4 is a rear elevational view of the printed circuit board with mounted components;

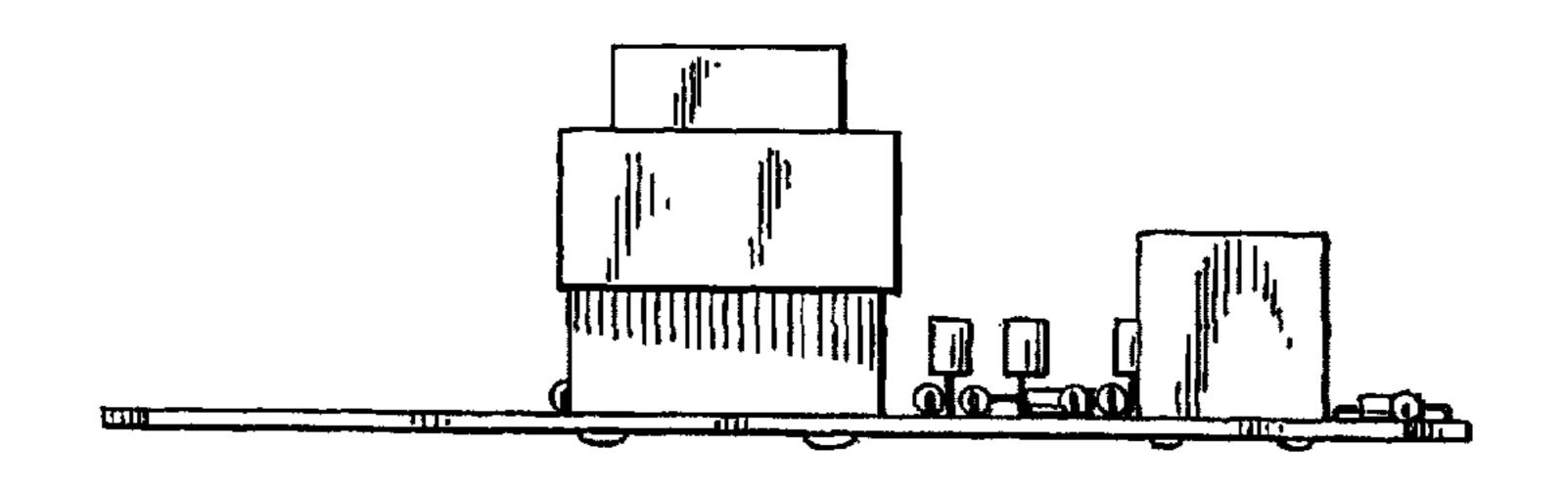
FIG. 5 is a left side elevational view of the printed circuit board with mounted components; and,

FIG. 6 is a right side elevational view of the printed circuit board with mounted components.

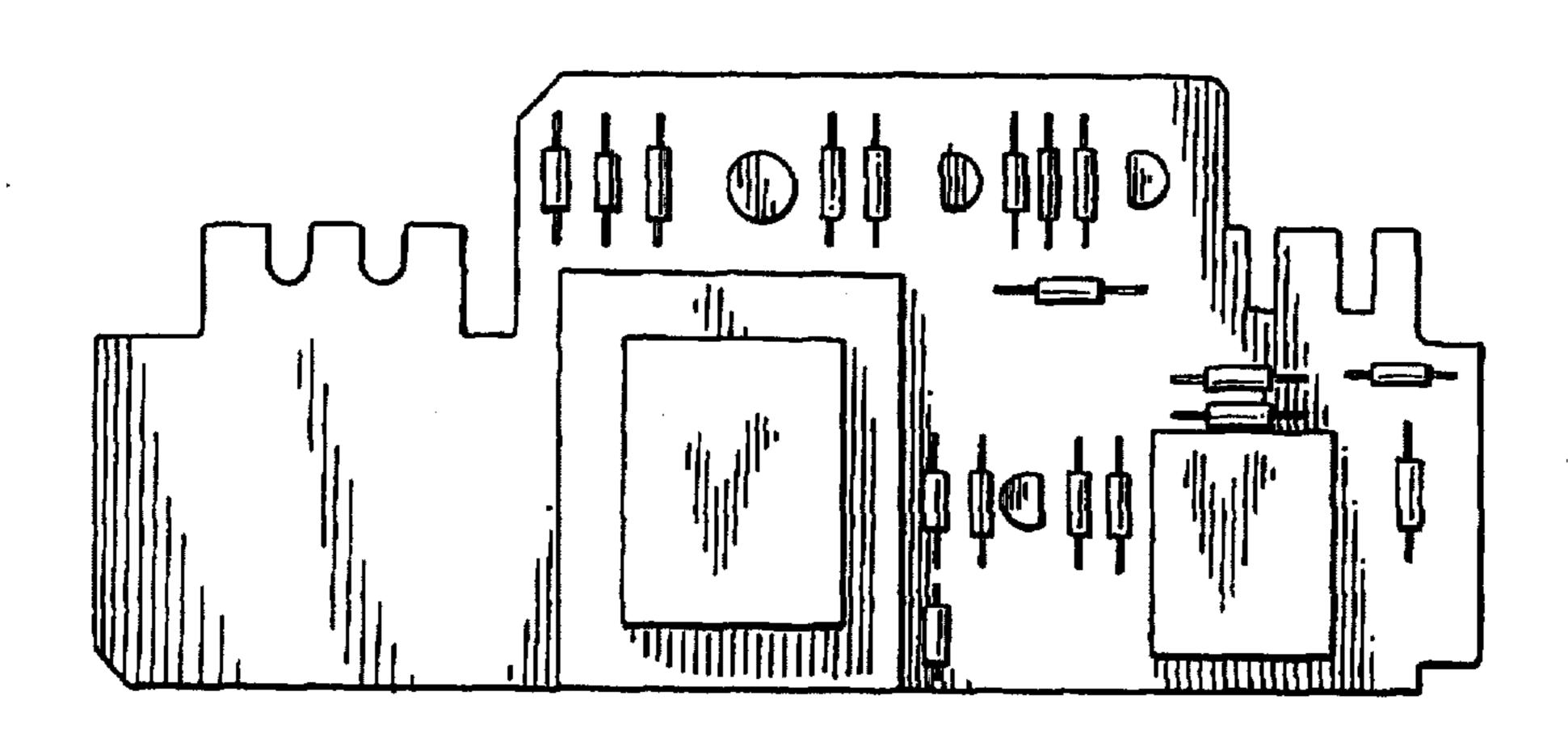
1 Claim, 2 Drawing Sheets



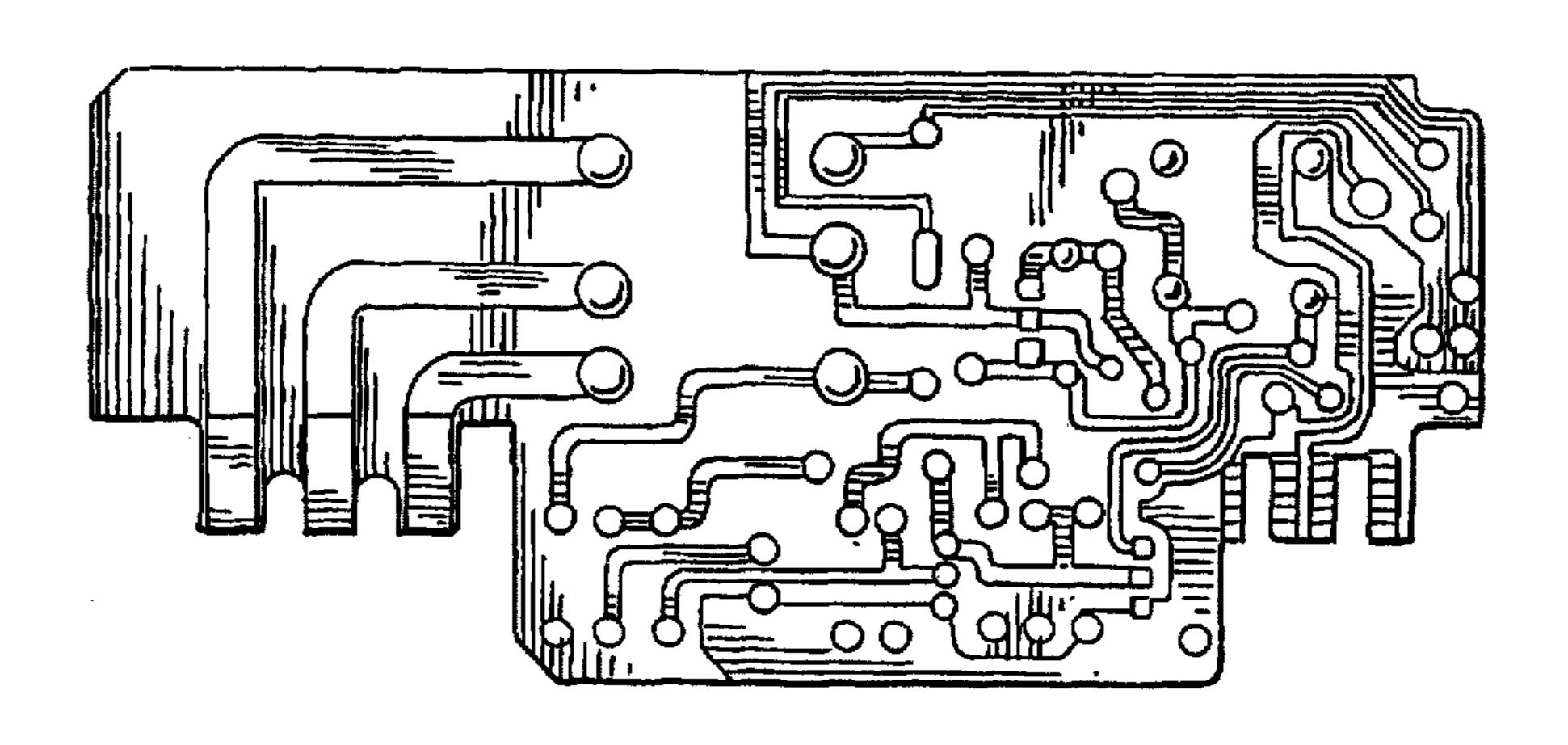


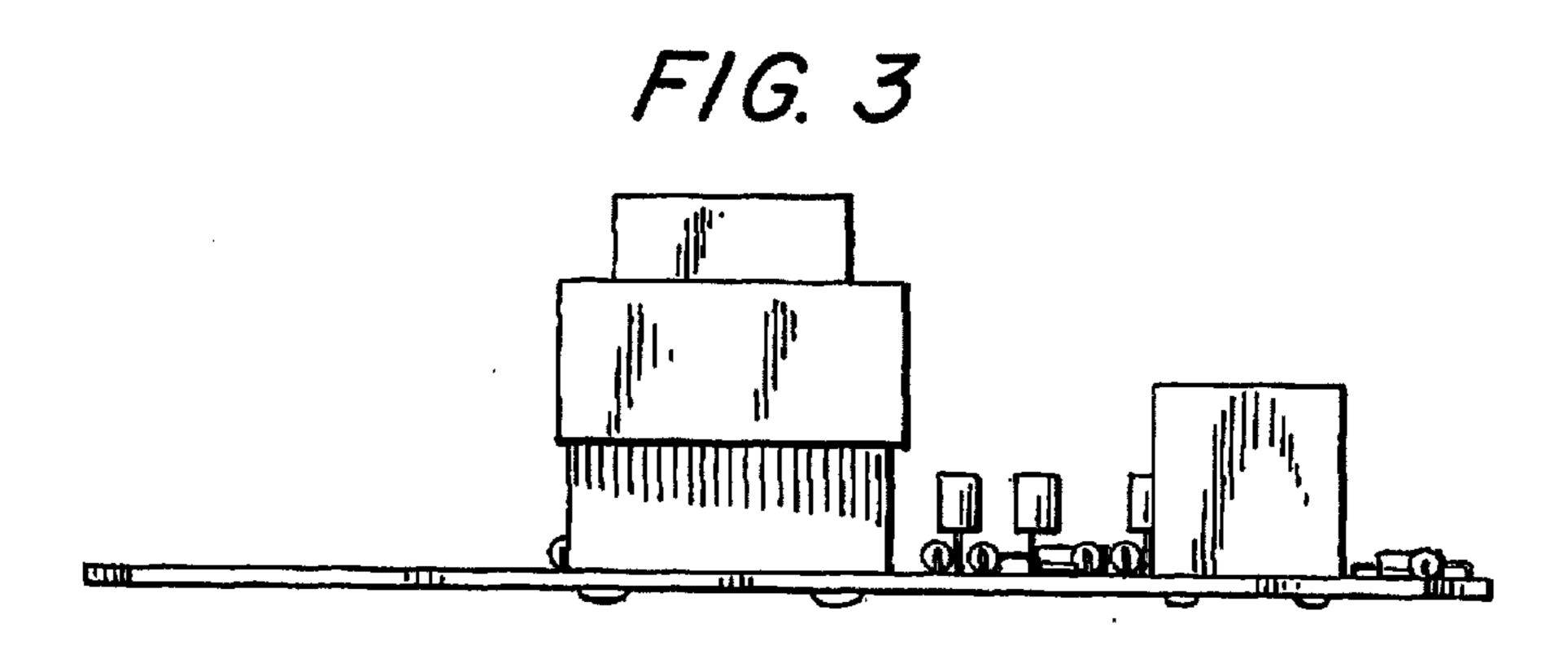


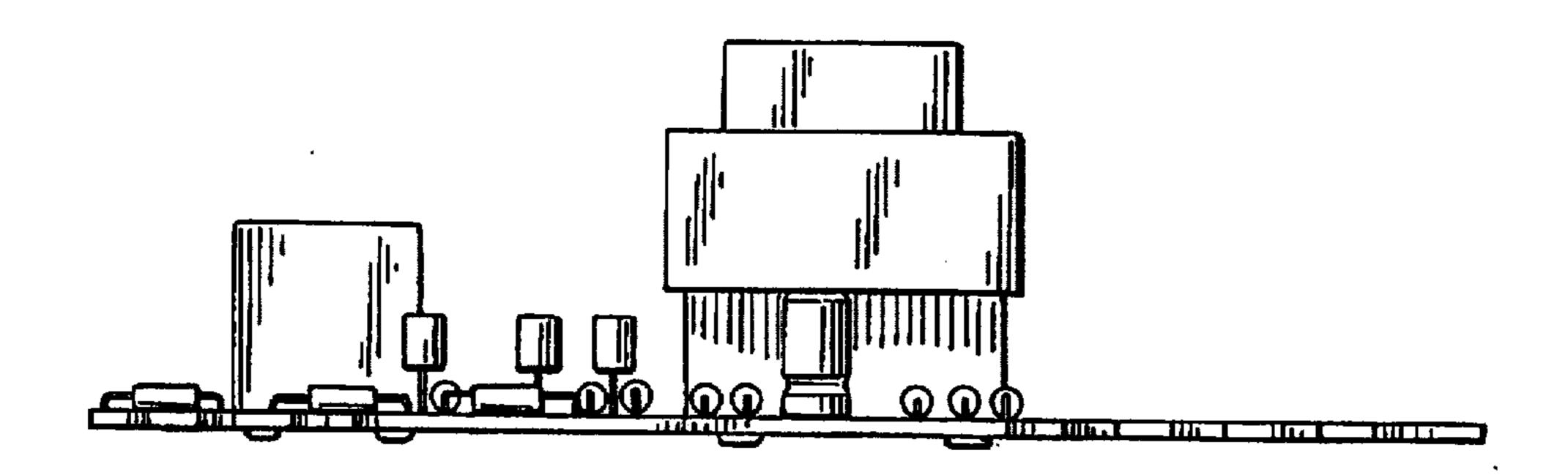
F/G. /



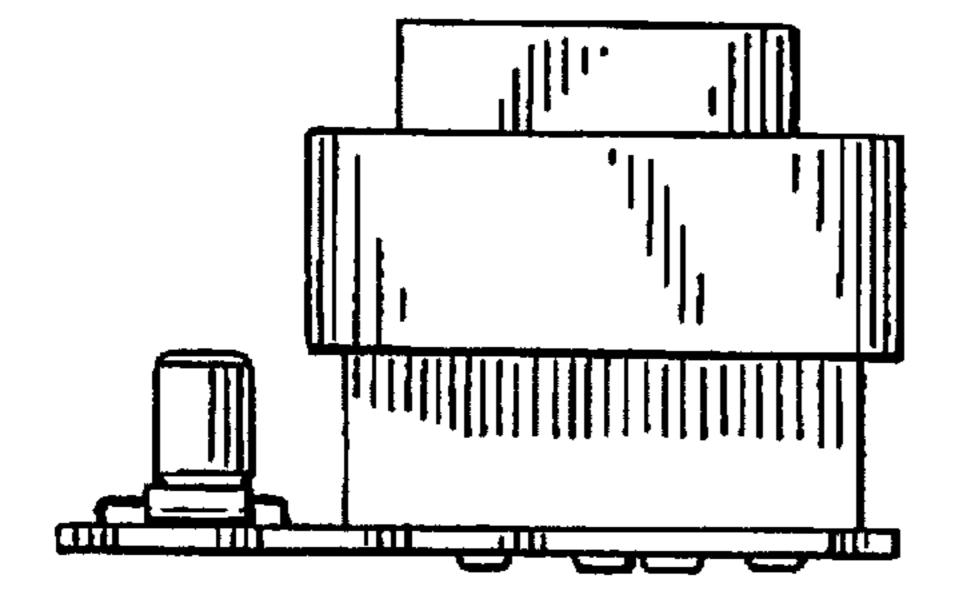
F1G. 2







F/G. 5



F/G. 6

