

# United States Patent [19]

[11] Patent Number: Des. 396,449
[45] Date of Patent: \*\*Jul. 28, 1998

- [54] CIRCUIT BOARD MODULE
- [75] Inventor: Brad Taylor, Oakland, Calif.
- [73] Assignee: Giga Operations Corporation, Berkeley, Calif.
- [\*\*] Term: 14 Years

Taylor

[21] Appl. No.: 41,332

#### [57] CLAIM

The ornamental design for a circuit board module, as shown.

#### DESCRIPTION

FIG. 1 is a face view of a circuit board module showing my new design;

FIG. 2 is a face view of a circuit board module showing my new design with a selection of devices shown in phantom; FIG. 3 is a bottom view of a circuit board module showing my new design with a selection of devices shown in phantom;

[22] Filed: Jul. 7, 1995

#### **Related U.S. Application Data**

- [63] Continuation-in-part of Ser. No. 13,593, Sep. 27, 1993, abandoned.

- [56] **References Cited**

#### U.S. PATENT DOCUMENTS

D. 306,576	3/1990	Hiatt et al
5,400,216	3/1995	Tsai

Primary Examiner—Antoine Duval Davis Attorney, Agent, or Firm—Crosby. Heafey. Roach & May FIG. 4 is a top, left side, isometric view of the circuit board module showing my new design;

FIG. 5 is a bottom, right side, isometric view of the bottom side of a circuit board module showing my new design; FIG. 6 is a top, left side, isometric view of the circuit board module showing my new design with a selection of devices mounted to the board of the inventive design;

FIG. 7 is a bottom, right side, isometric view of the bottom side of a circuit board module showing my new design with a selection of devices mounted to the board of the inventive design;

FIG. 8 is a first side view of a circuit board module showing my new design;

FIG. 9 is a second side view of a circuit board module showing my new design; and.

FIG. 10 is a side view of a circuit board module as shown in FIG. 9 with a selection of devices shown in phantom.

#### 1 Claim, 4 Drawing Sheets



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FIG. 1



## FIG. 3

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FIG. 2



FIG. 8





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