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United States Patent [19]

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Ishihara

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[54] **IC MODULE**

D. 369,156 4/1996 Ohmori et al. D14/117
D. 369,157 4/1996 Ohmori et al. D14/117

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FOREIGN PATENT DOCUMENTS

[73] Assignee: **Kabushiki Kaisha Toshiba, Kawasaki, Japan**

780452 2/1990 Japan .
865665 4/1993 Japan .

[**] Term: **14 Years**

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[21] Appl. No.: **62,838**

[22] Filed: **Nov. 27, 1996**

[57] CLAIM

[30] Foreign Application Priority Data

May 29, 1996 [JP] Japan 8-15398 U

The ornamental design for an IC module, as shown and described.

[51] LOC (6) Cl. **14-02**

DESCRIPTION

[52] U.S. Cl. **D14/114; D14/117**

FIG. 1 is a front, bottom and right side perspective view of an IC module, showing my new design;
FIG. 2 is a front elevational view thereof;
FIG. 3 is a right side elevational view thereof, the opposite side being a mirror image;
FIG. 4 is a top plan view thereof;
FIG. 5 is a bottom plan view thereof; and,
FIG. 6 is a rear elevational view thereof.

[58] Field of Search D14/114, 117,
D14/107; 235/378-380, 492, 487; 257/731,
669

[56] References Cited

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D. 368,903 4/1996 Ohmori et al. D14/117

1 Claim, 1 Drawing Sheet

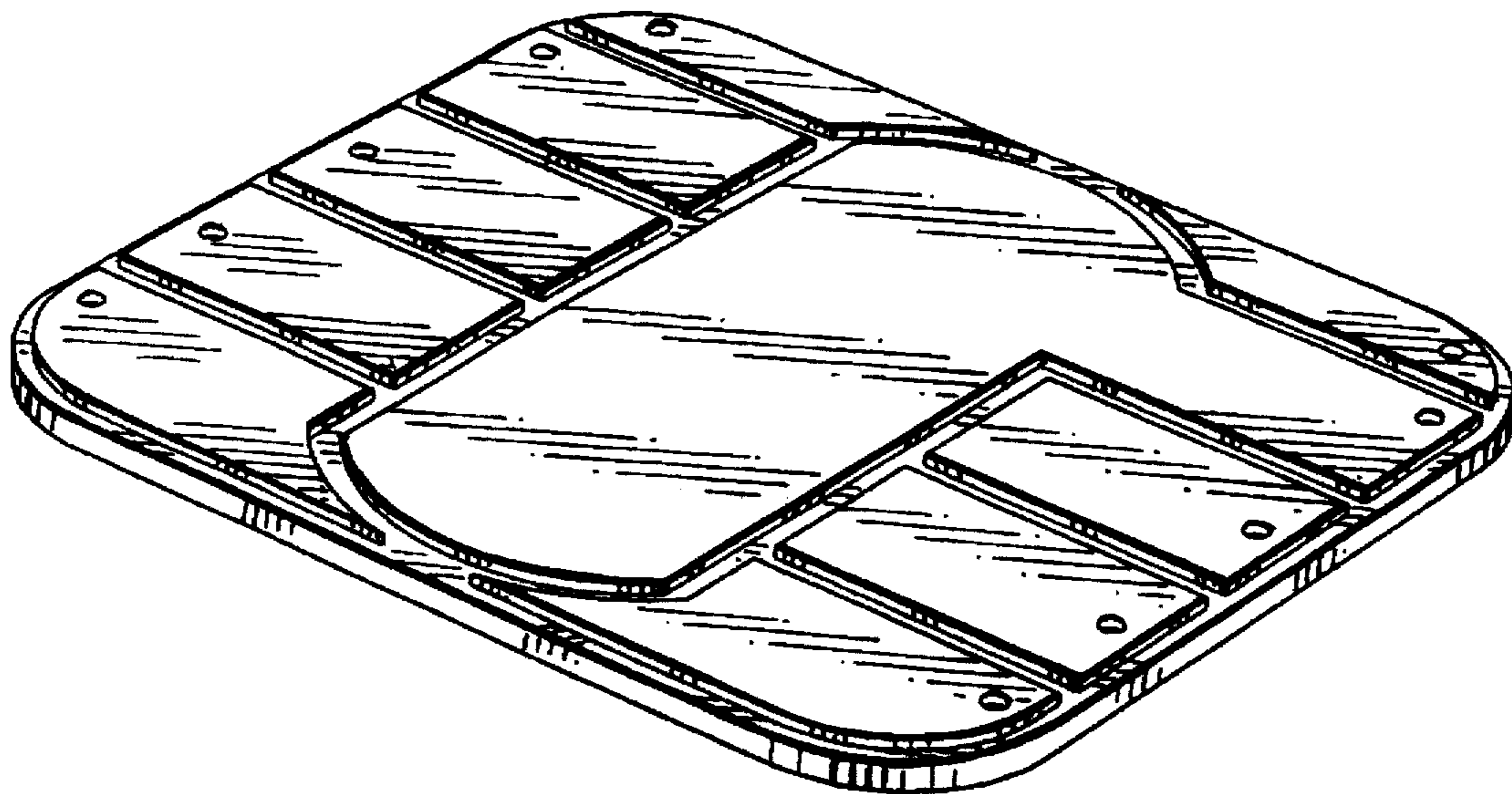


FIG. 4

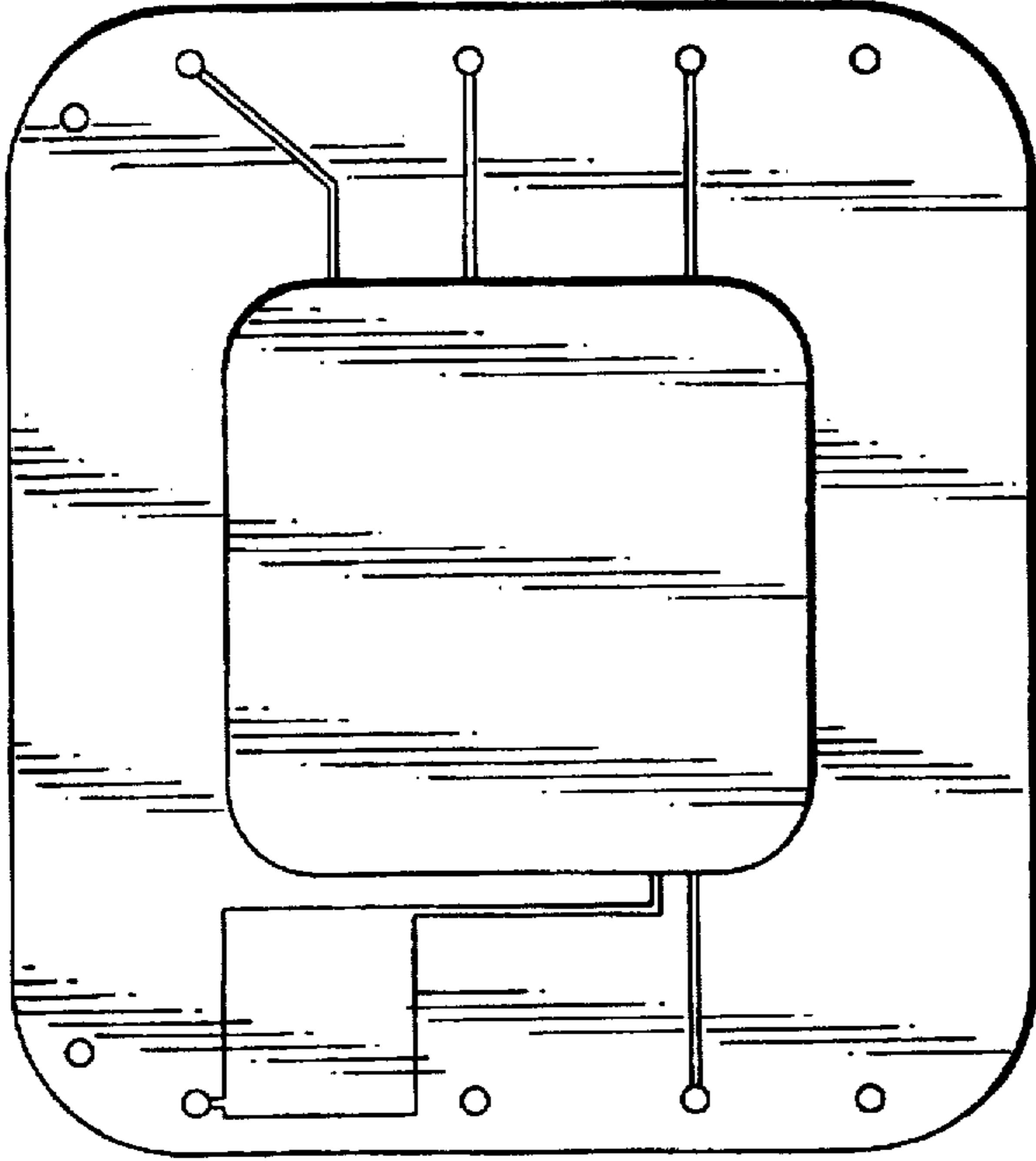


FIG. 5



FIG. 6



FIG. 1

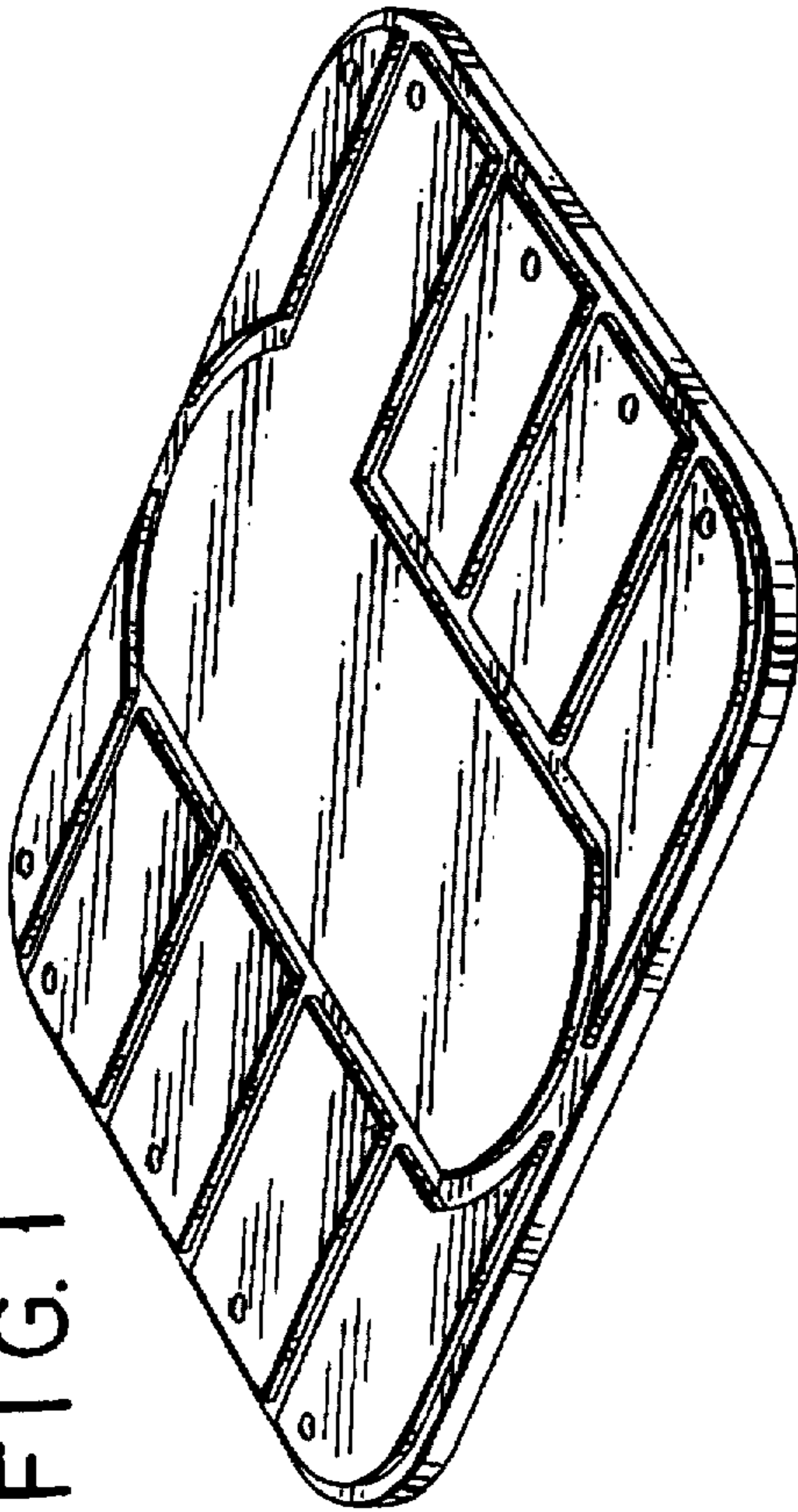


FIG. 3

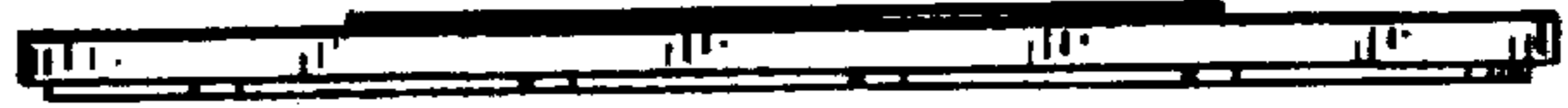


FIG. 2

