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United States Patent [19]

Horstmann

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[54] **MINI FAULTED CIRCUIT INDICATOR**

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[73] Assignee: **Dipl.-Ing H. Horstmann GmbH**, Germany

[**] Term: **14 Years**

[21] Appl. No.: **56,110**

[22] Filed: **Jun. 24, 1996**

[51] **LOC (6) Cl.** **10-04**

[52] **U.S. Cl.** **D10/75**

[58] **Field of Search** D10/75; 324/133, 324/522, 555; 340/660, 664, 646, 656, 652; 361/1, 59, 83, 93, 94, 96

[56] **References Cited**

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5,497,096	3/1996	Banting	324/555

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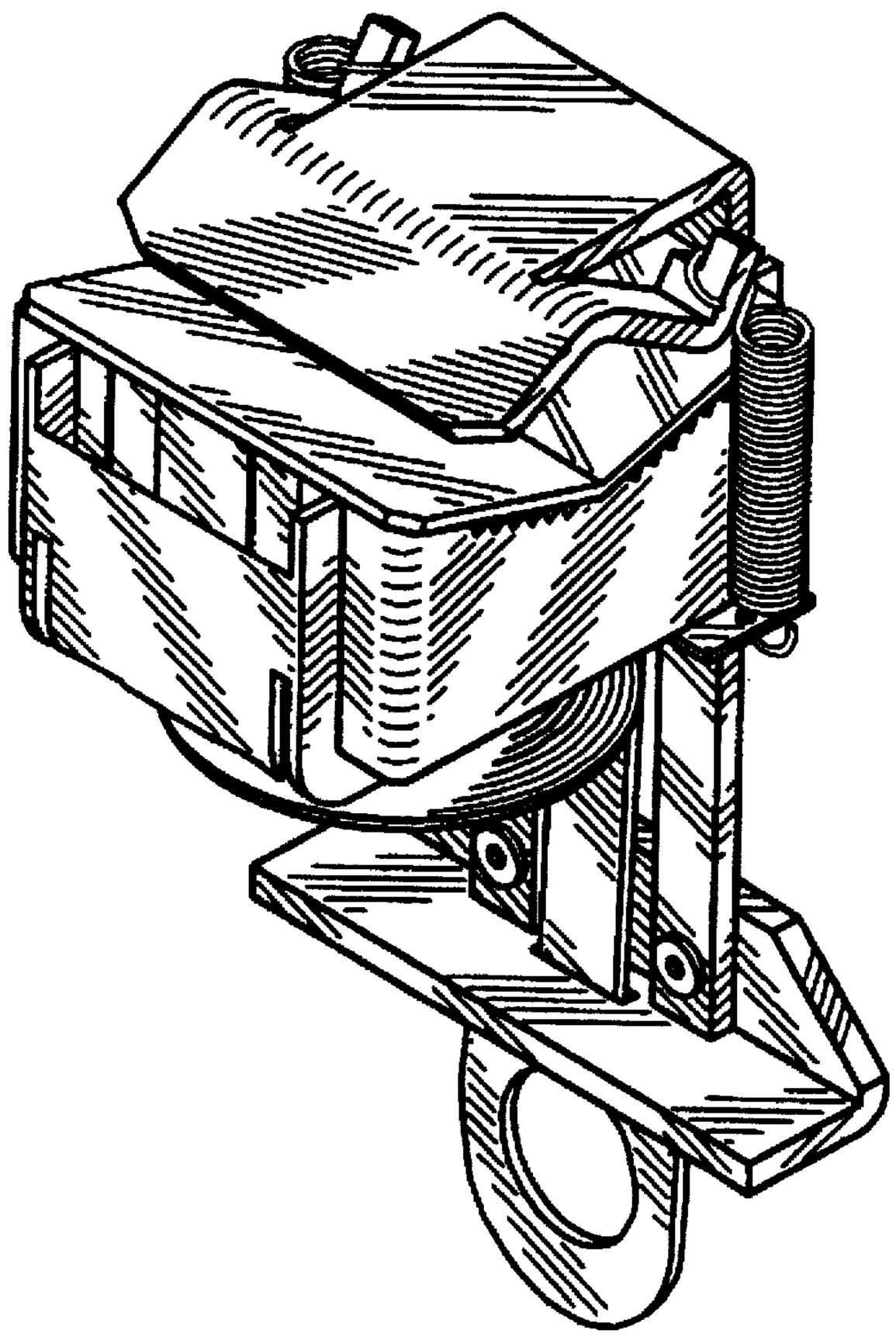
[57] **CLAIM**

The ornamental design for a mini faulted circuit indicator, as shown and described.

DESCRIPTION

FIG. **1** is a front elevational view of a mini faulted circuit indicator showing my design;
FIG. **2** is a top plan view of the design shown in FIG. **1**;
FIG. **3** is a right side elevational view of the design shown in FIG. **1**;
FIG. **4** is a bottom view of the design shown in FIG. **1**;
FIG. **5** is a back view of the design shown in FIG. **1**; and,
FIG. **6** is a perspective view of the design shown in FIG. **1**.

1 Claim, 2 Drawing Sheets



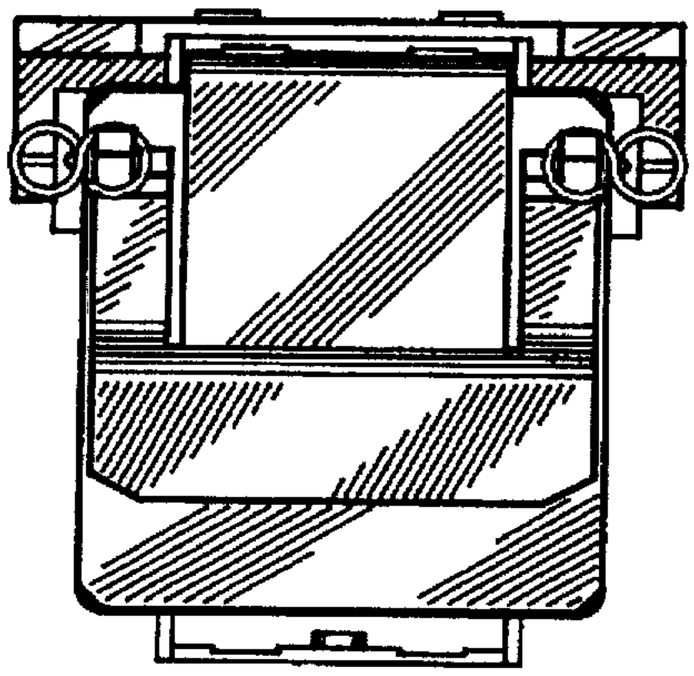


Fig. 2

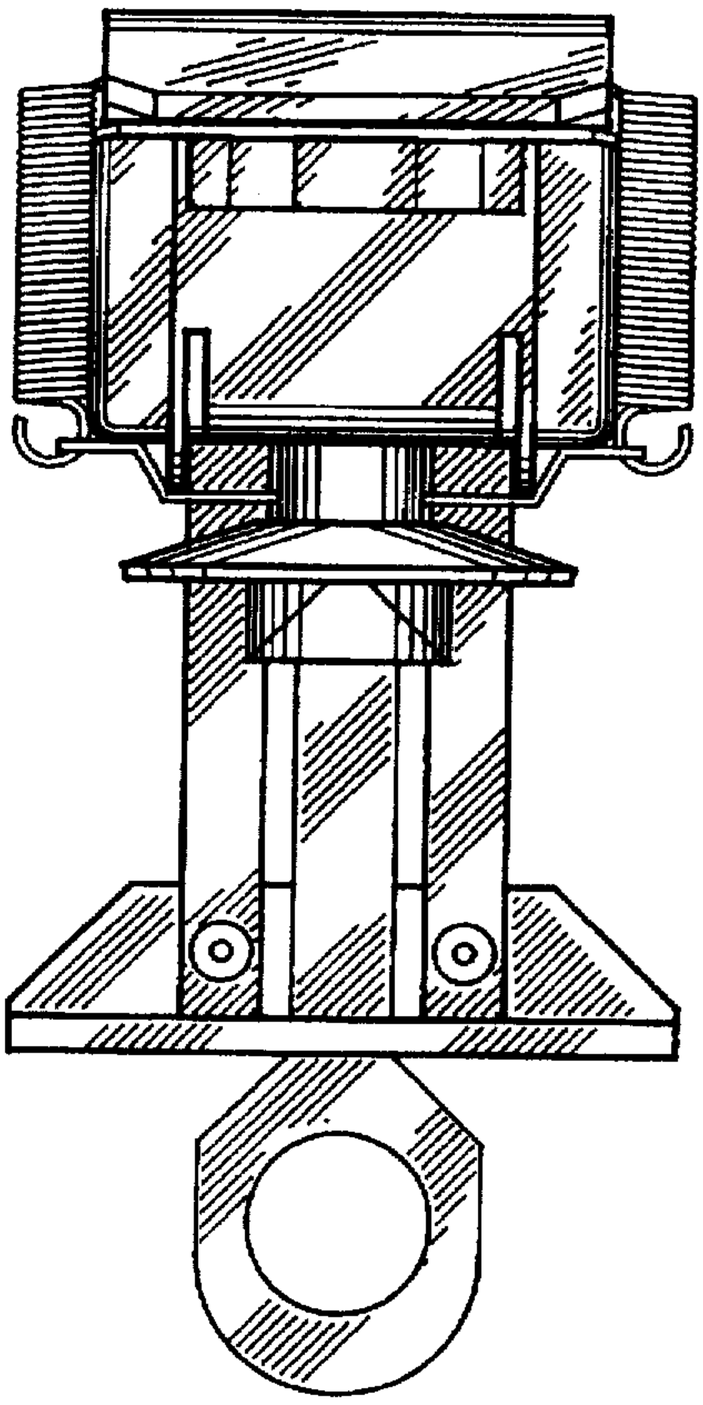


Fig. 1

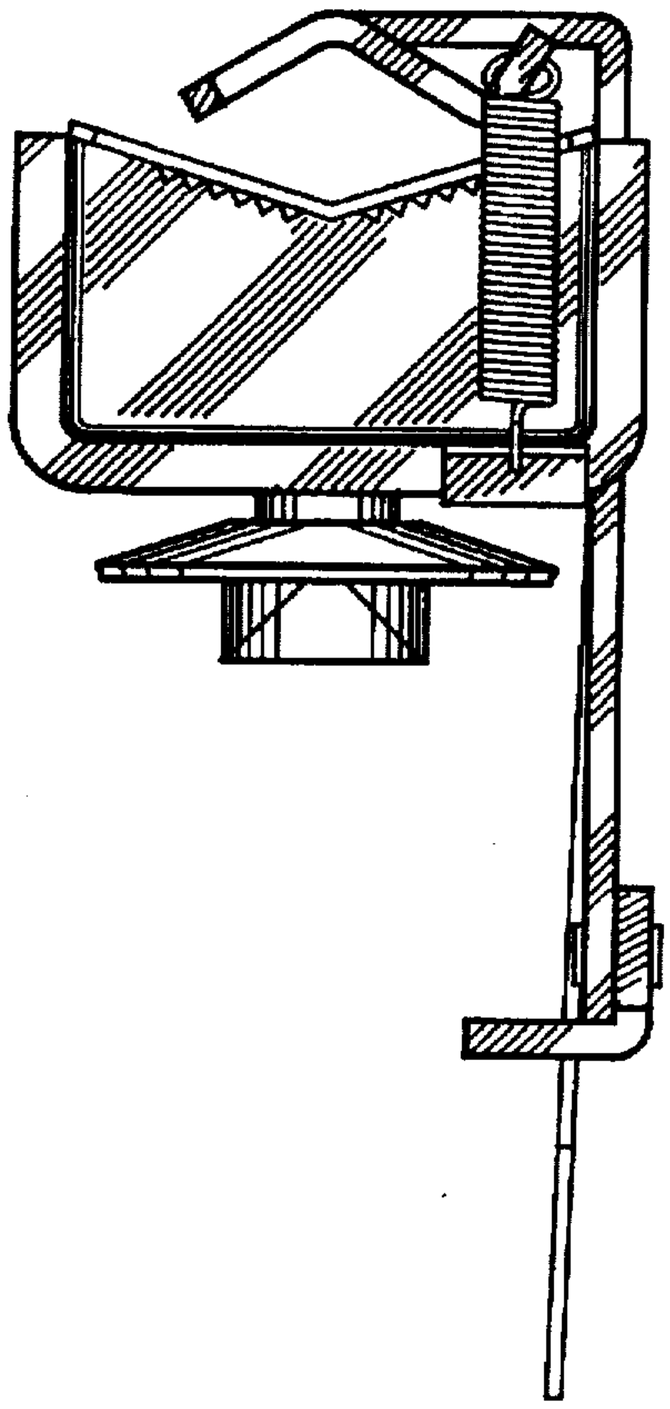


Fig. 3

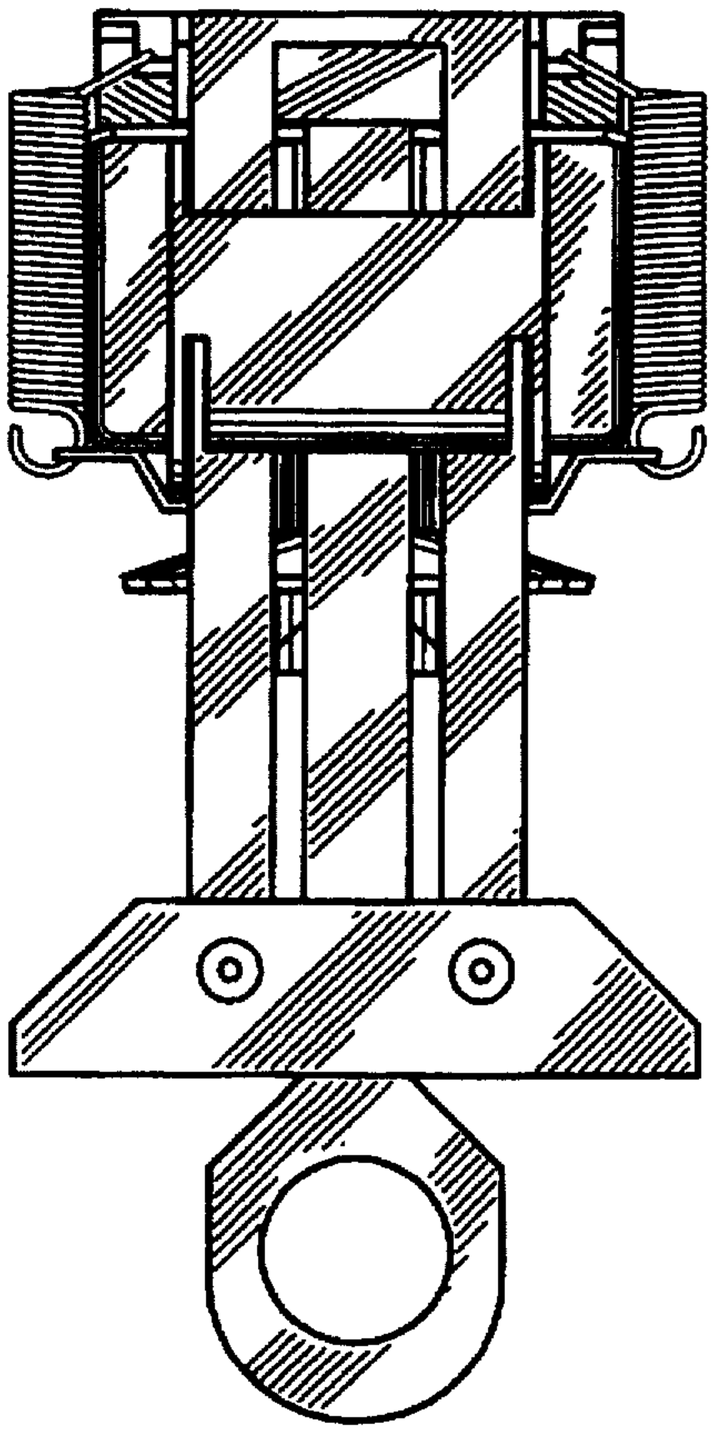


Fig. 5

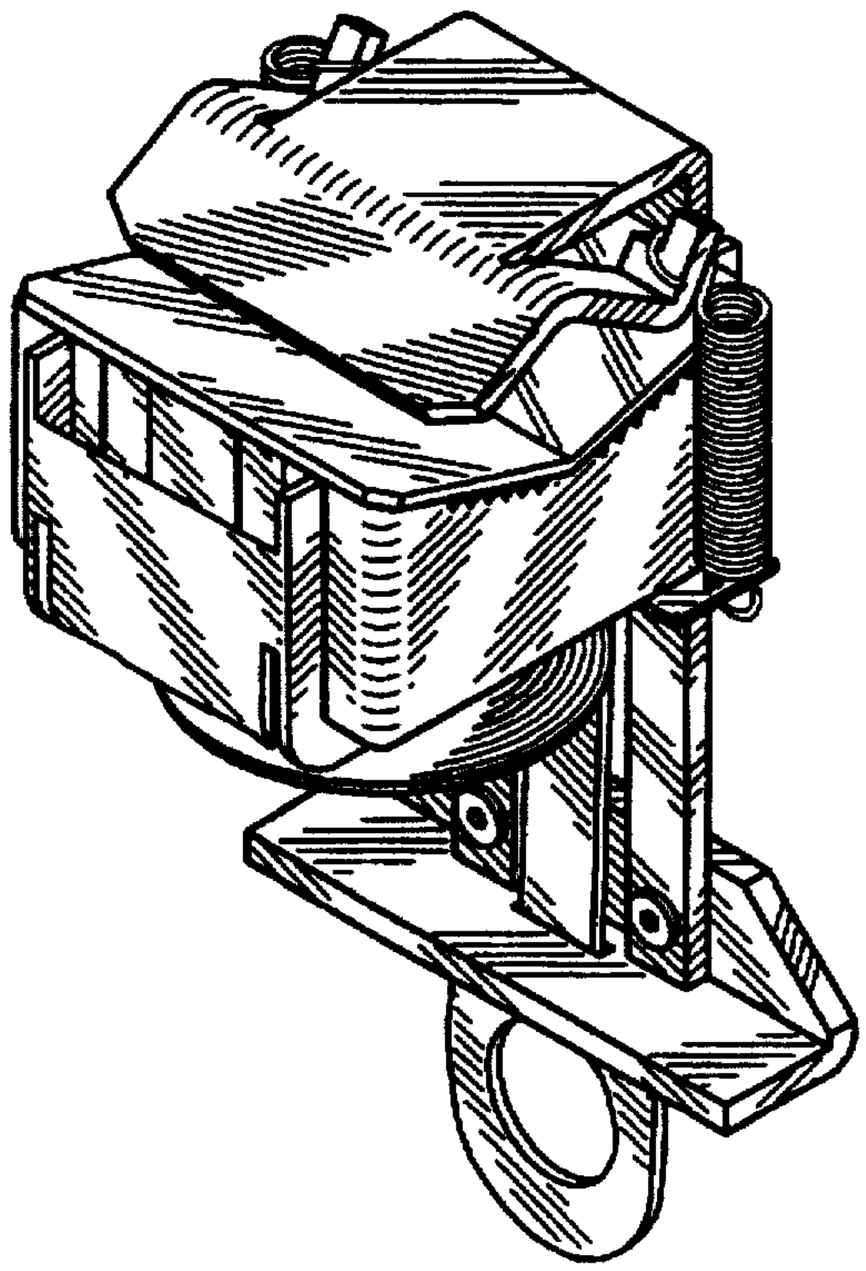


Fig. 6

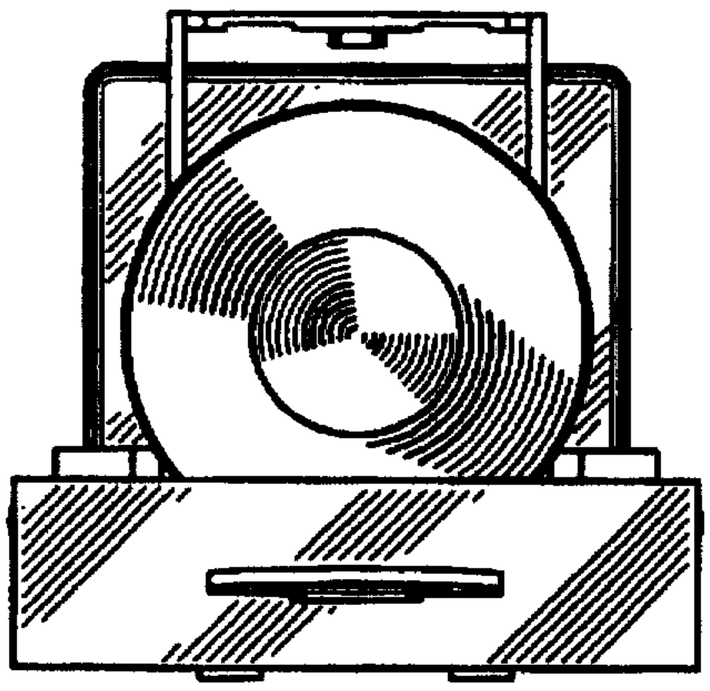


Fig. 4