

US00D384650S

# United States Patent [19]

Heberling et al.

[11] Patent Number: **Des. 384,650**

[45] Date of Patent: **\*\*Oct. 7, 1997**

[54] **MULTIPLE POSITION ENCLOSURE FOR ELECTRONICS DEVICE**

D. 371,348 7/1996 Idleman et al. .... D14/109  
D. 372,021 7/1996 Idleman et al. .... D14/109

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### [57] CLAIM

[\*\*] Term: **14 Years**

The ornamental design for a multiple position enclosure for electronics device, as shown.

[21] Appl. No.: **56,260**

### DESCRIPTION

[22] Filed: **Jun. 18, 1996**

FIG. 1 is a perspective view of a multiple position enclosure for electronics device embodying our new design;

[51] LOC (6) Cl. .... **14-02**

FIG. 2 is a front elevational view thereof;

[52] U.S. Cl. .... **D14/102; D14/109**

FIG. 3 is a rear elevational view thereof;

[58] Field of Search ..... D14/100, 102,

FIG. 4 is a left side elevational view thereof;

D14/107-109; D13/162, 184, 199; D6/432, 436, 445, 448; 312/208.1, 254, 292; 360/99.12; 369/34, 36; 361/694, 696, 680-693, 695

FIG. 5 is a right side elevational view thereof;

### [56] References Cited

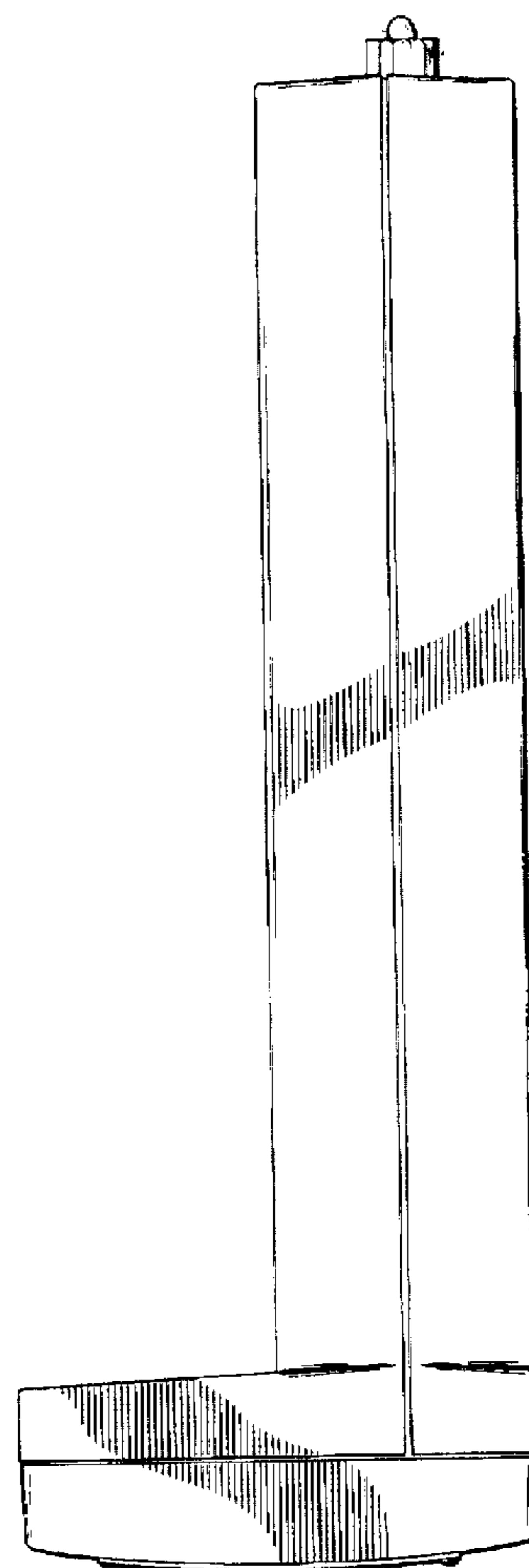
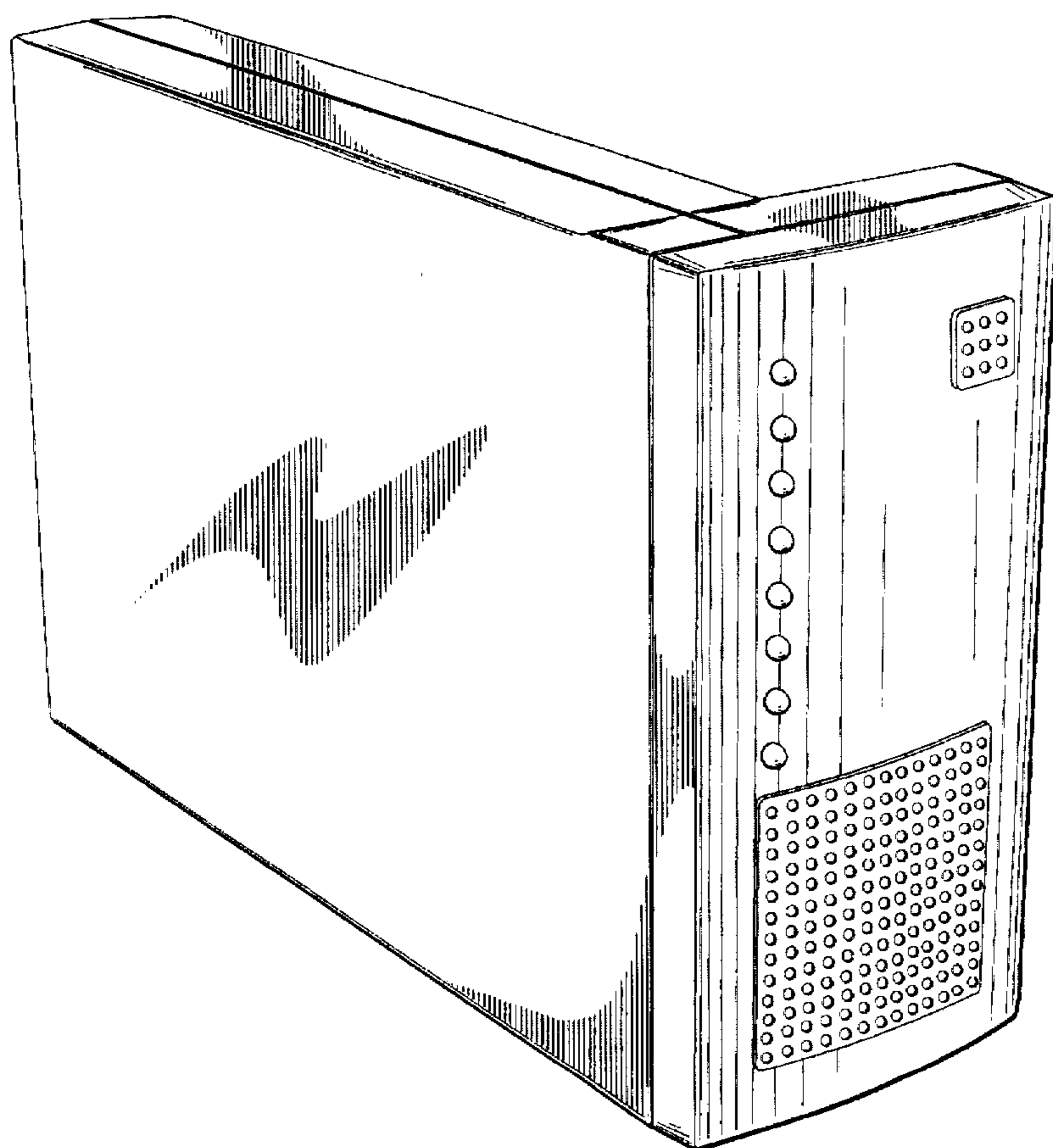
FIG. 6 is a top plan view thereof; and,

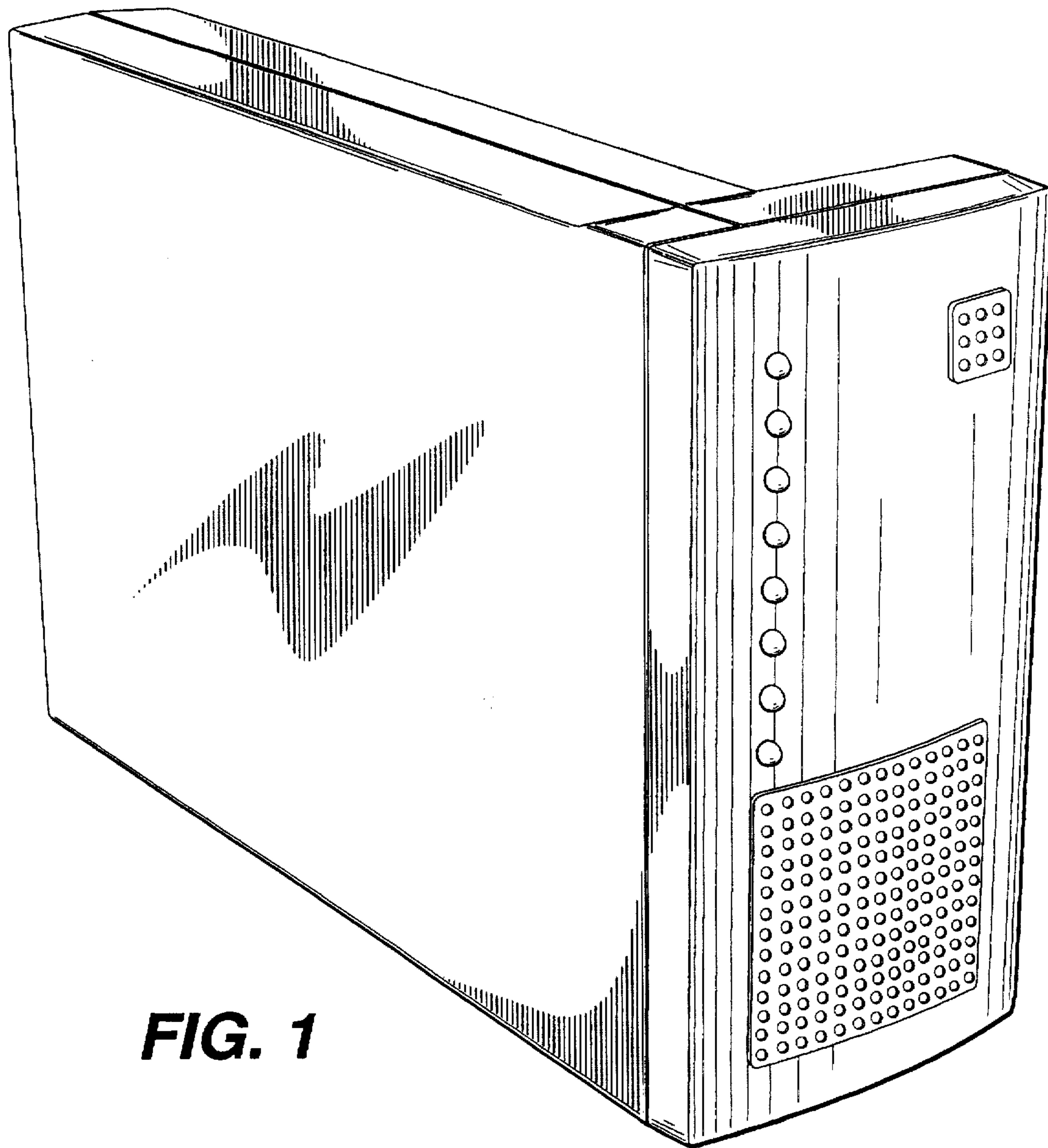
FIG. 7 is a bottom plan view thereof.

#### U.S. PATENT DOCUMENTS

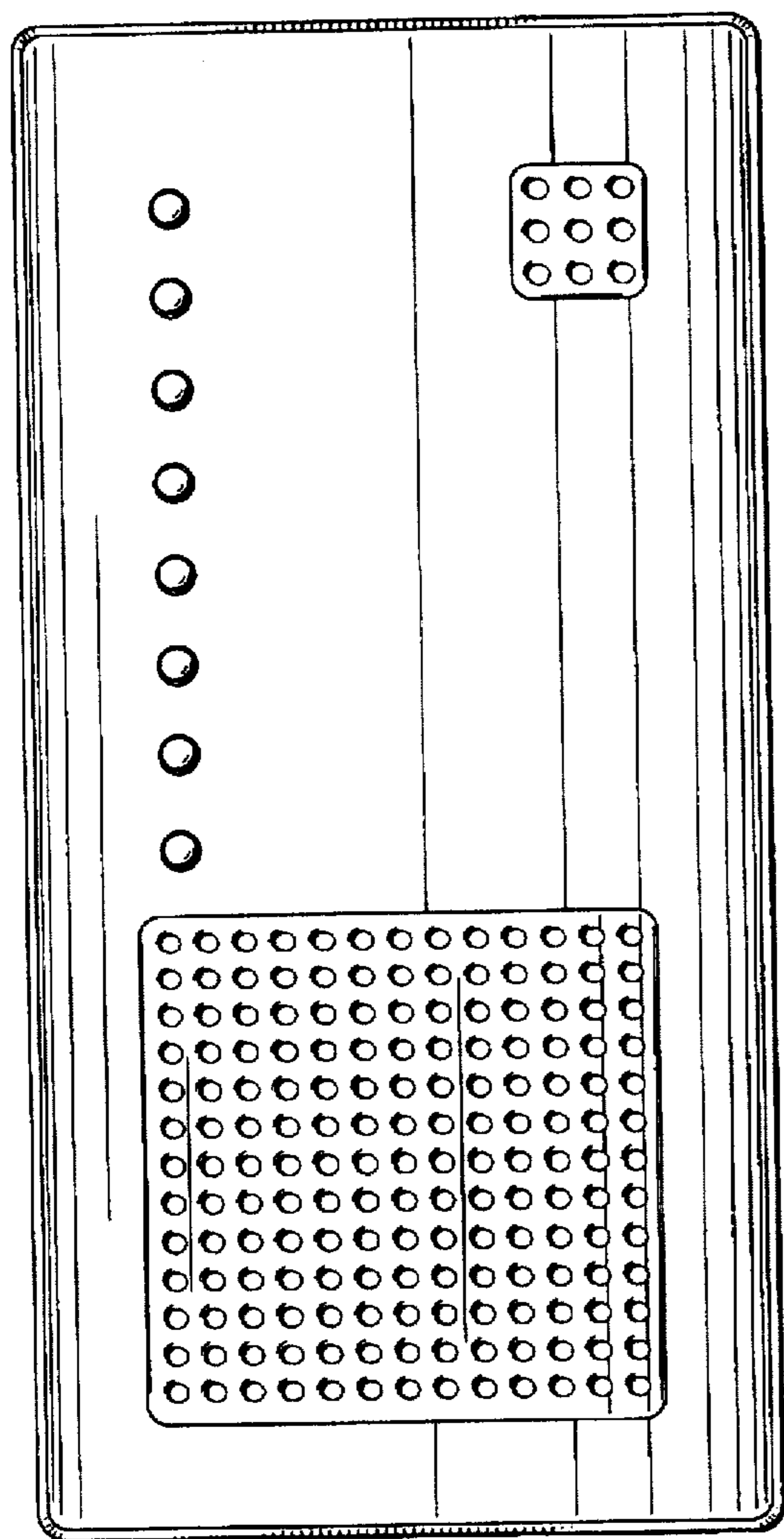
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**1 Claim, 5 Drawing Sheets**

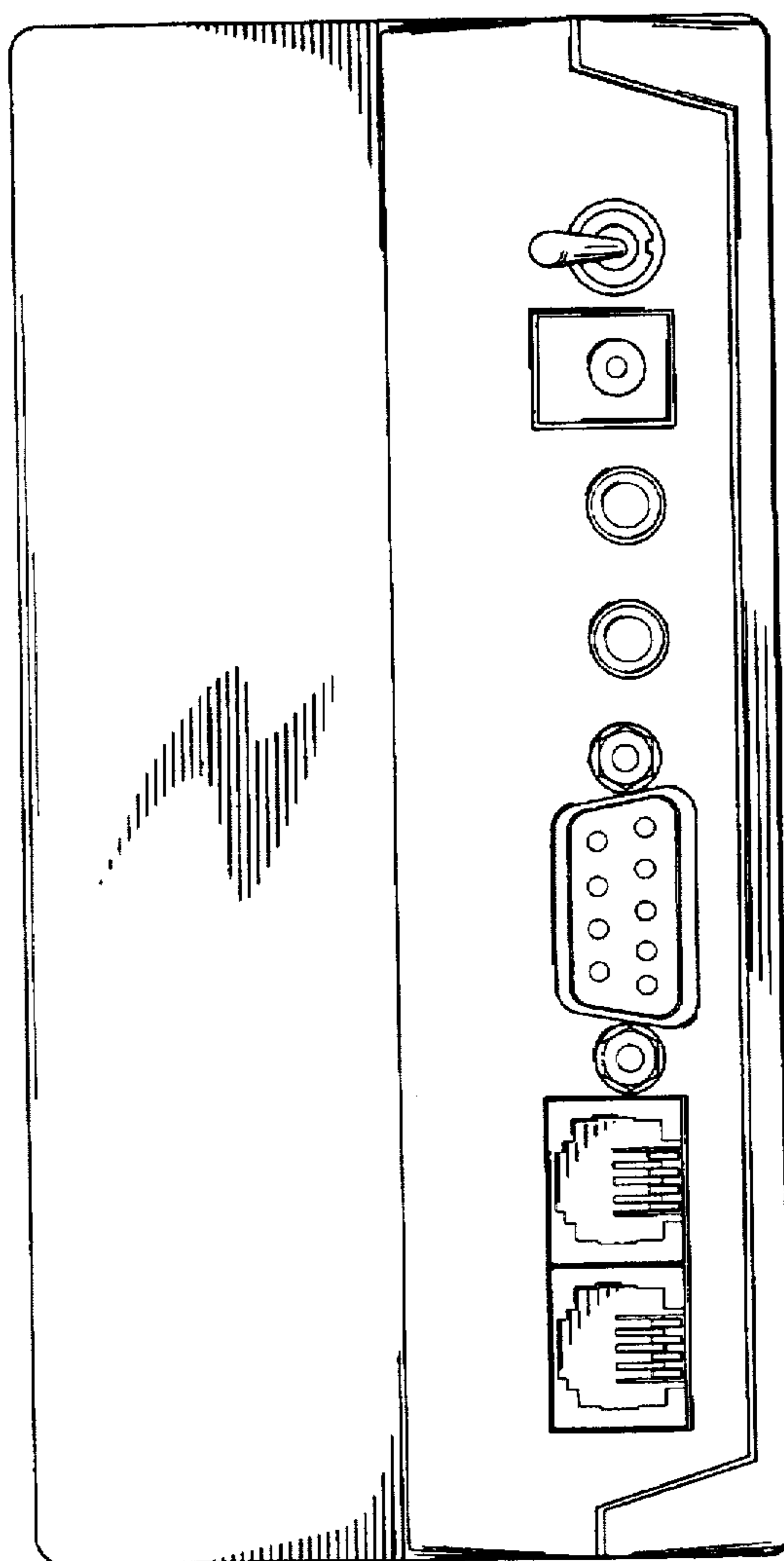




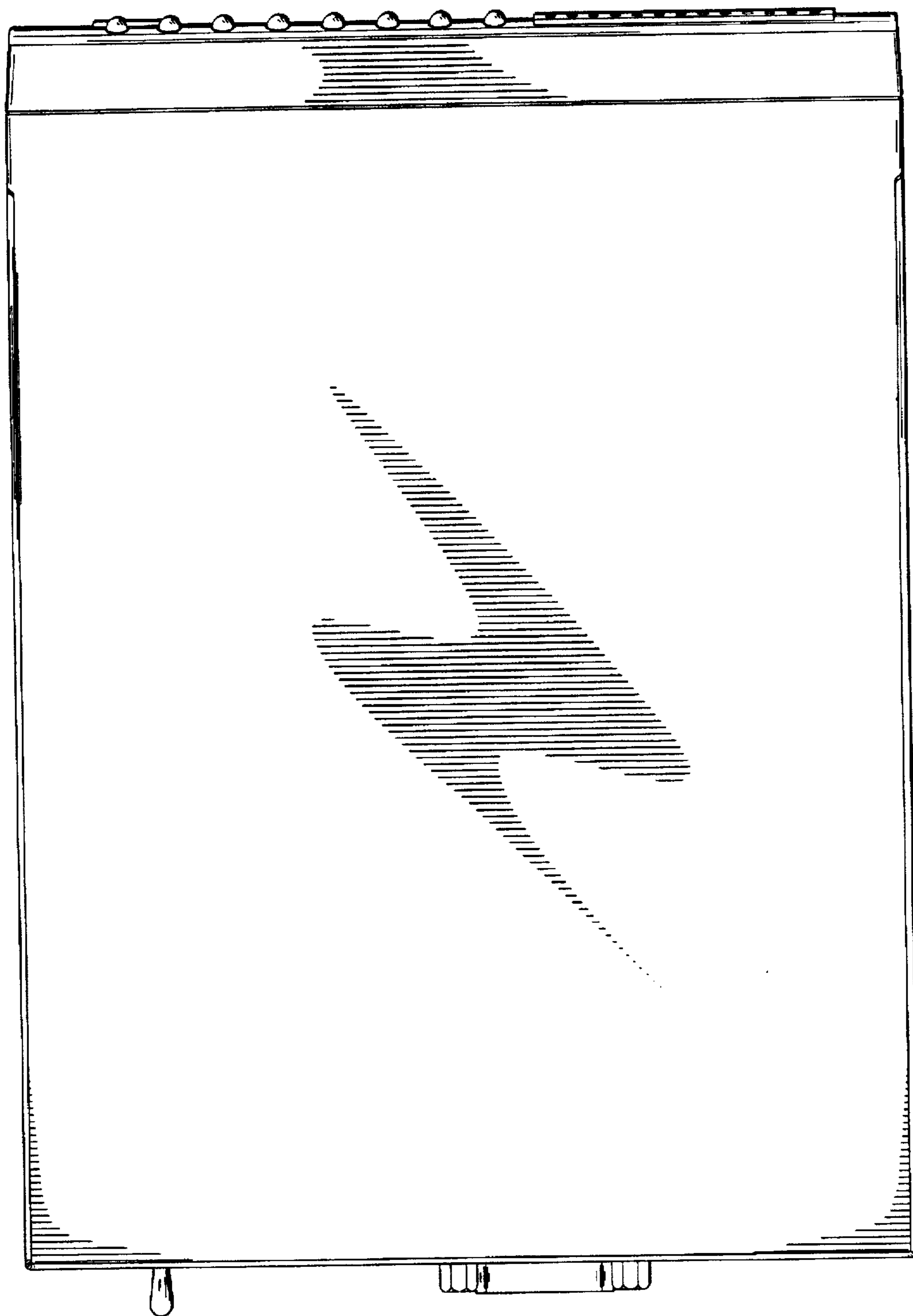
**FIG. 1**



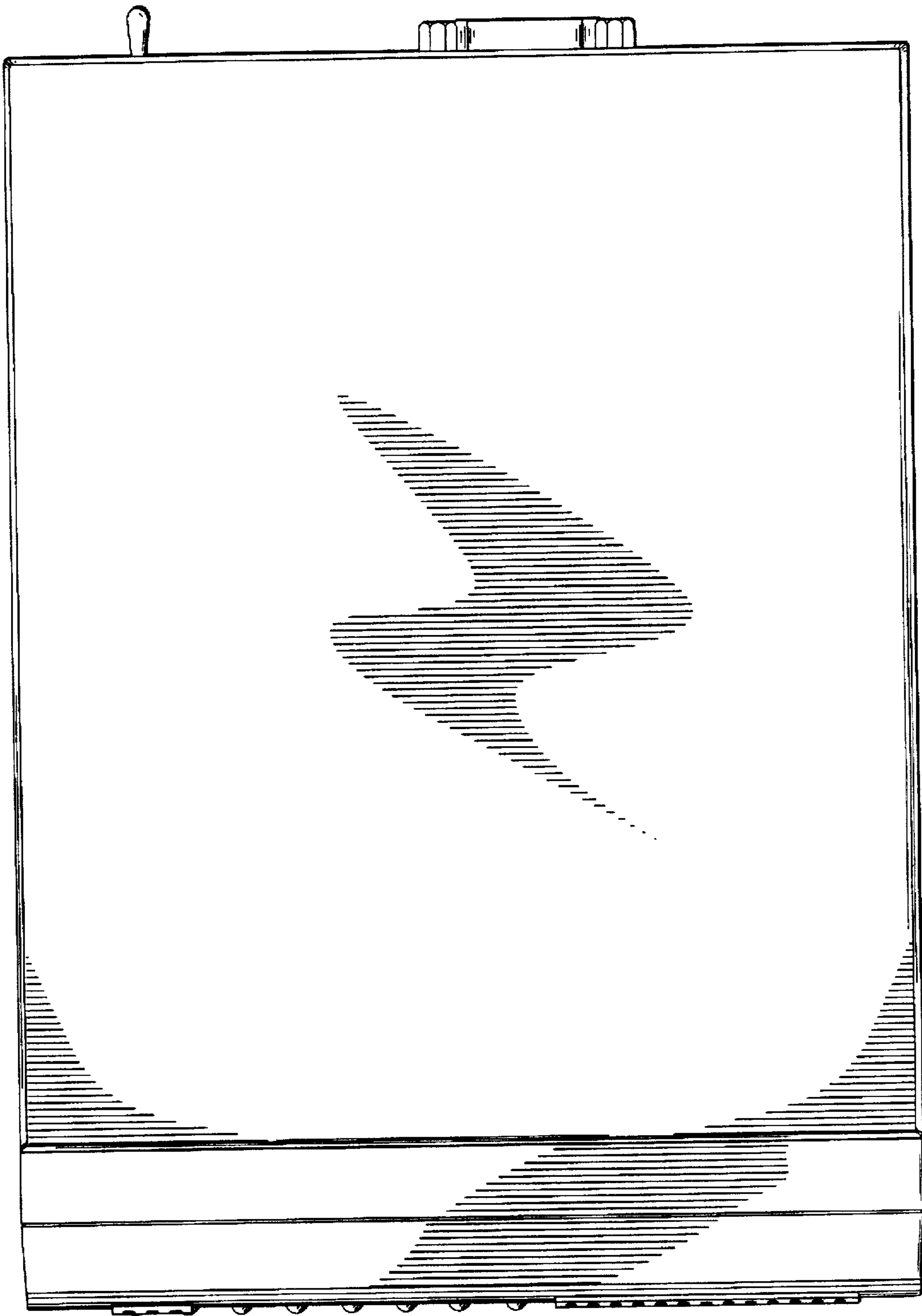
**FIG. 2**



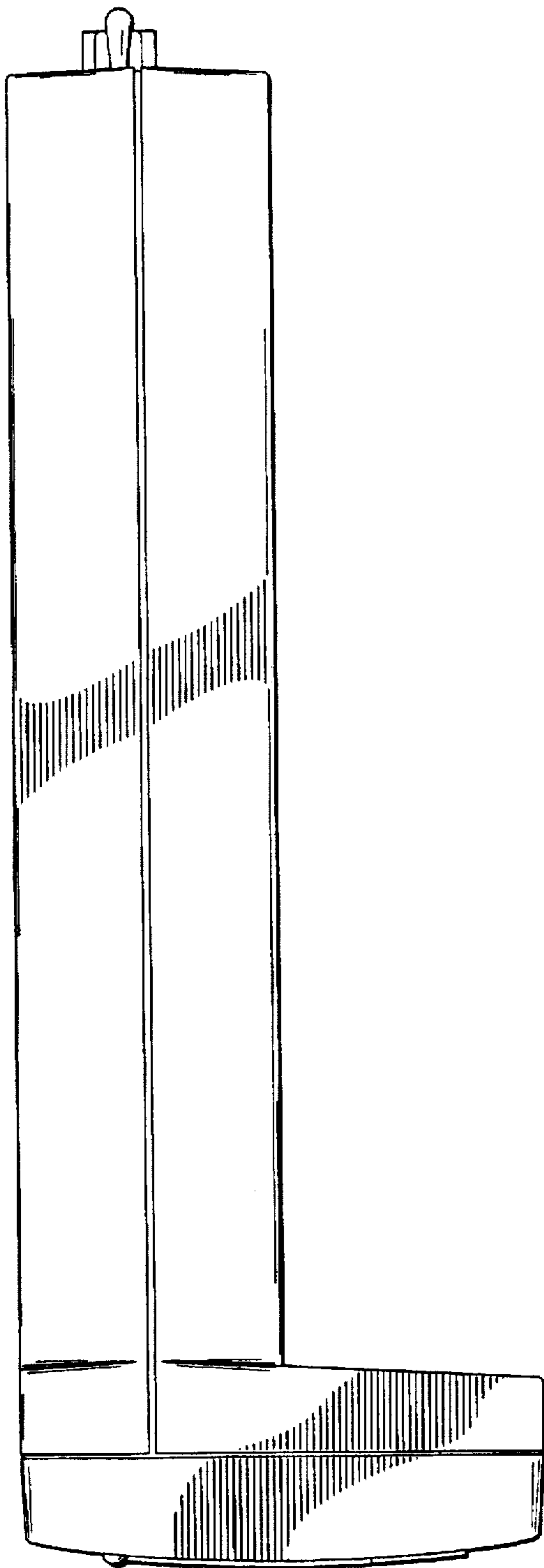
**FIG. 3**



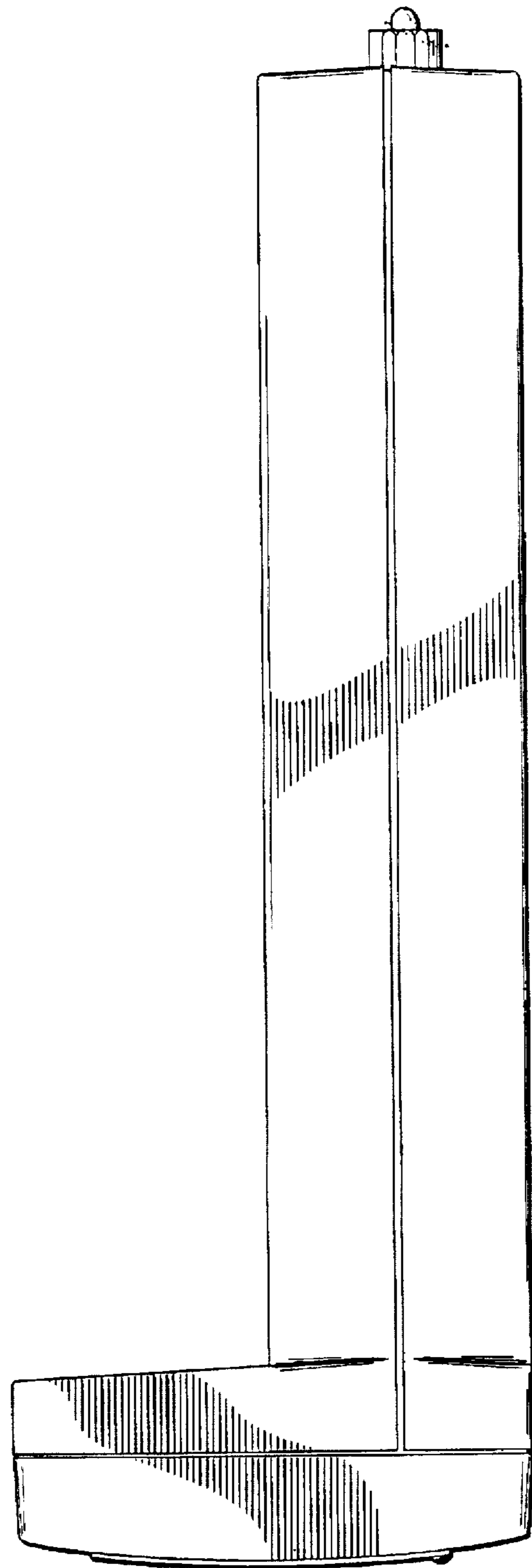
**FIG. 4**



**FIG. 5**



**FIG. 6**



**FIG. 7**