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United States Patent [19]

[11] Patent Number: **Des. 384,649**

Heberling et al.

[45] Date of Patent: ****Oct. 7, 1997**

[54] **MINI-TOWER ENCLOSURE FOR ELECTRONICS DEVICE**

D. 335,489 5/1993 Sander D14/100
D. 360,406 7/1995 Hill et al. D14/100

[75] Inventors: **James R. Heberling**, Lawrenceville; **J. Stephen Wells**, Buford, both of Ga.

Primary Examiner—Freda Nunn
Attorney, Agent, or Firm—Jones & Askew

[73] Assignee: **Hayes Microcomputer Products, Inc.**, Norcross, Ga.

[57] CLAIM

[**] Term: **14 Years**

The ornamental design for a mini-tower enclosure for electronics device, a shown.

[21] Appl. No.: **56,257**

DESCRIPTION

[22] Filed: **Jun. 18, 1996**

FIG. 1 is a perspective view of a mini tower enclosure for electronics device embodying our new design;

[51] **LOC (6) Cl.** **14-02**

FIG. 2 is a front elevational view thereof;

[52] **U.S. Cl.** **D14/102; D14/100**

FIG. 3 is a rear elevational view thereof;

[58] **Field of Search** D14/100, 102, D14/107-109; D13/162, 184, 199; D6/432-436, 445, 448; 312/208.1, 254, 292; 360/99.12; 361/680-695

FIG. 4 is a right side elevational view thereof;

FIG. 5 is a left side elevational view thereof;

FIG. 6 is a top plan view thereof; and,

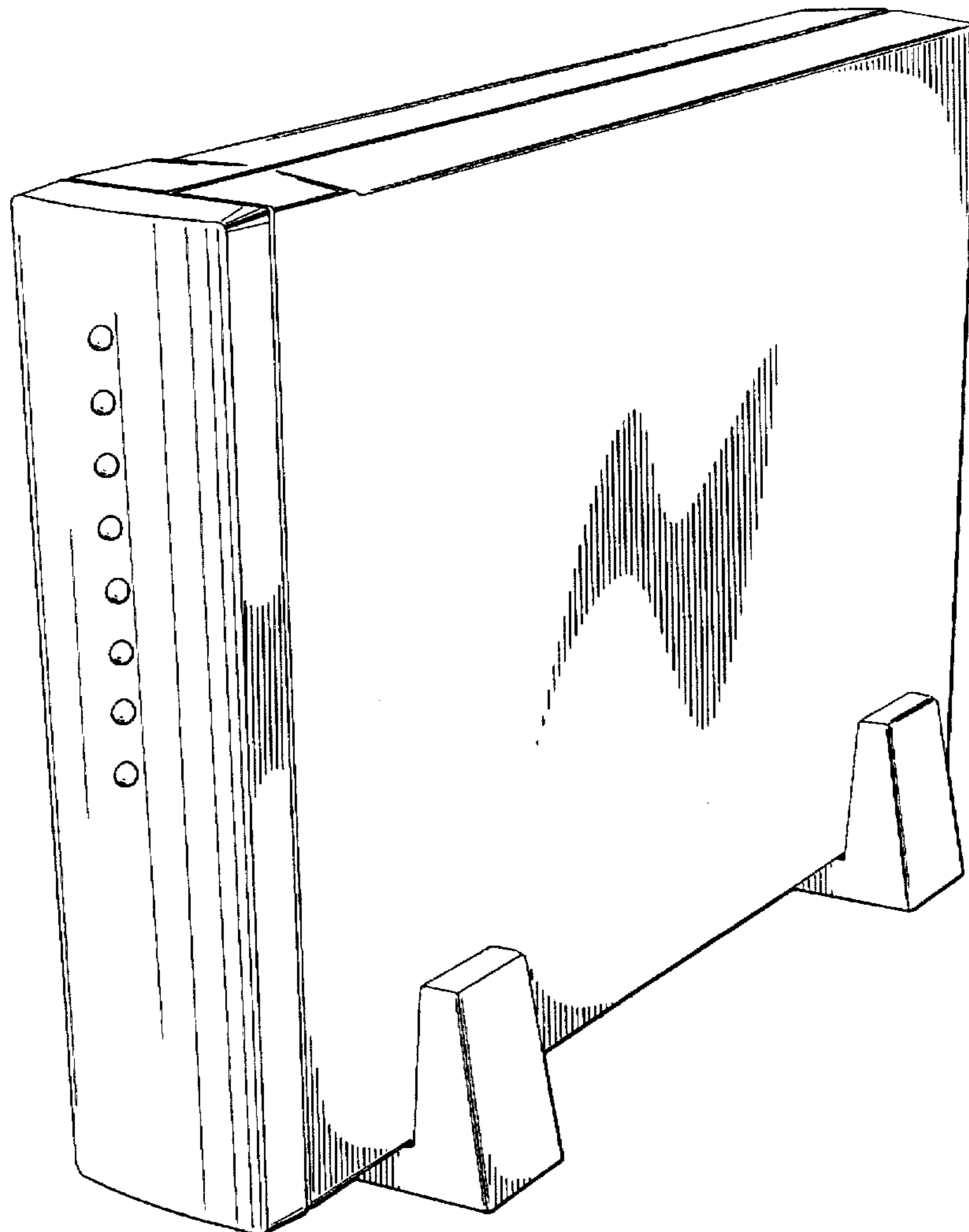
FIG. 7 is a bottom plan view thereof.

[56] References Cited

U.S. PATENT DOCUMENTS

D. 305,753 1/1990 Hoshino D14/102

1 Claim, 5 Drawing Sheets



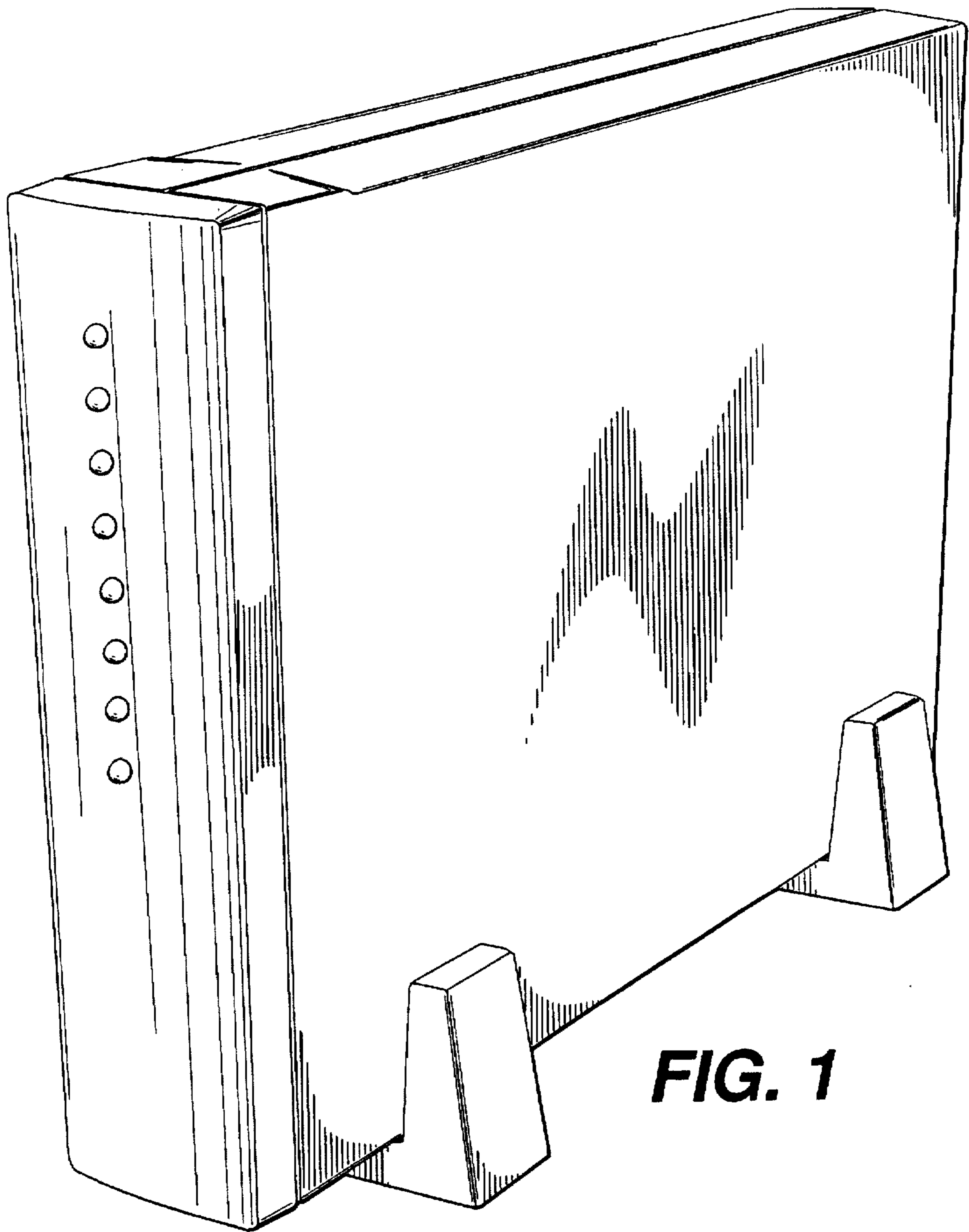


FIG. 1

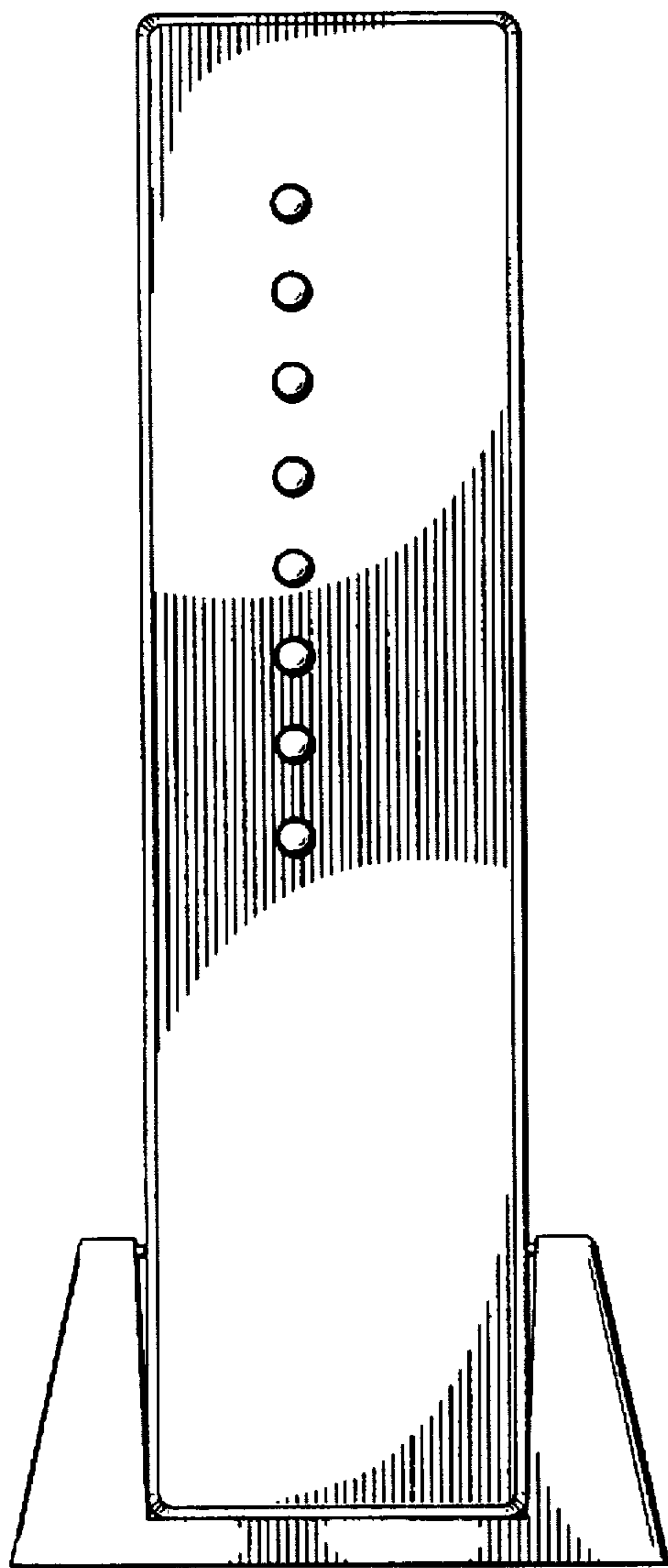


FIG. 2

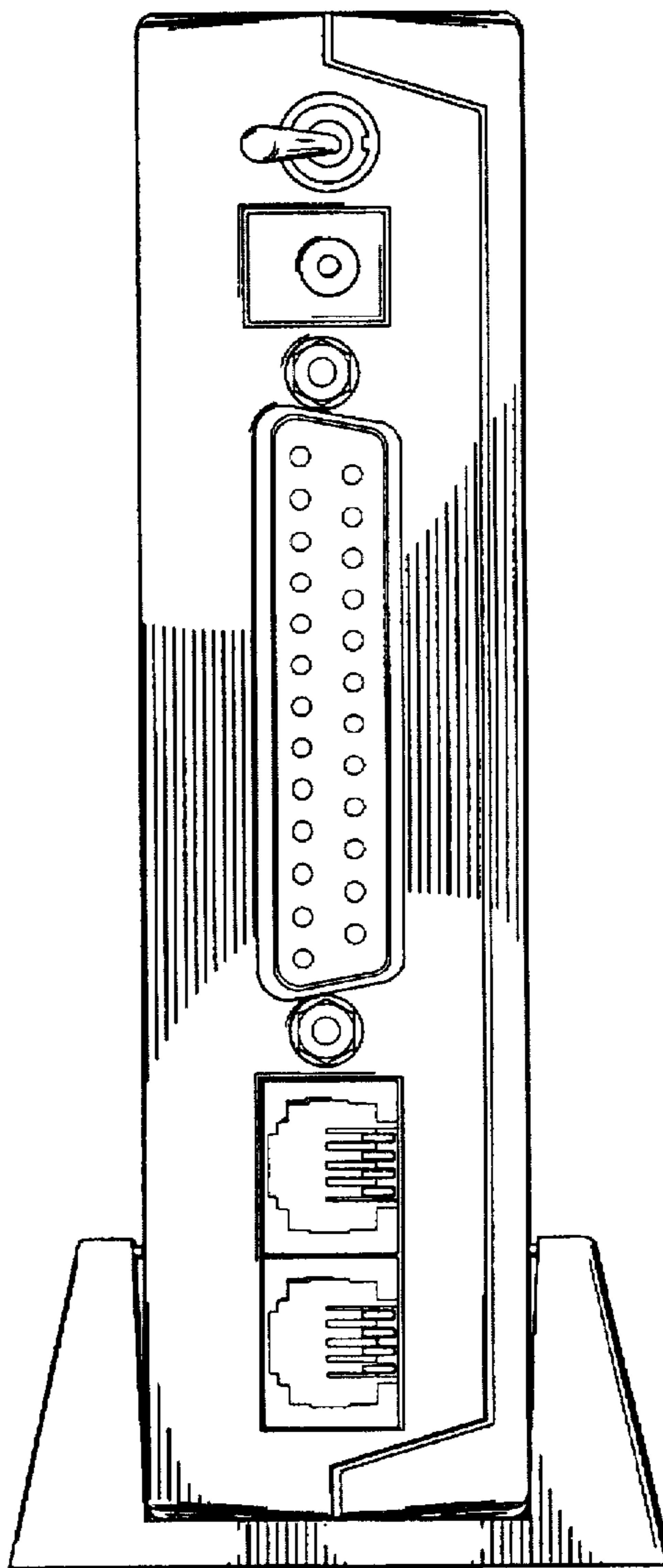


FIG. 3

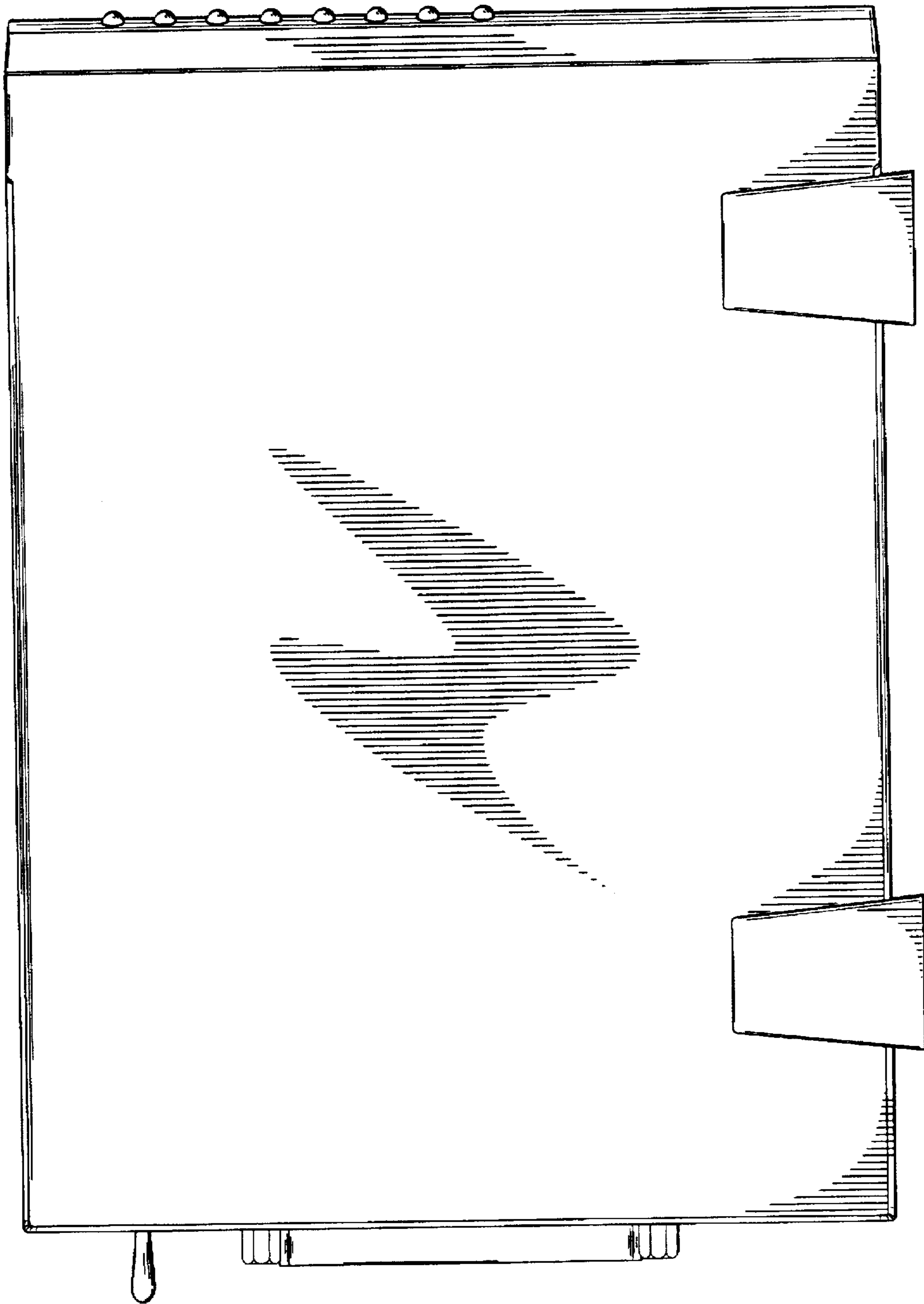


FIG. 4

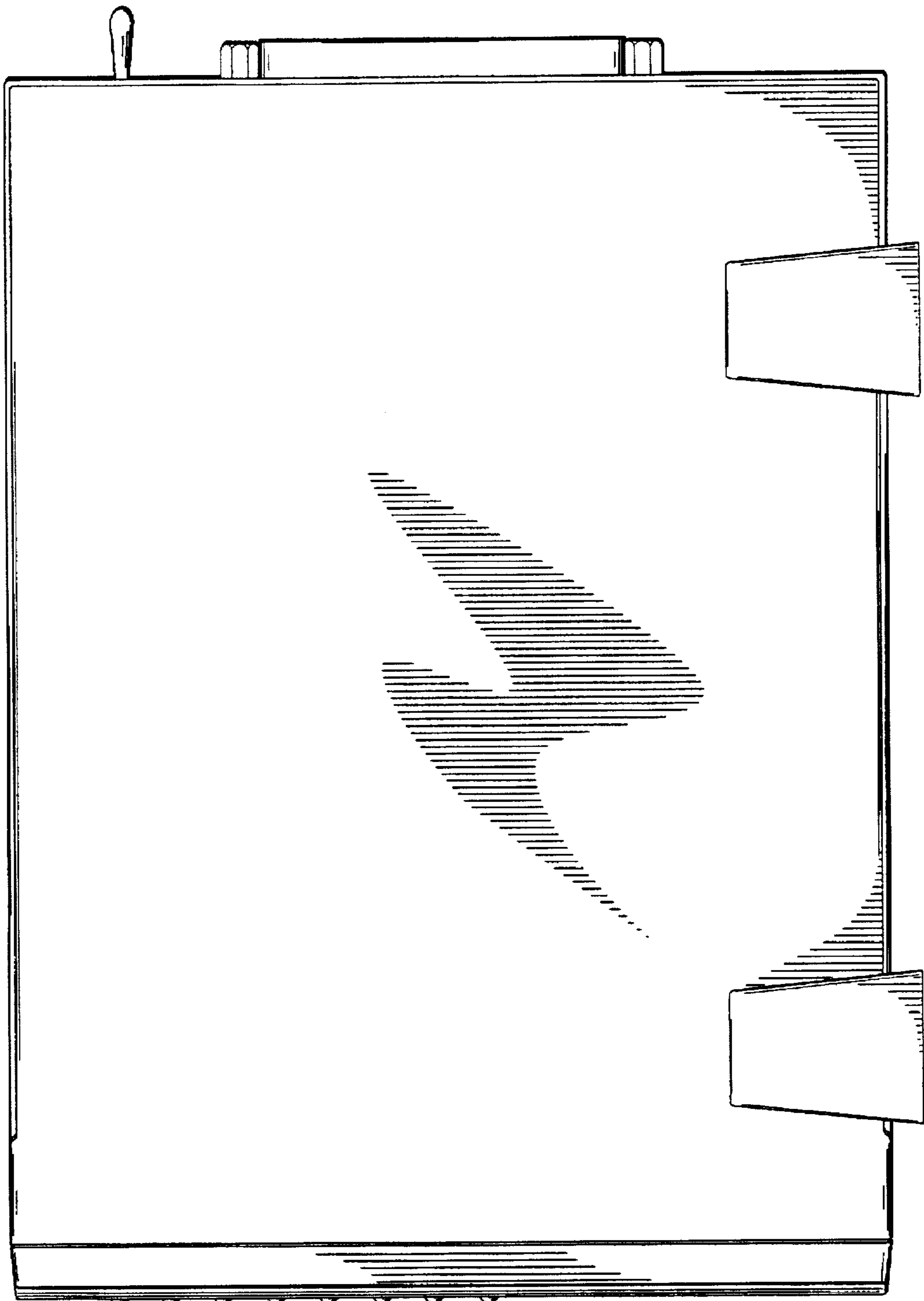


FIG. 5

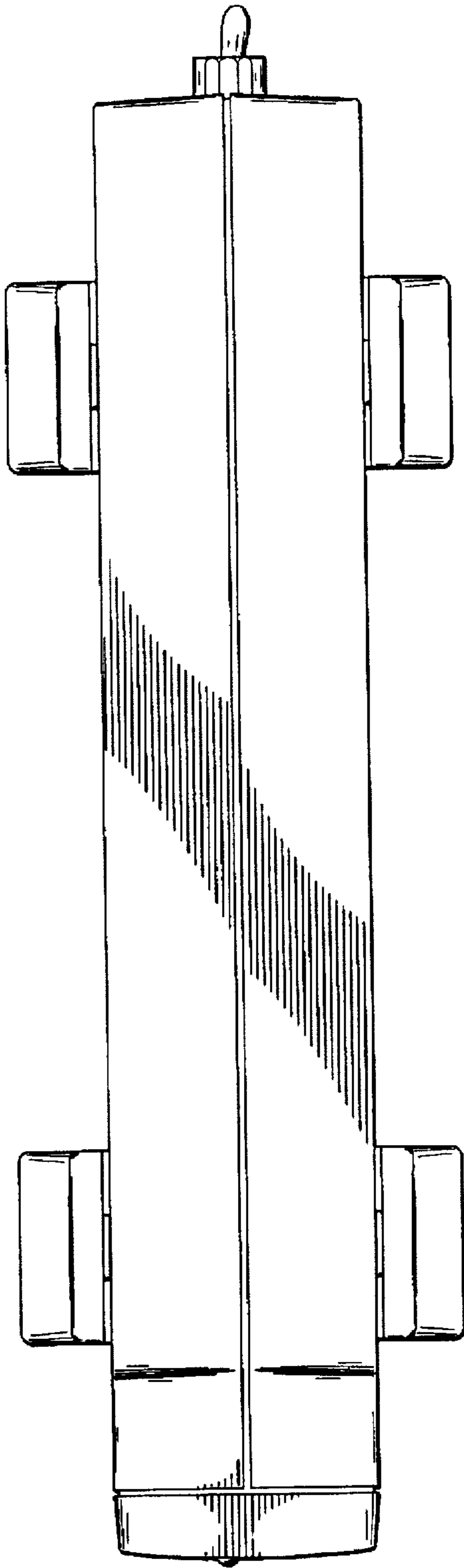


FIG. 6

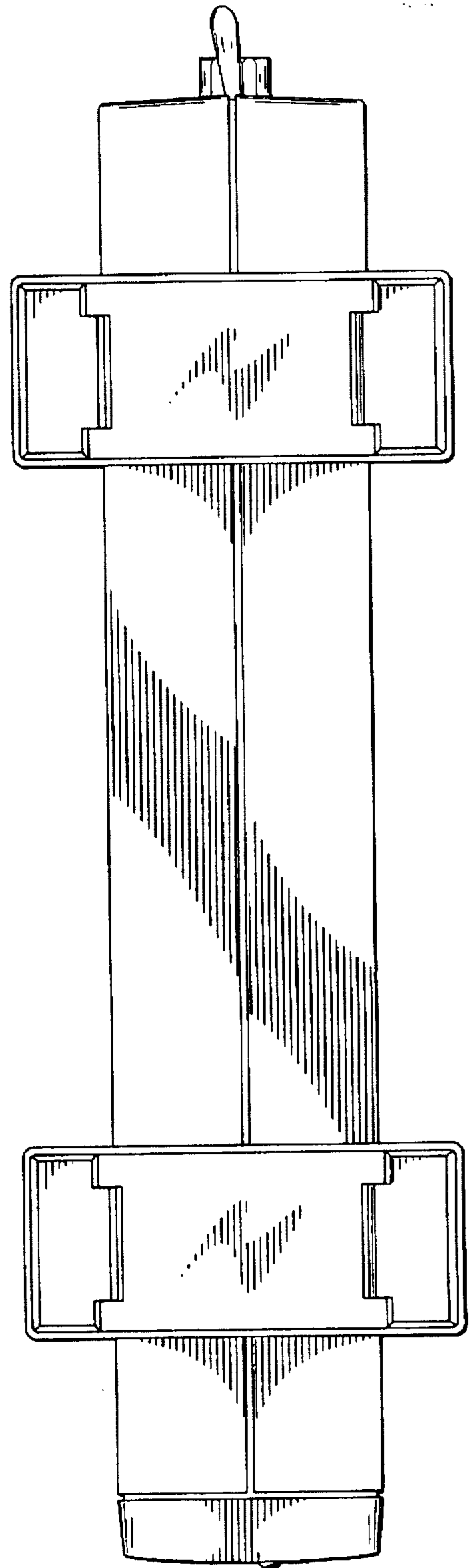


FIG. 7