

US00D384044S

United States Patent [19] Tanimura

[11] Patent Number: **Des. 384,044**
[45] Date of Patent: ****Sep. 23, 1997**

[54] **PORT REPLICATING UNIT FOR A COMPUTER**
[75] Inventor: **Yoichi Tanimura**, Nara-ken, Japan
[73] Assignee: **Sharp Kabushiki Kaisha**, Osaka, Japan
[**] Term: **14 Years**

D. 359,034 6/1995 Kondo D14/107
D. 361,758 8/1995 Youens et al. D14/107
D. 364,853 12/1995 Ojeda D14/107
D. 366,468 1/1996 Ryan et al. D14/107

Primary Examiner—Freda Nunn
Attorney, Agent, or Firm—Nixon & Vanderhye

[21] Appl. No.: **56,388**
[22] Filed: **Jun. 27, 1996**

[57] CLAIM

The ornamental design for a "port replicating unit for a computer", as shown and described.

[30] **Foreign Application Priority Data**
Dec. 27, 1995 [JP] Japan 7-39823
[51] **LOC (6) Cl.** **14-02**
[52] **U.S. Cl.** **D14/107**
[58] **Field of Search** D14/100, 107,
D14/114; D13/184, 199; 361/683-686;
364/708.1; 439/95, 248

DESCRIPTION

FIG. 1 is a top, front and right side perspective view of a port replicating unit for a computer according to my design;
FIG. 2 is a plan view thereof;
FIG. 3 is a front elevational view thereof;
FIG. 4 is a right side elevational view thereof;
FIG. 5 is a rear elevational view thereof;
FIG. 6 is a left side elevational view thereof; and,
FIG. 7 is a bottom plan view thereof.

[56] **References Cited**
U.S. PATENT DOCUMENTS
D. 302,812 8/1989 Hiroki D14/107

1 Claim, 5 Drawing Sheets

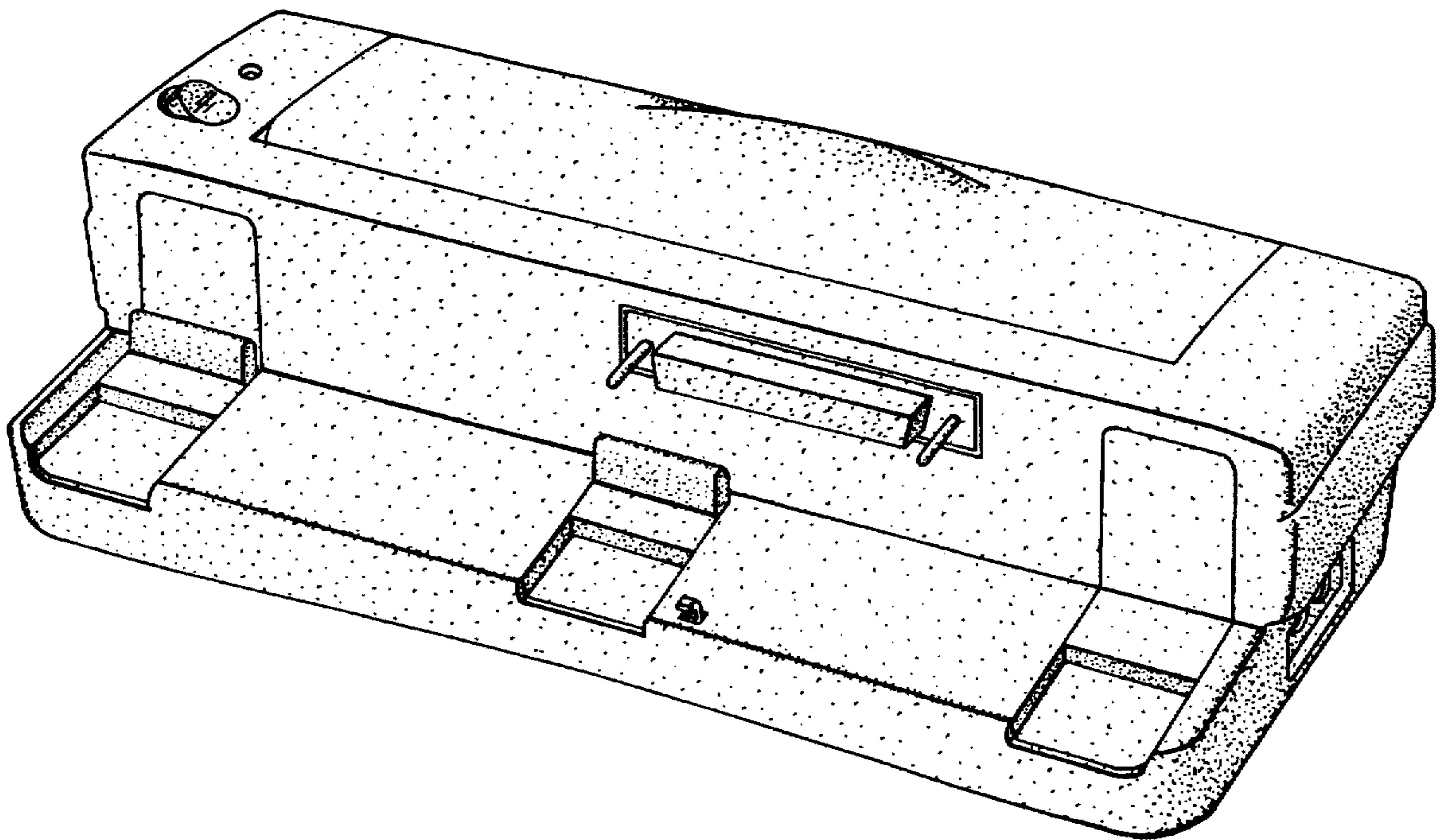


FIG. 1

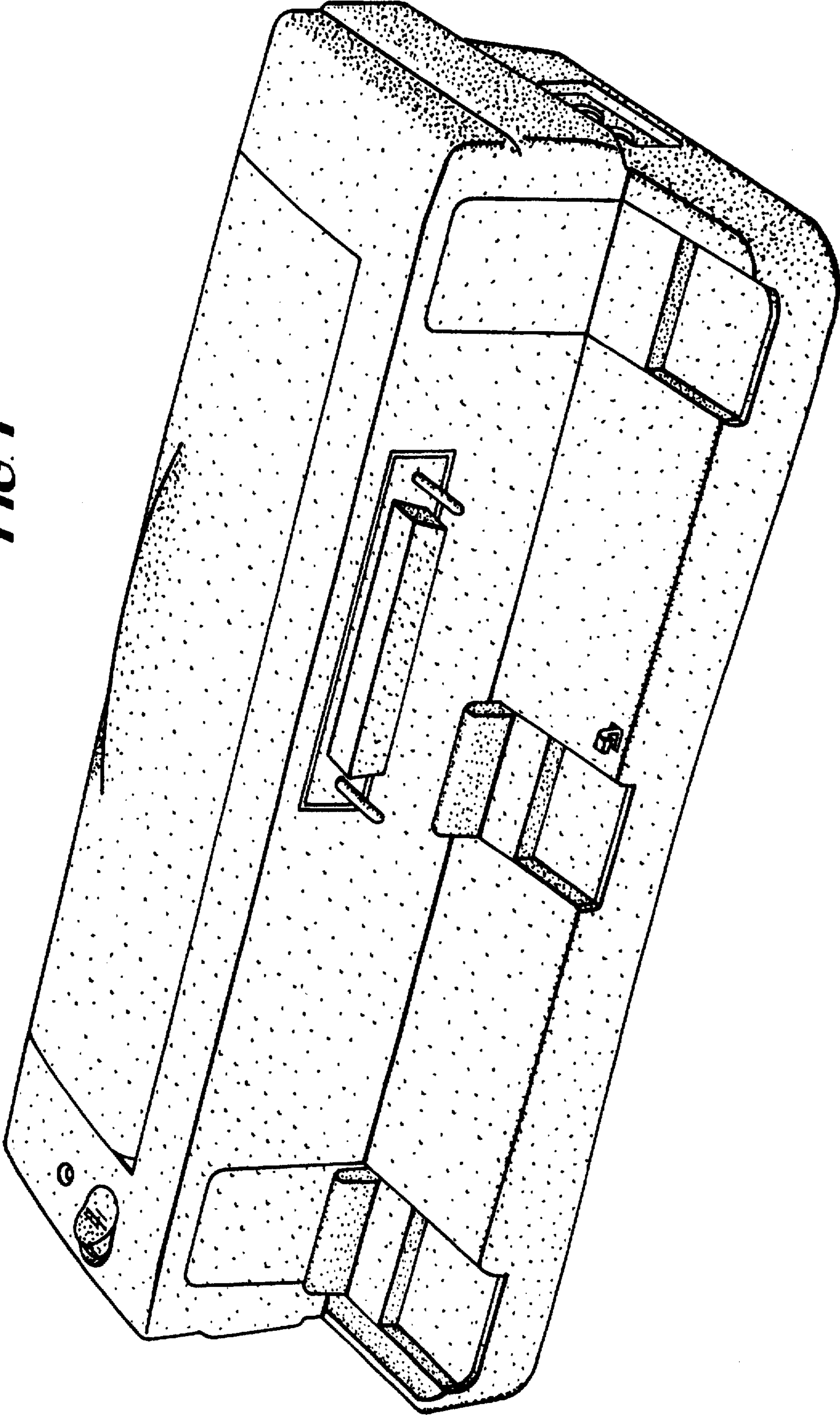


FIG. 2

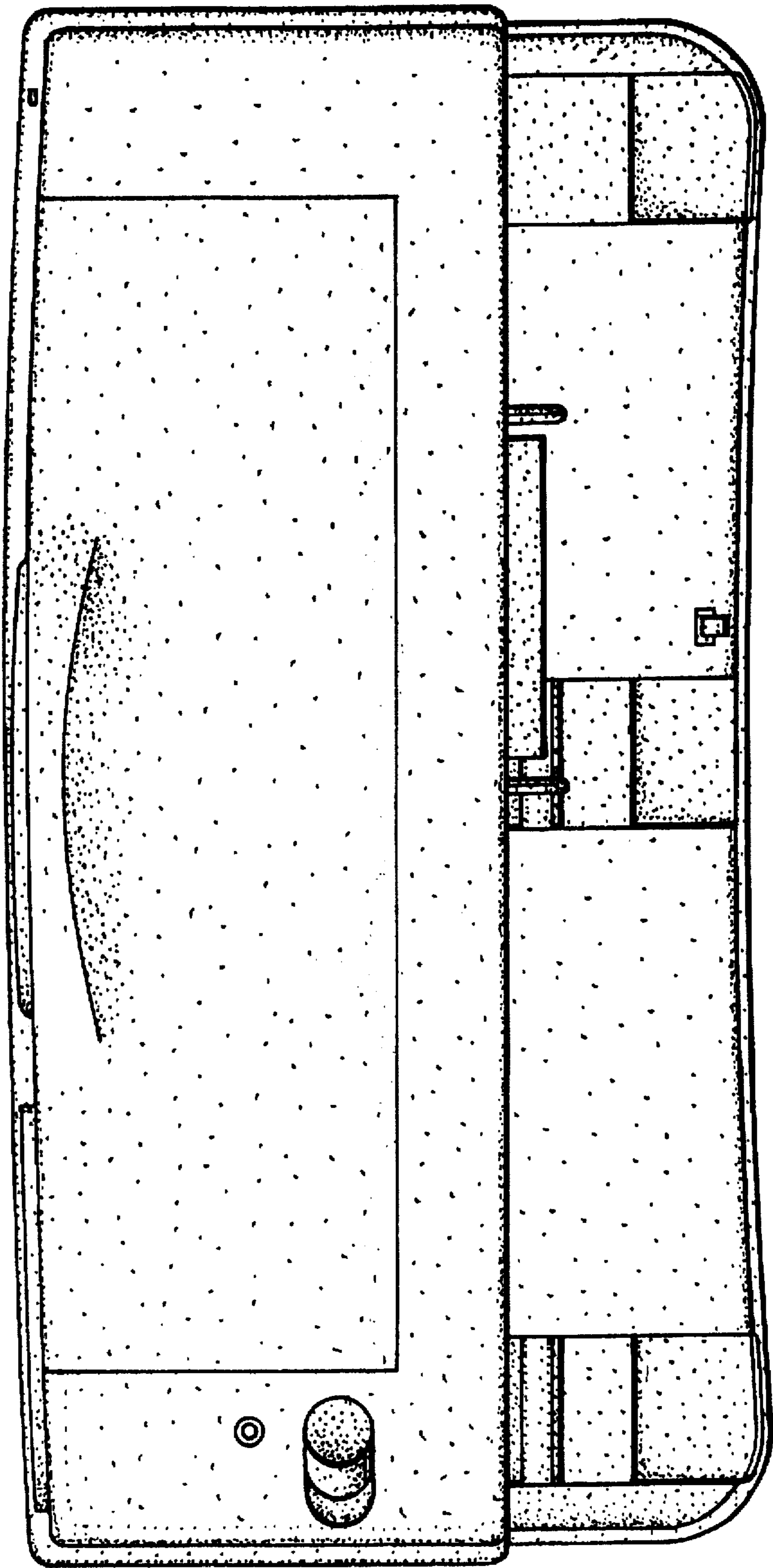


FIG. 3

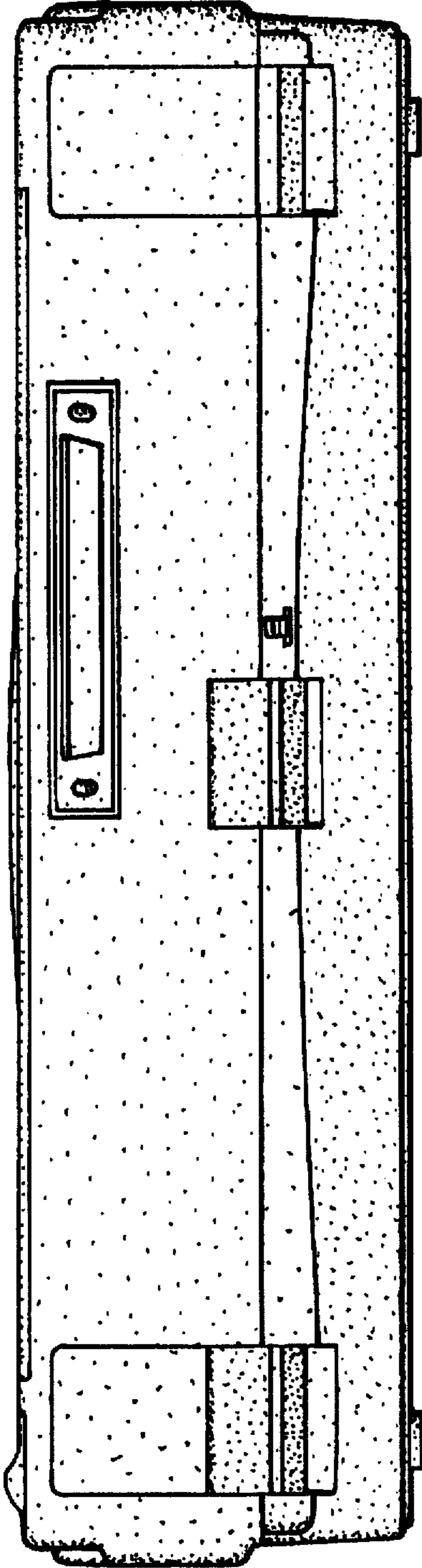


FIG. 4

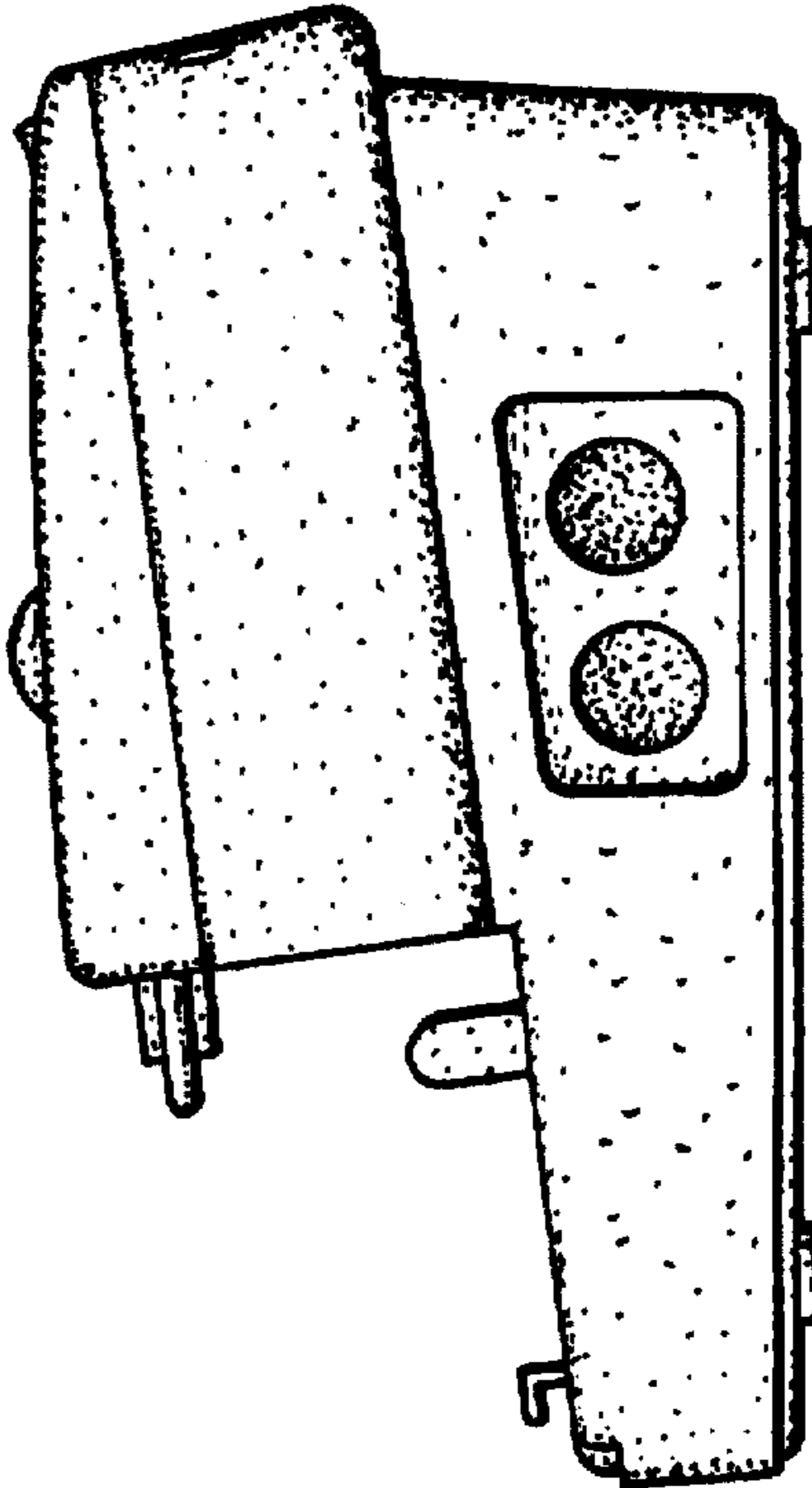


FIG. 5

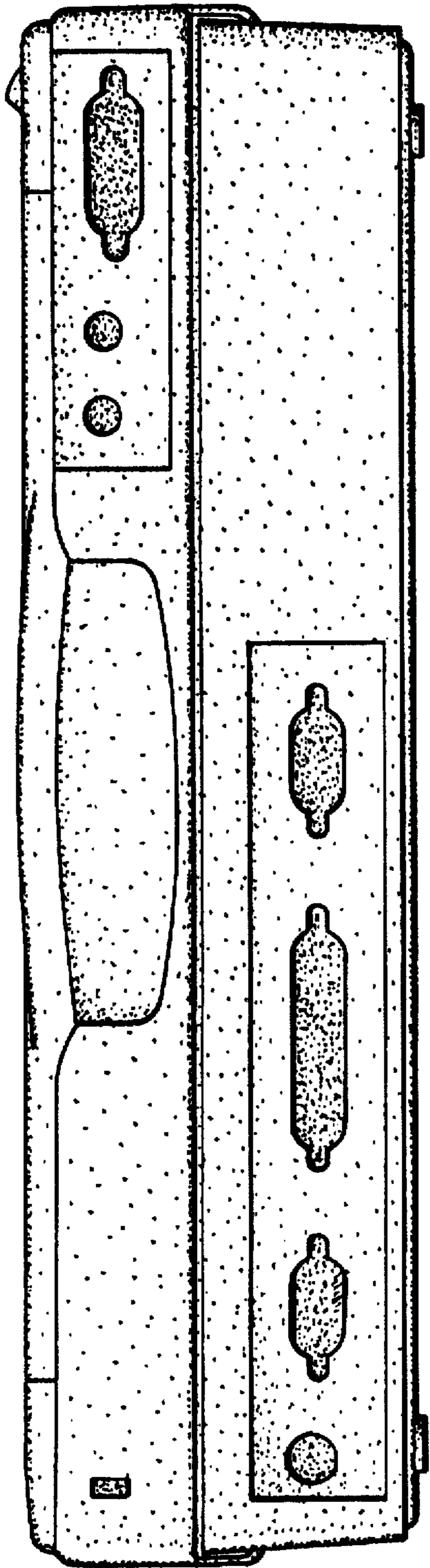


FIG. 6

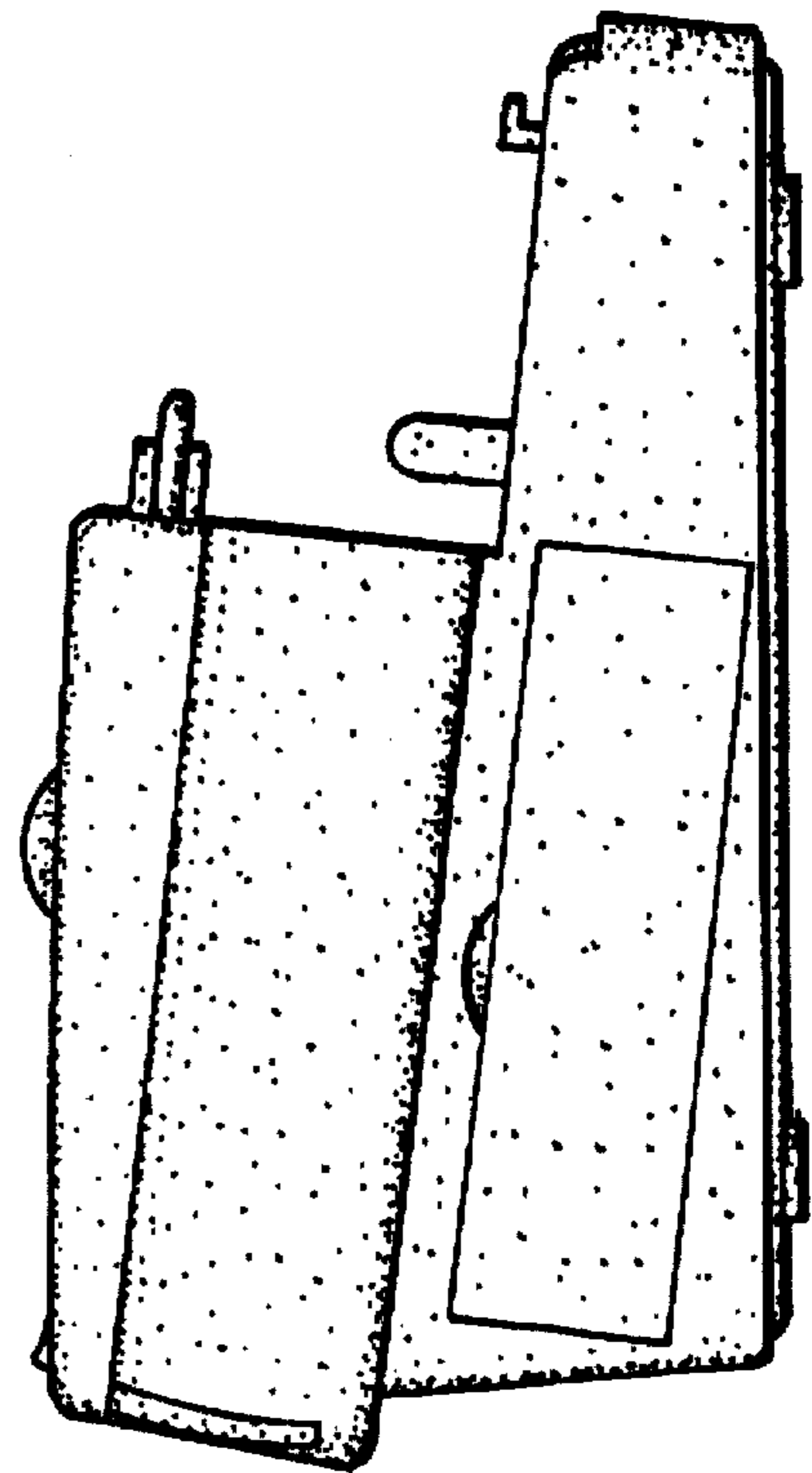


FIG. 7

