



US00D383438S

# United States Patent [19]

Gerber et al.

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[45] Date of Patent: **\*\*Sep. 9, 1997**

[54] **POWER MODULE COVER**

D. 362,661 9/1995 Busdor et al. .... D13/184  
4,950,842 8/1990 Menninga ..... 174/67

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*Primary Examiner*—Doris Clark  
*Attorney, Agent, or Firm*—Jenkins & Gilchrist

[73] Assignee: **Dallas Semiconductor Corp.**, Dallas, Tex.

[57] **CLAIM**

[\*\*] Term: **14 Years**

The ornamental design for the power module cover, as shown and described.

[21] Appl. No.: **51,306**

**DESCRIPTION**

[22] Filed: **Mar. 6, 1996**

[51] LOC (6) Cl. .... **13-99**

[52] U.S. Cl. .... **D13/118**

[58] Field of Search ..... D13/118, 156,  
D13/162, 177, 184; 174/55, 66, 67

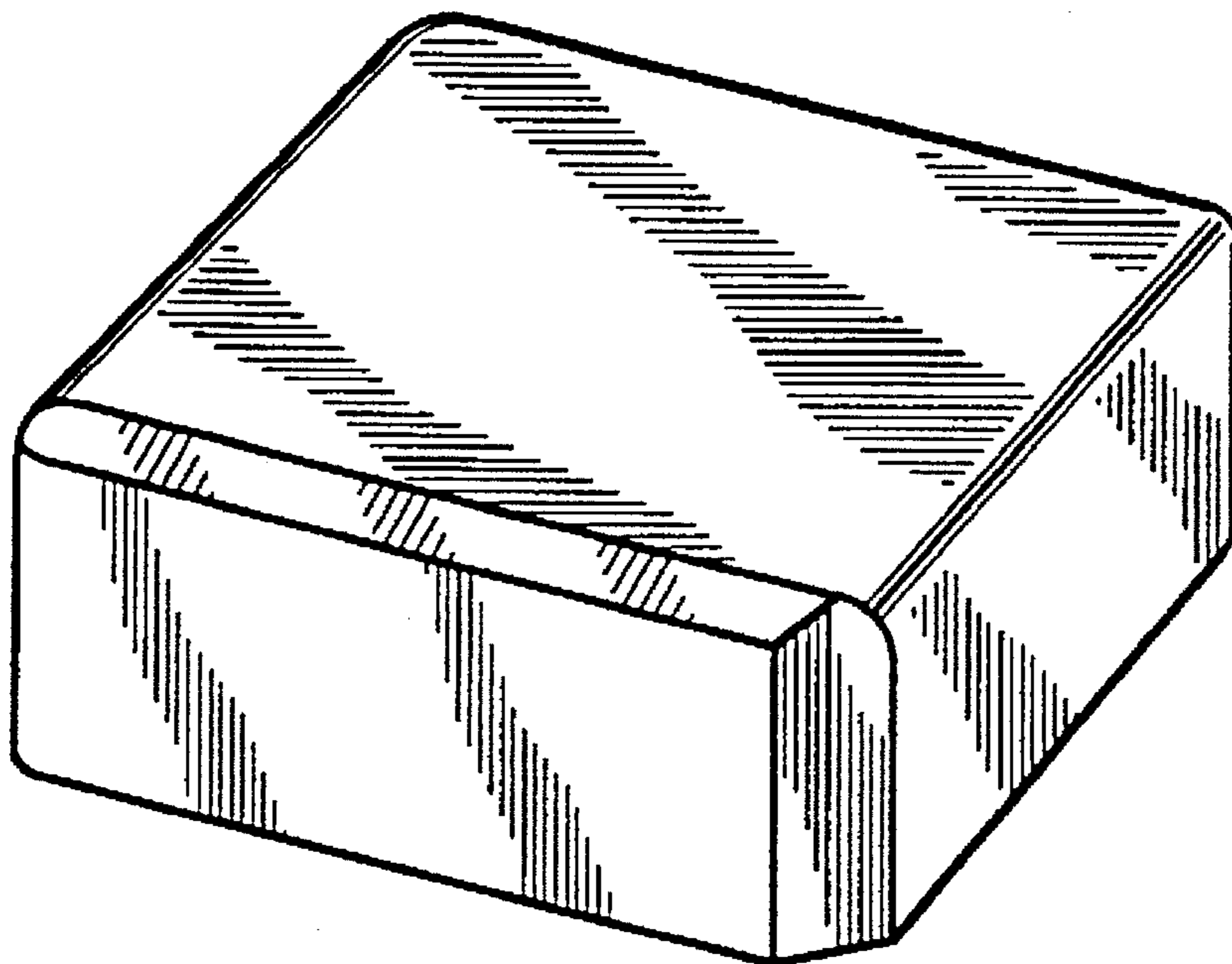
FIG. 1 is a bottom plan view of the power module cover;  
FIG. 2 is a top plan view of the power module cover;  
FIG. 3 is a rear elevational view of the power module cover;  
FIG. 4 is a side elevational view of the power module cover taken from the left side thereof;  
FIG. 5 is a side elevational view of the power module cover taken from the right side thereof;  
FIG. 6 is a front elevational view of the power module cover; and,  
FIG. 7 is a perspective view of the power module cover.

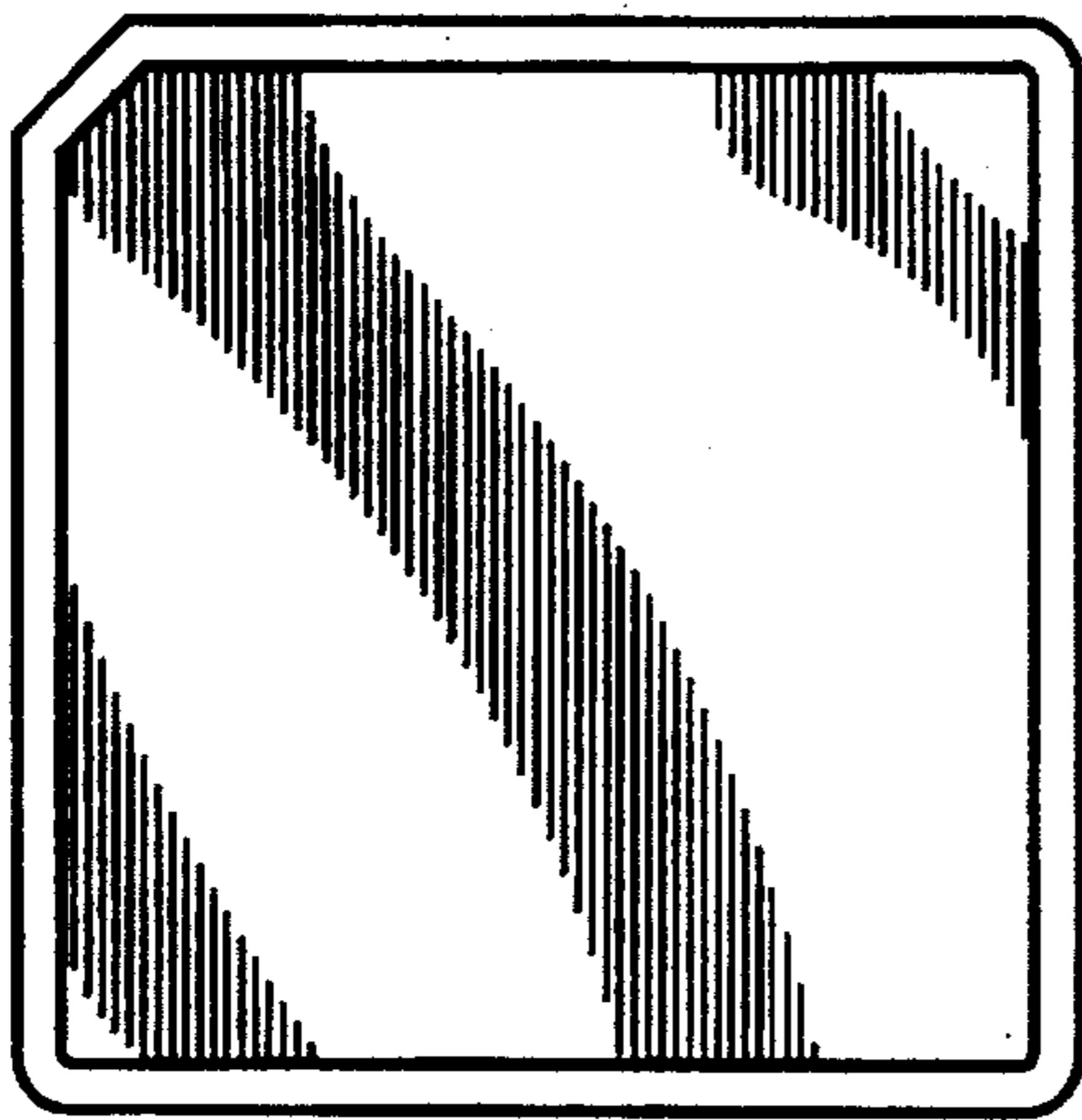
[56] **References Cited**

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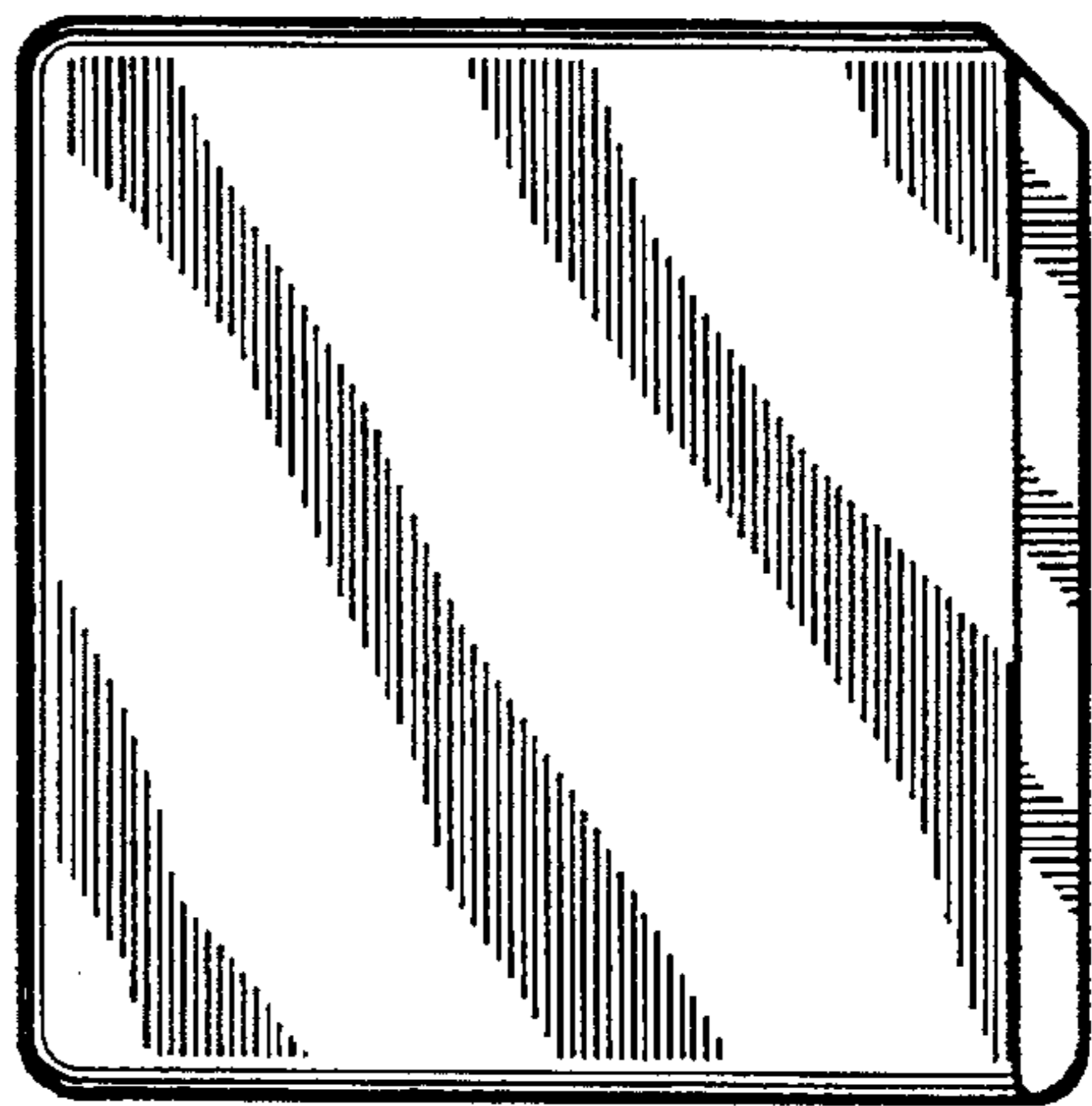
D. 311,176 10/1990 Hill et al. .... D13/184  
D. 325,902 5/1992 Hudson et al. .... D13/184

**1 Claim, 1 Drawing Sheet**

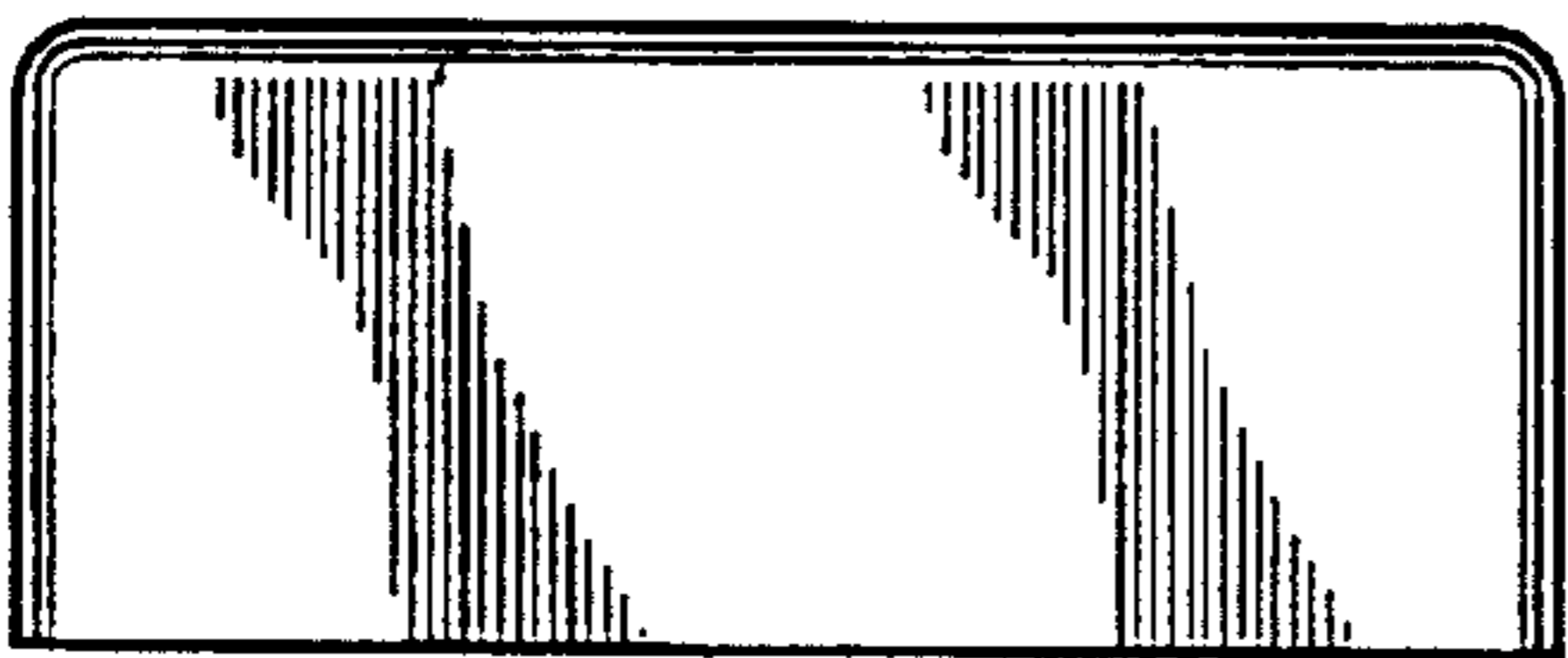




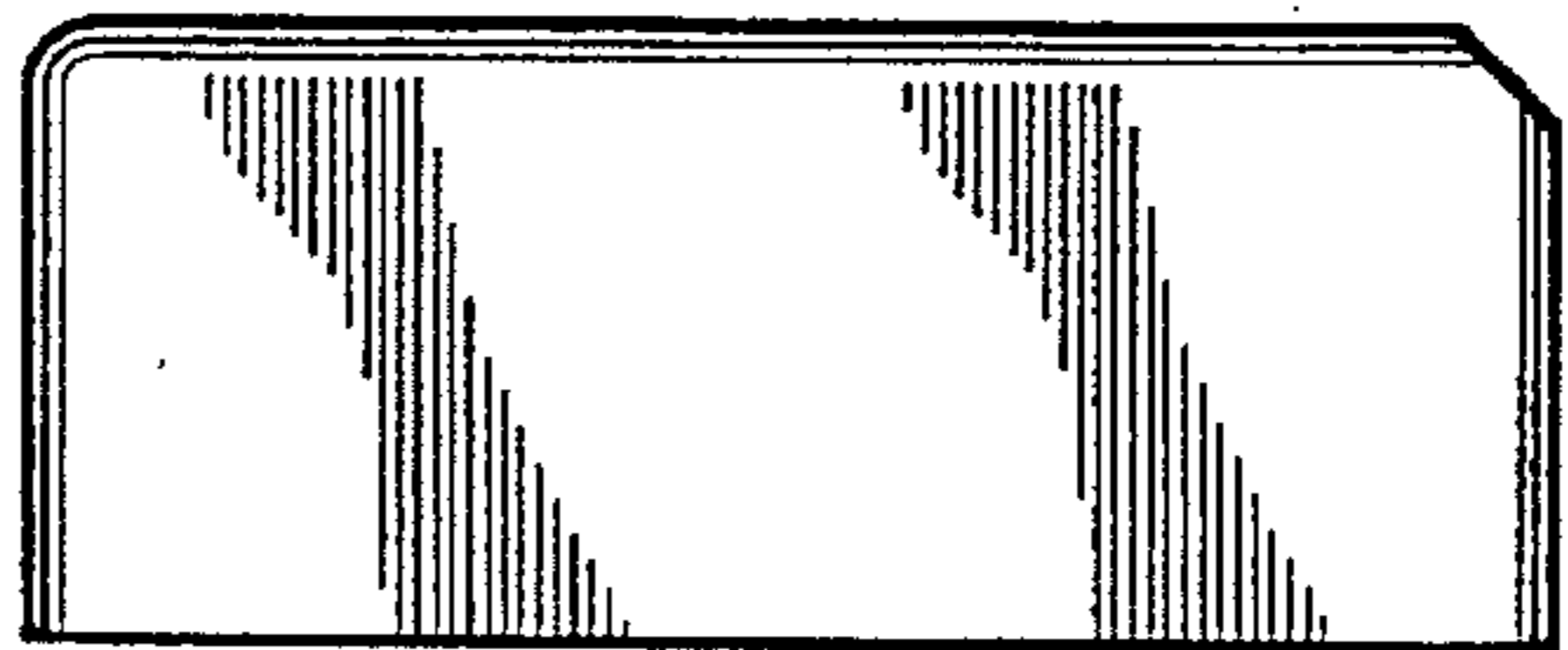
**Fig.1**



**Fig.2**



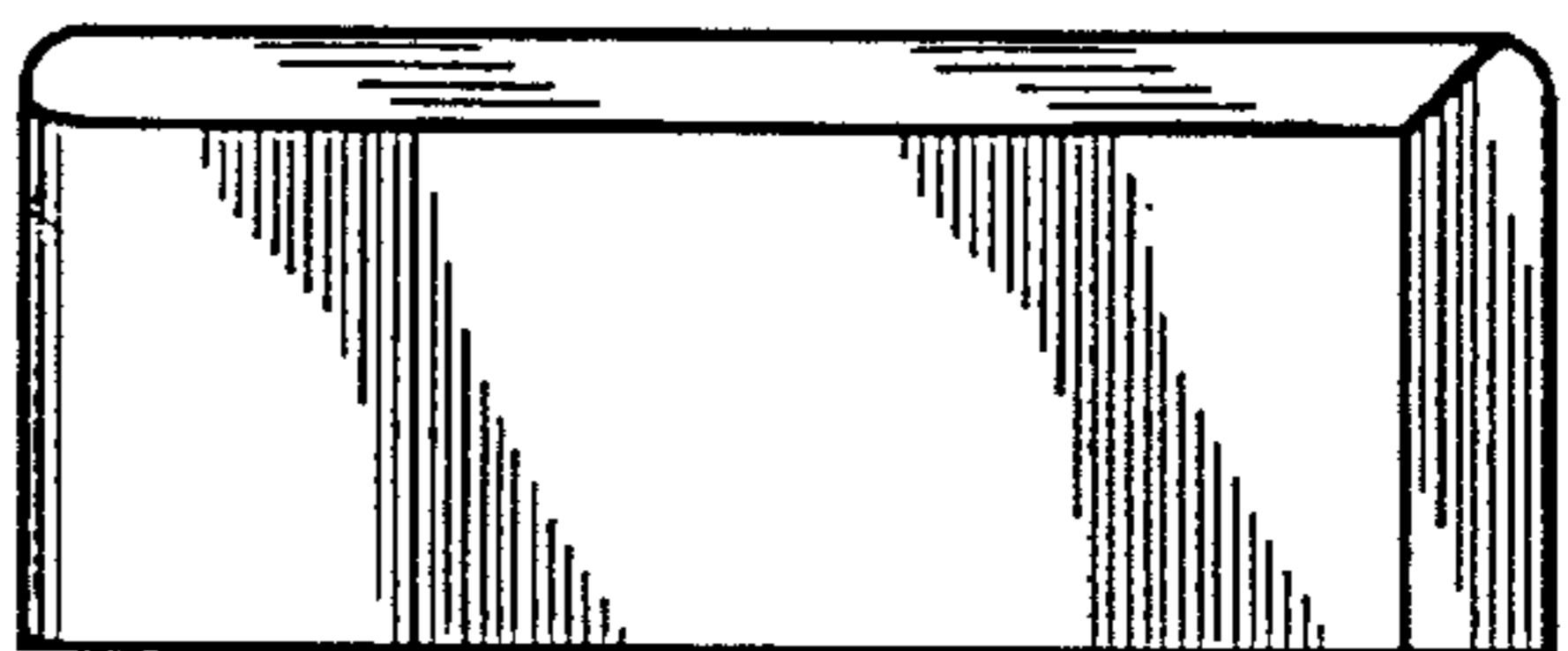
**Fig.3**



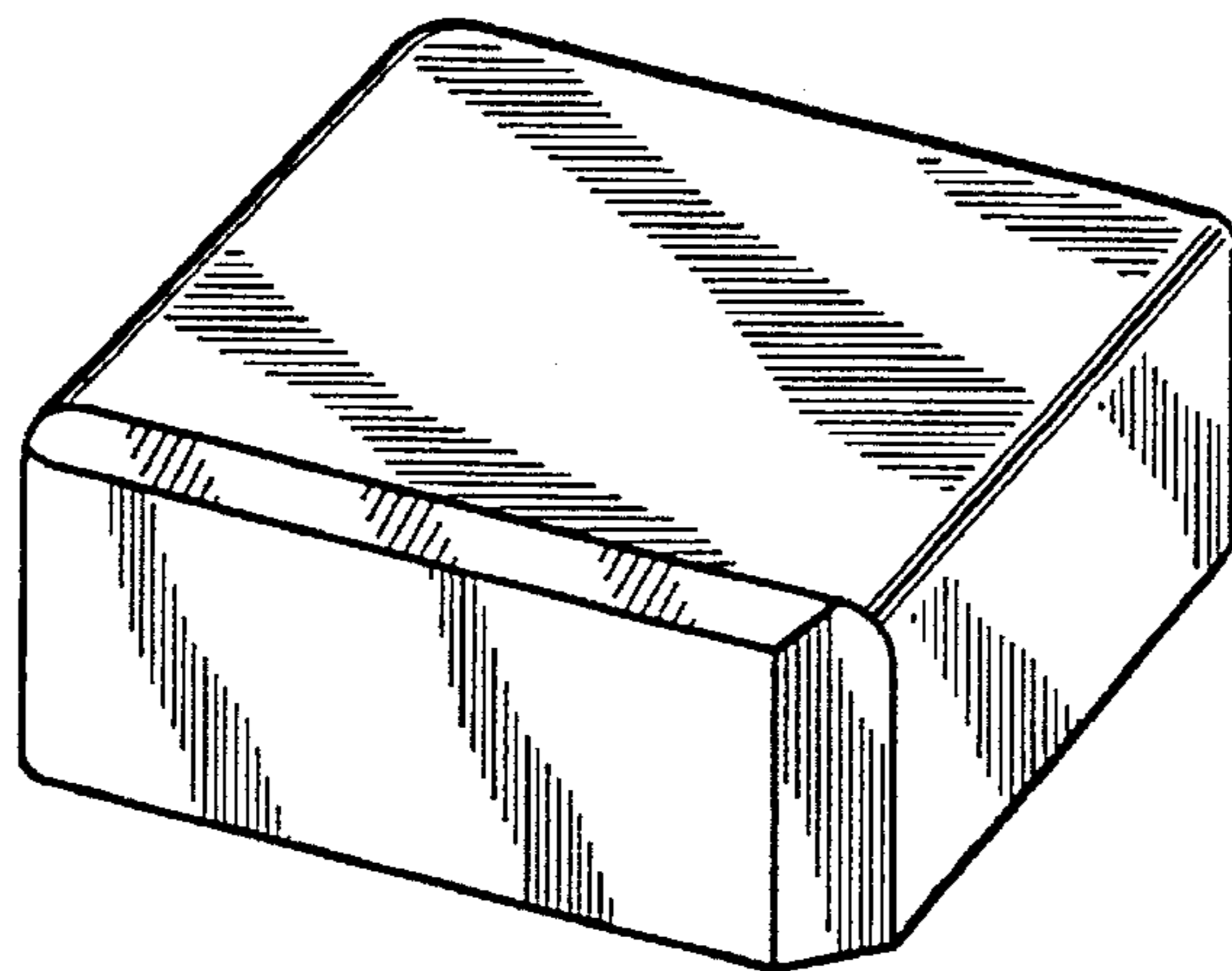
**Fig.4**



**Fig.5**



**Fig.6**



**Fig.7**