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United States Patent [19]

Schroeder

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[45] Date of Patent: **Jul. 8, 1997

[54] **CHIP SWITCH ASSEMBLY UNIT**

4,924,213 5/1990 Decho et al. 340/653
5,501,605 3/1996 Ozaki et al. 439/724 X

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[**] Term: **14 Years**

[57] **CLAIM**

[21] Appl. No.: **43,269**

The ornamental design for a chip switch assembly unit, as shown.

[22] Filed: **Aug. 30, 1995**

[51] **LOC (6) Cl.** **13-03**

[52] **U.S. Cl.** **D13/146**

[58] **Field of Search** D13/146, 147,
D13/151; 361/823; 439/709, 721, 723,
724

DESCRIPTION

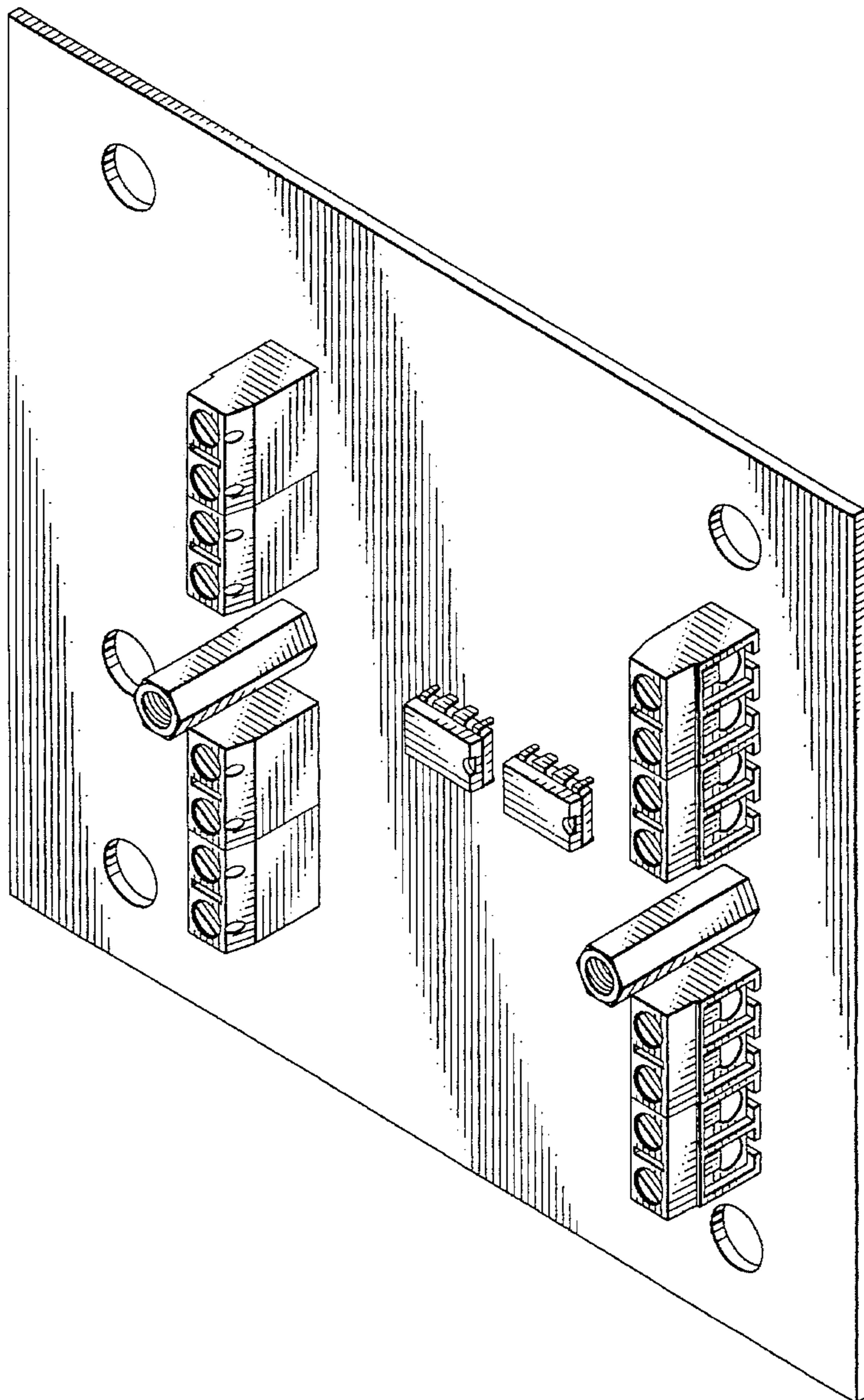
FIG. 1 is a perspective view of chip switch assembly unit;
FIG. 2 is a front view;
FIG. 3 is a rear view;
FIG. 4 is a top view; and,
FIG. 5 is a side view.

[56] **References Cited**

U.S. PATENT DOCUMENTS

D. 352,701 11/1994 Evans D13/182

1 Claim, 3 Drawing Sheets



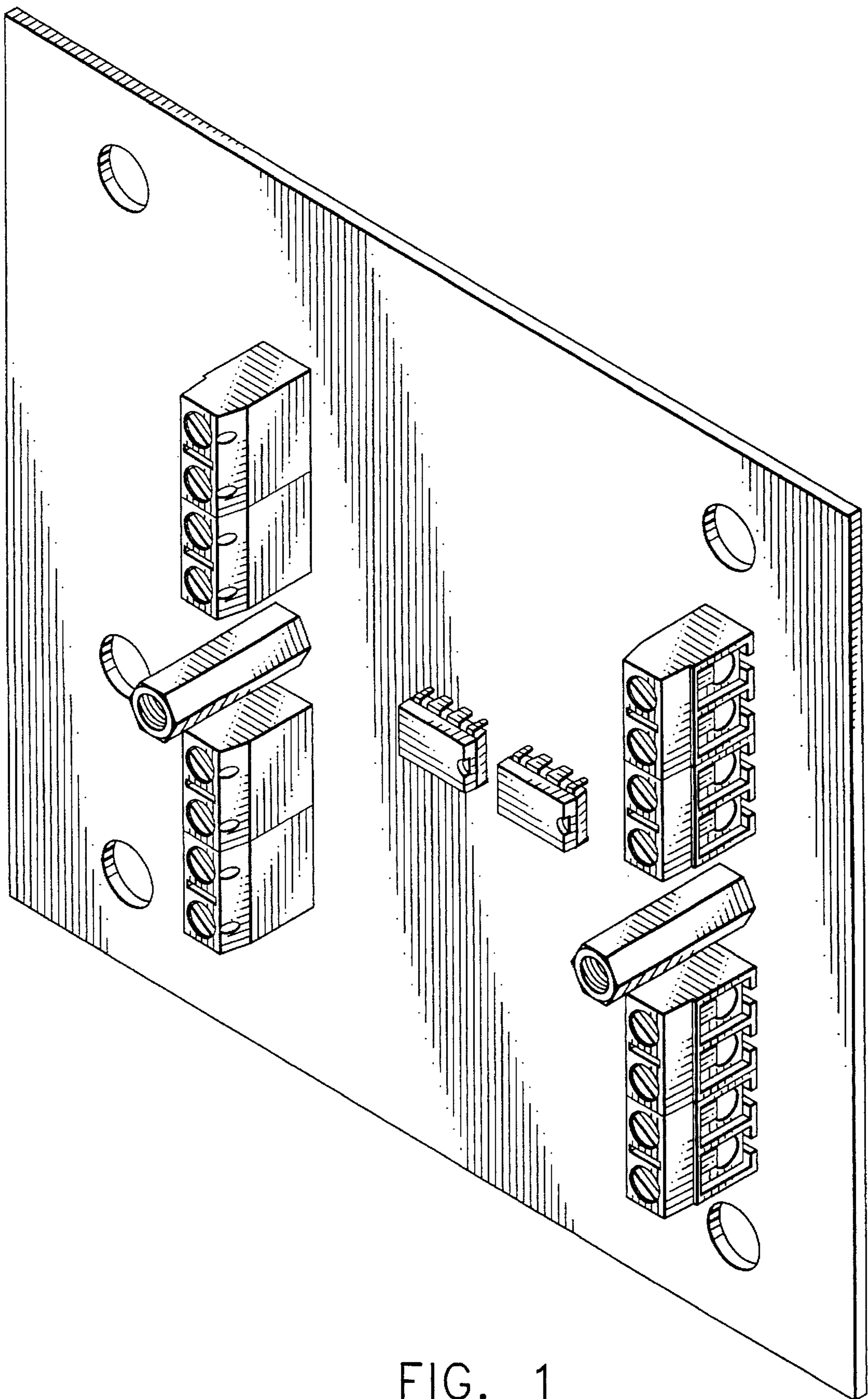


FIG. 1

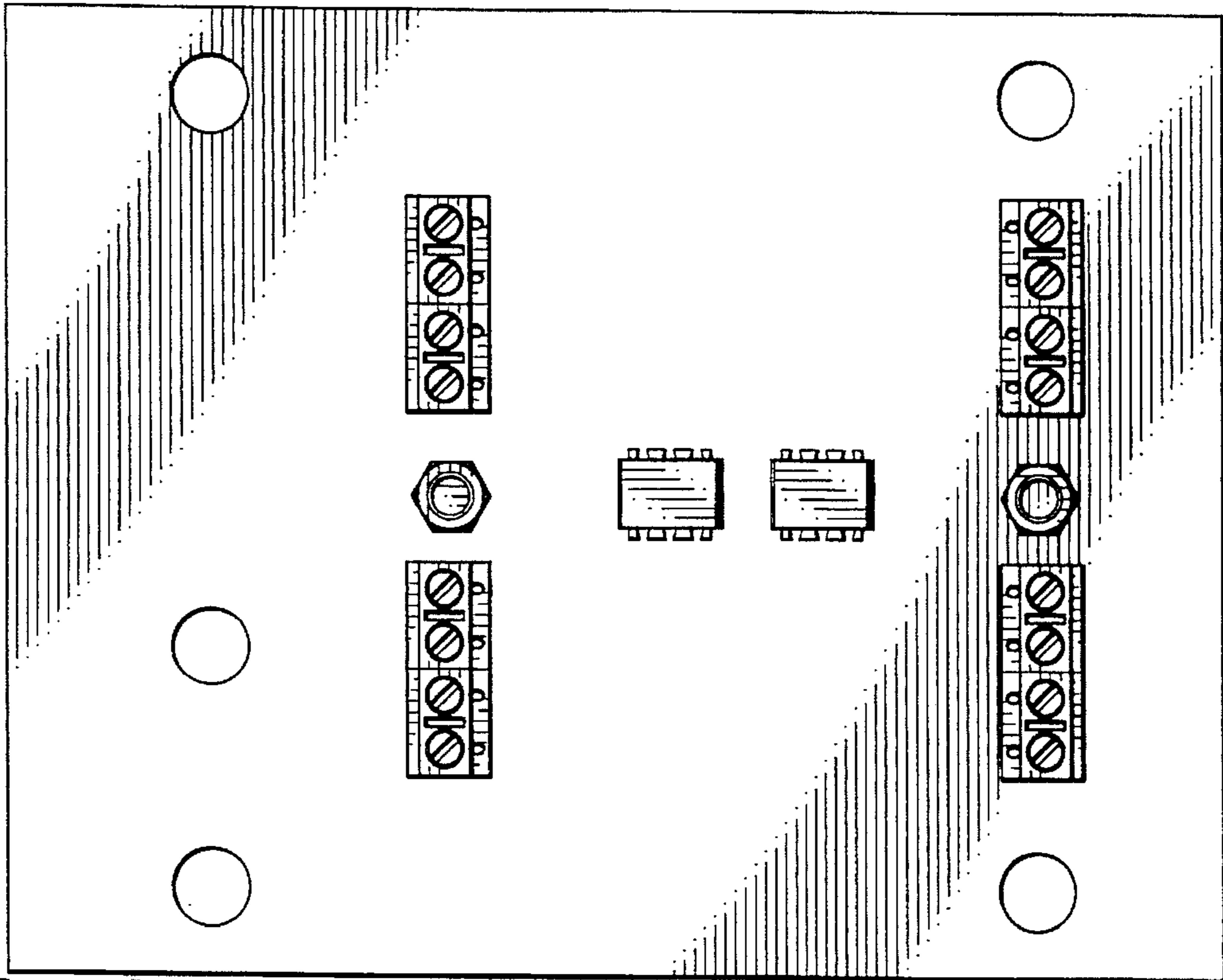


FIG. 2

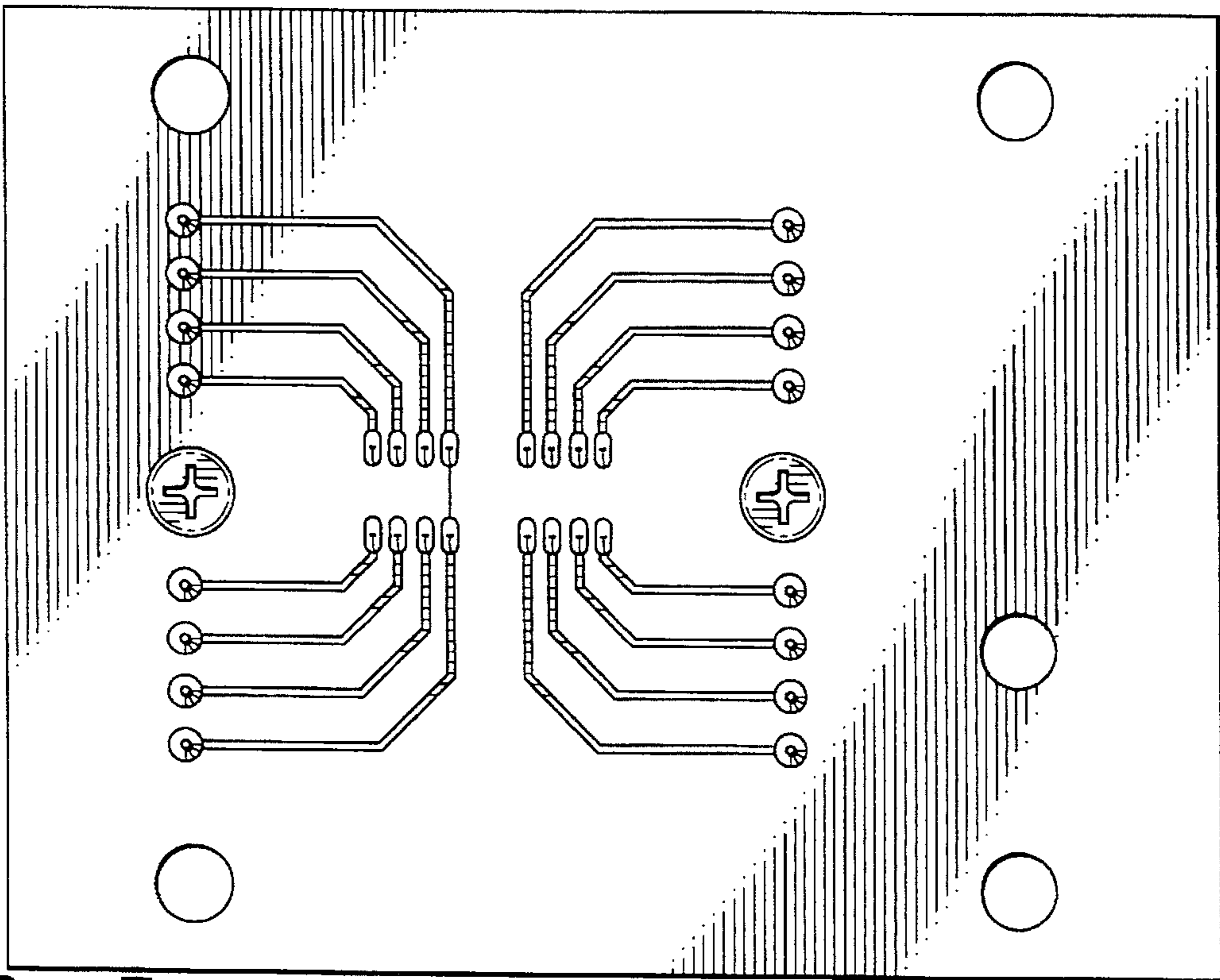


FIG. 3

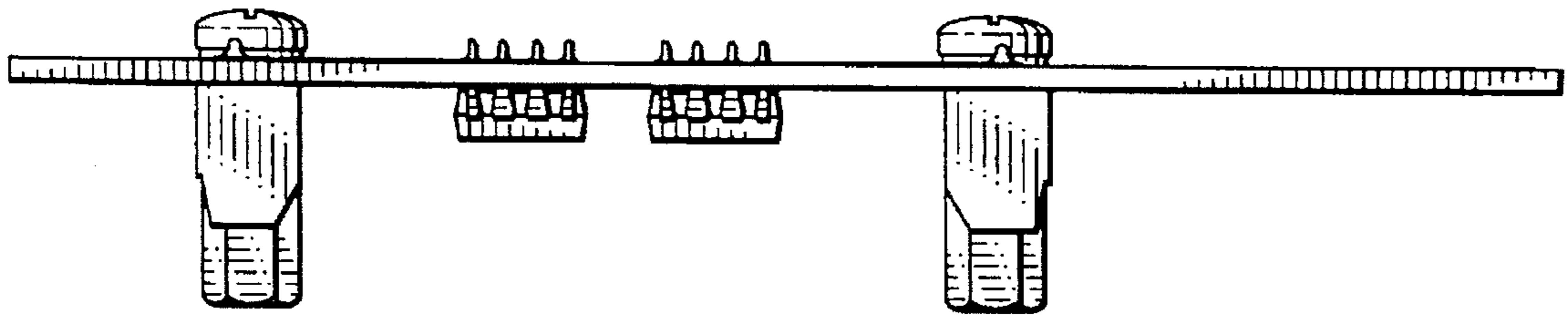


FIG. 4

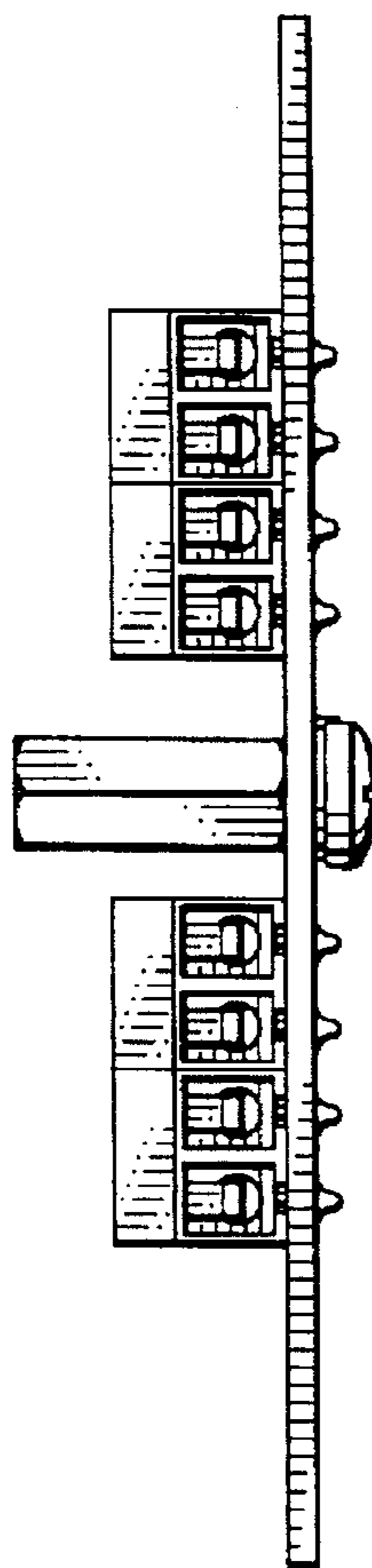


FIG. 5