



US00D380663S

**United States Patent** [19]  
**Nakamura**

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[45] **Date of Patent:** **\*\*Jul. 8, 1997**

[54] **SPACER**  
[75] **Inventor:** **Toshinobu Nakamura**, Tokyo, Japan  
[73] **Assignee:** **Shinagawa Shoko Co., Ltd.**, Tokyo, Japan  
[\*\*] **Term:** **14 Years**

D. 245,477 8/1977 Mullenax ..... D8/354 X  
D. 342,968 1/1994 Meinke ..... D19/55  
4,901,402 2/1990 Begemann ..... 24/129 D  
5,008,777 4/1991 Burns ..... 174/138 G X

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[22] **Filed:** **Nov. 7, 1995**

[57] **CLAIM**  
The ornamental design for spacer, as shown and described.

[30] **Foreign Application Priority Data**  
May 23, 1995 [JP] Japan ..... 7-14225  
[51] **LOC (6) Cl.** ..... **08-05**  
[52] **U.S. Cl.** ..... **D8/354**  
[58] **Field of Search** ..... D8/349, 354, 356,  
D8/382, 383, 394-396; D19/55, 81, 83;  
174/138 D, 138 E, 138 G; 313/341; D34/38;  
108/56.3, 53.1, 53.5, 55.3; 24/129 D; D13/153,  
155, 129

**DESCRIPTION**

FIG. 1 is a perspective view of spacer embodying the present design;  
FIG. 2 is a front view thereof;  
FIG. 3 is a top plan view thereof;  
FIG. 4 is a left side view thereof.  
FIG. 5 is a sectional view taken along line 5—5 of FIG. 2; and,  
FIG. 6 is an environmental perspective view with the broken line showing of a light emitting diode being for illustrative purposes only and forming no part of the claimed design.  
A right side view is a mirror image of the left side view.  
A bottom view is a mirror image of the top plan view.

[56] **References Cited**  
**U.S. PATENT DOCUMENTS**

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**1 Claim, 2 Drawing Sheets**

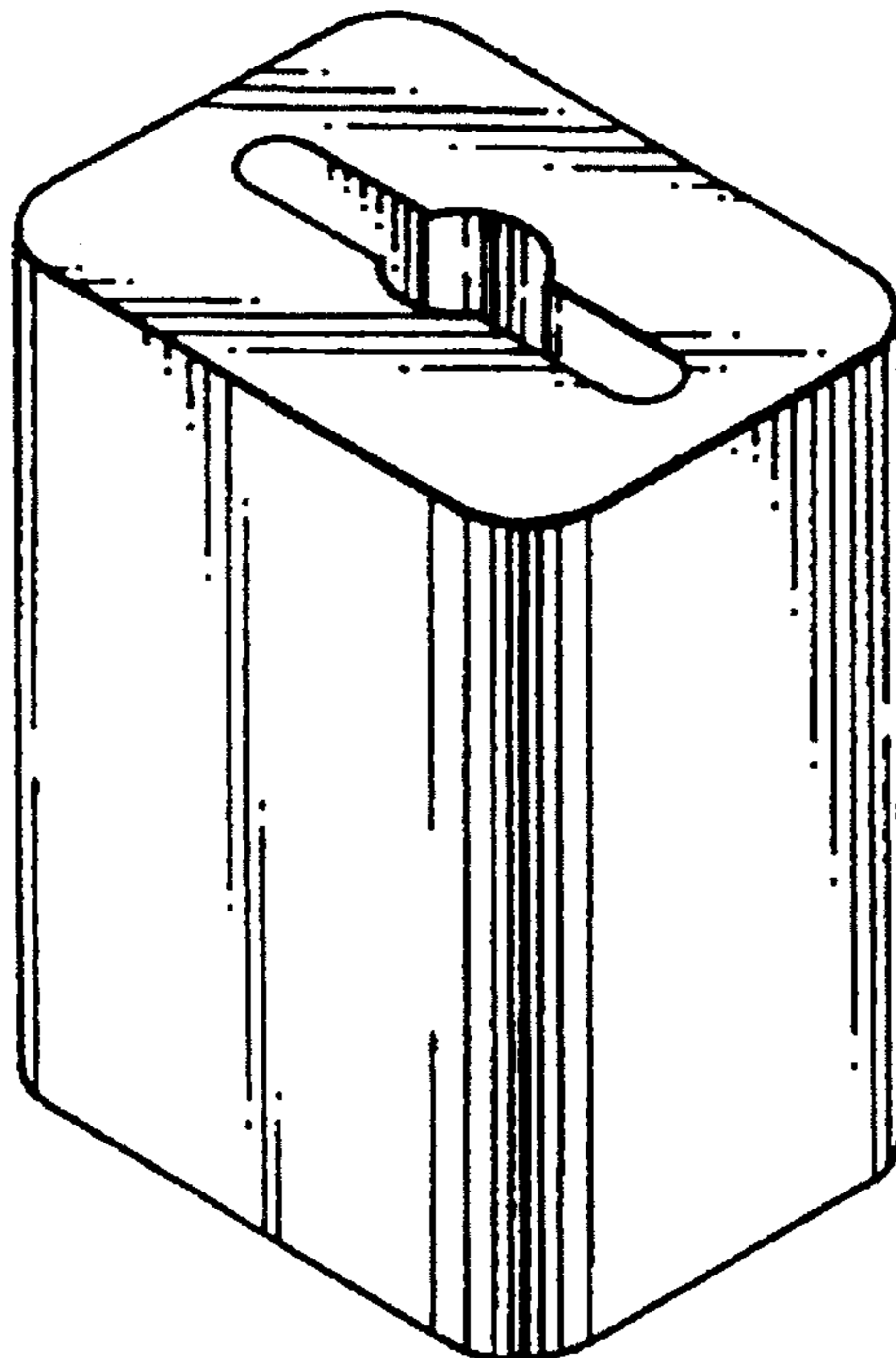


FIG. 1

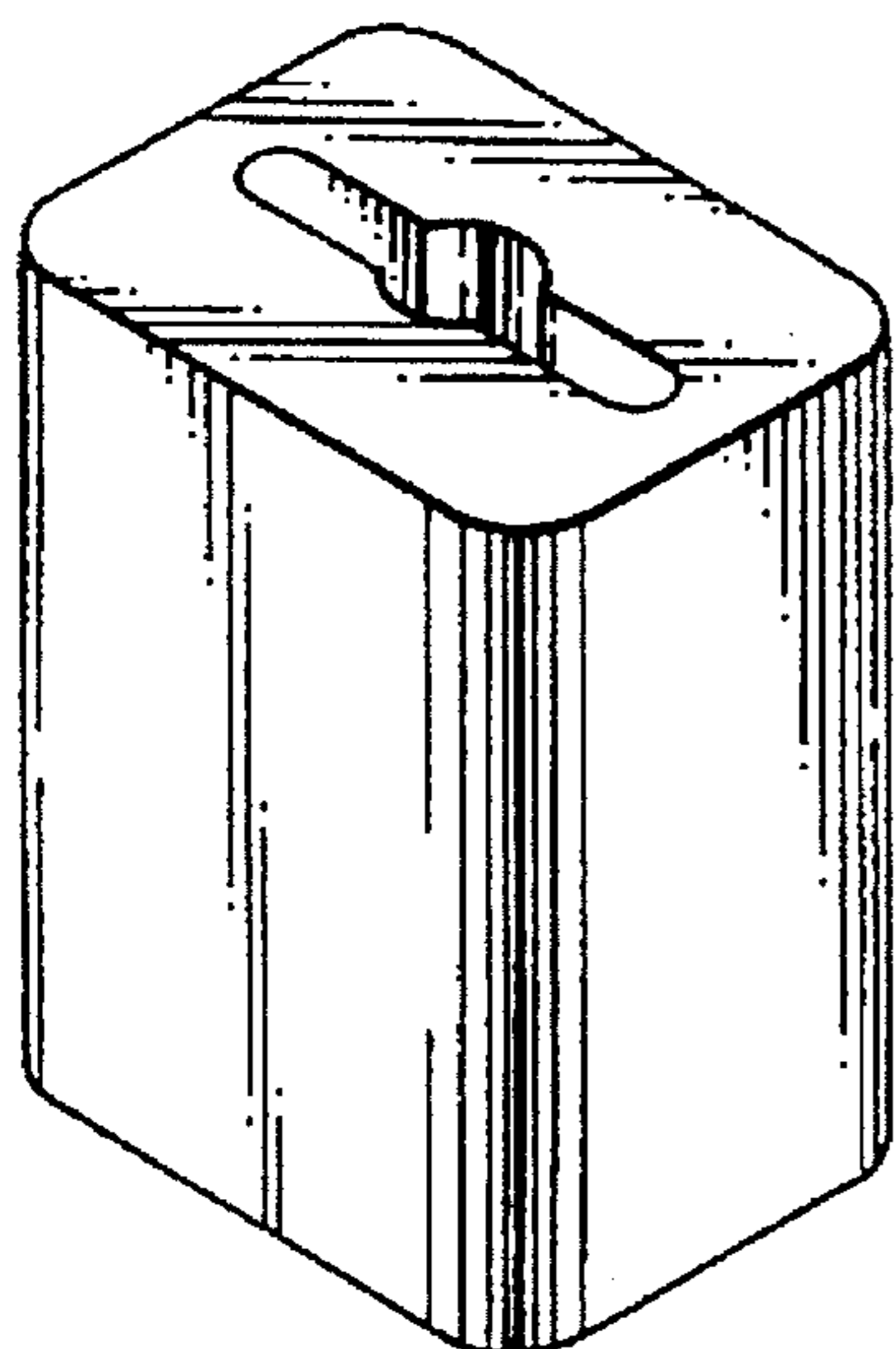


FIG. 2

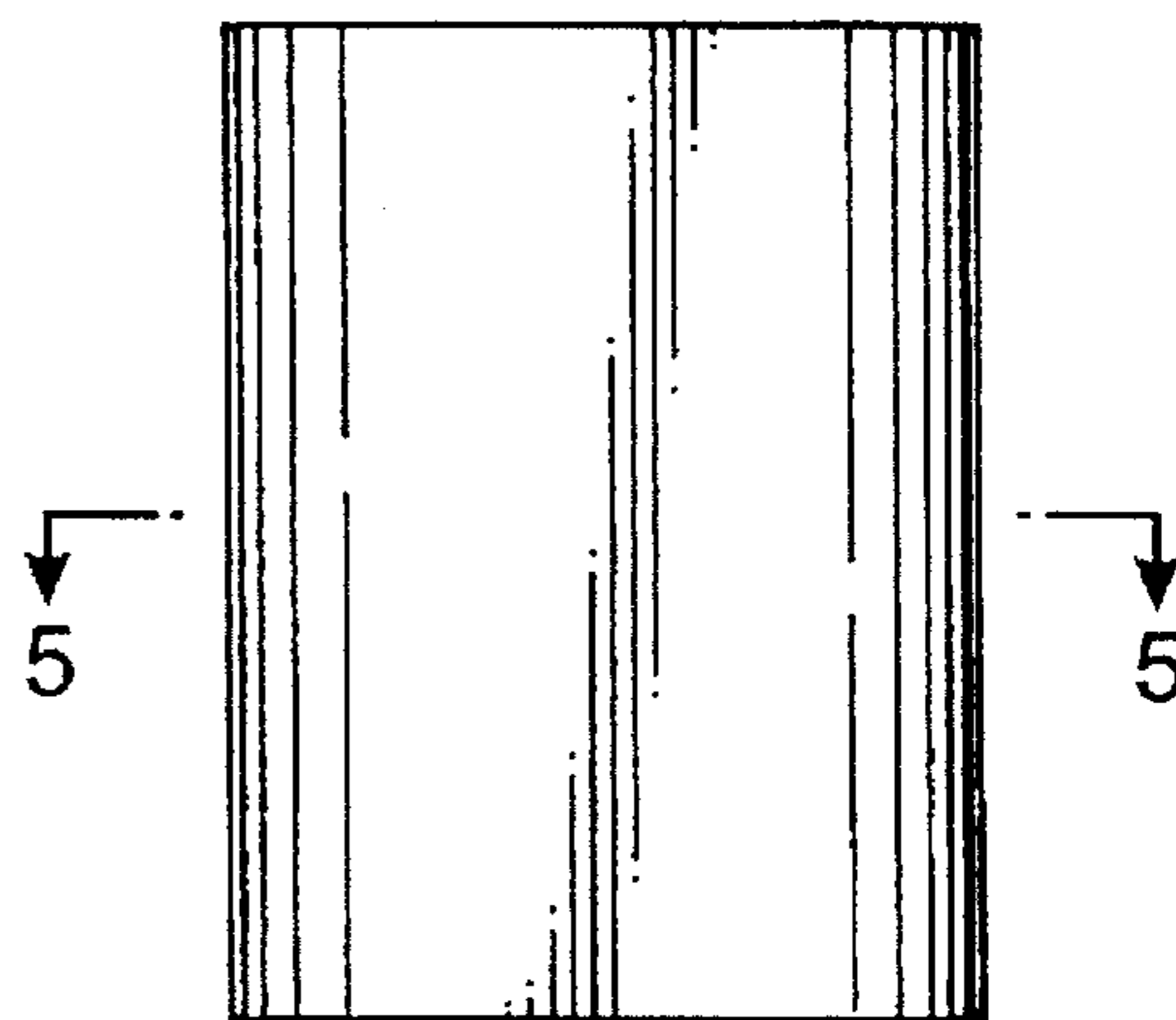


FIG. 3

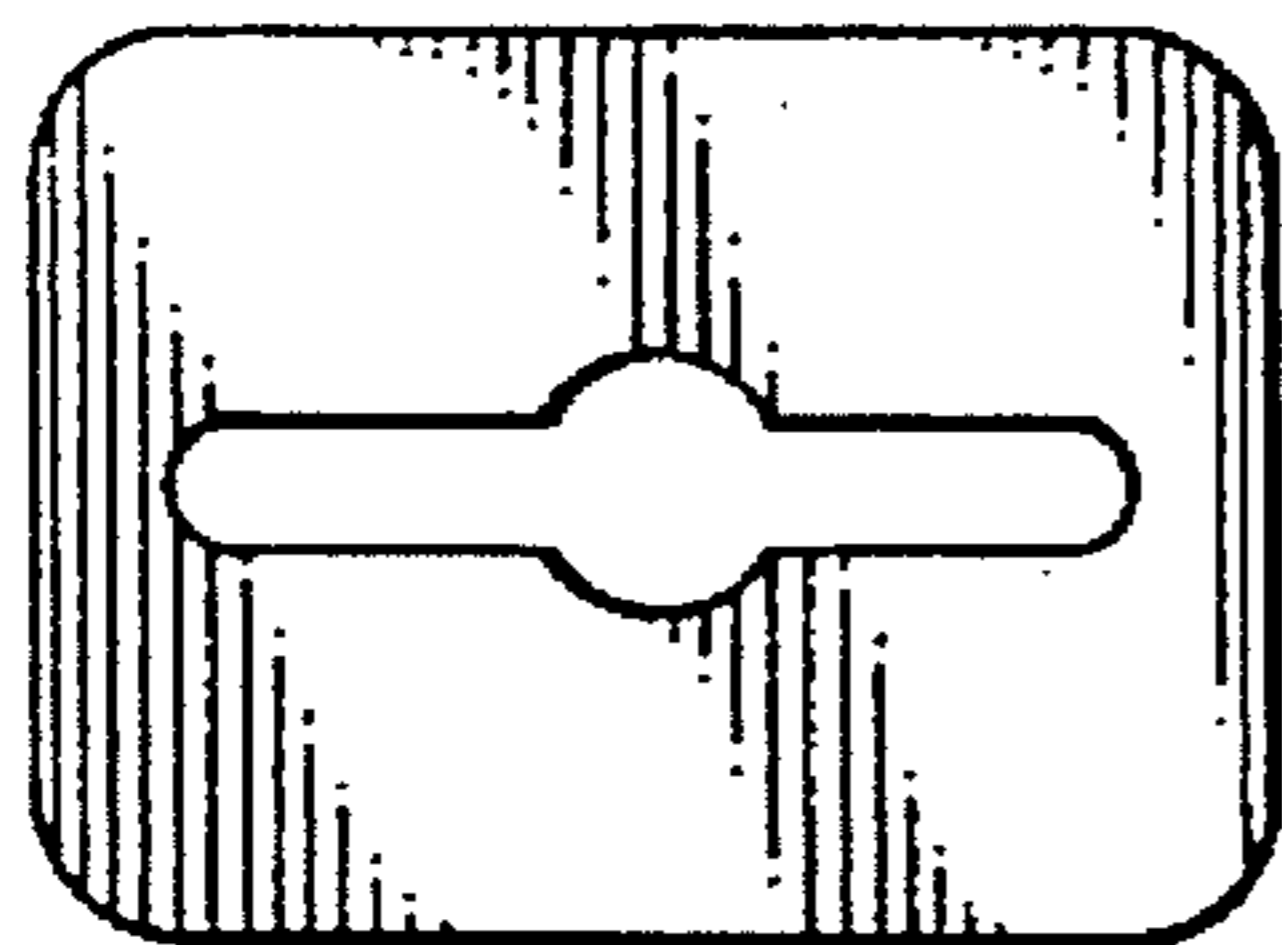


FIG. 4

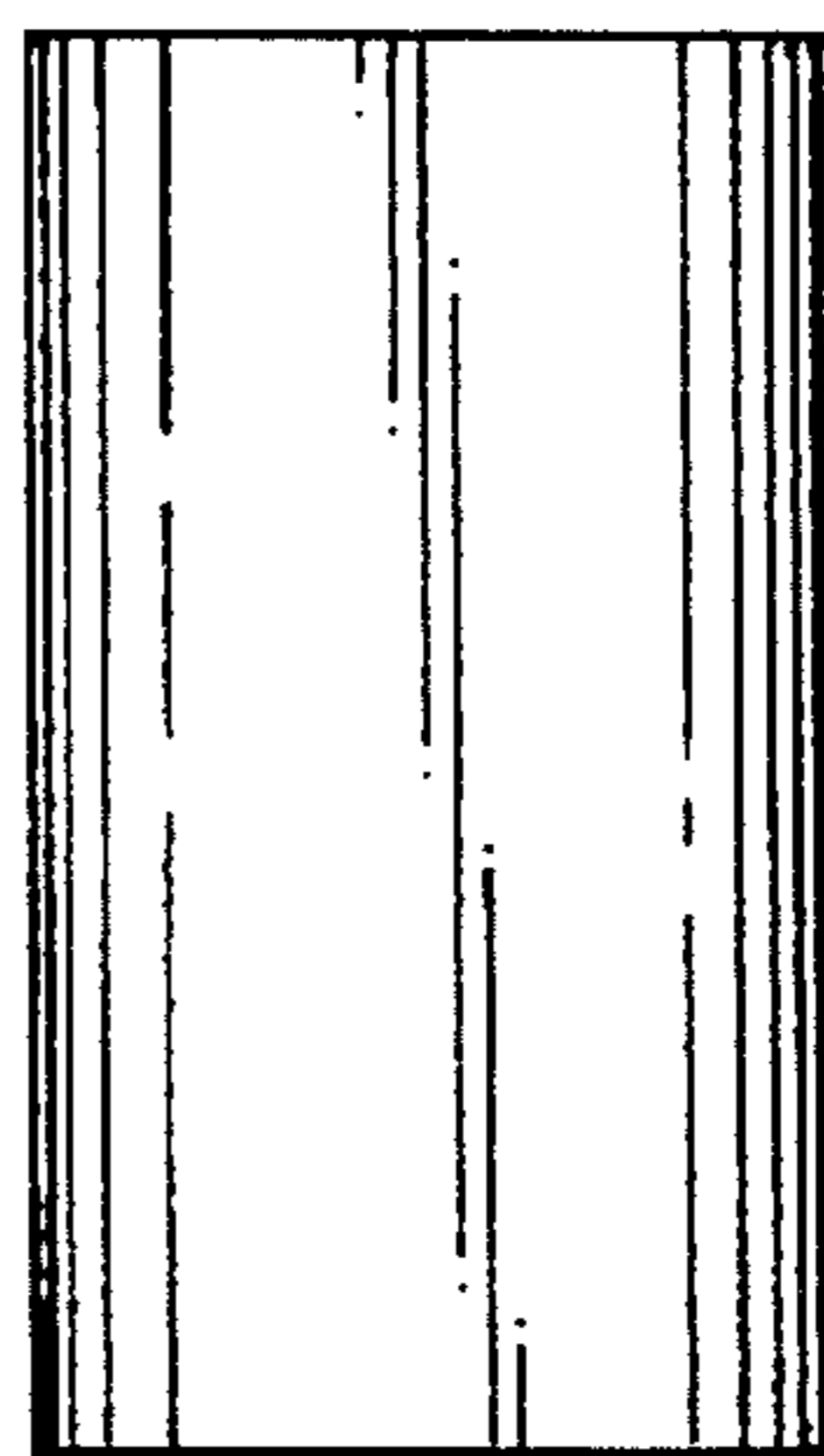


FIG. 5

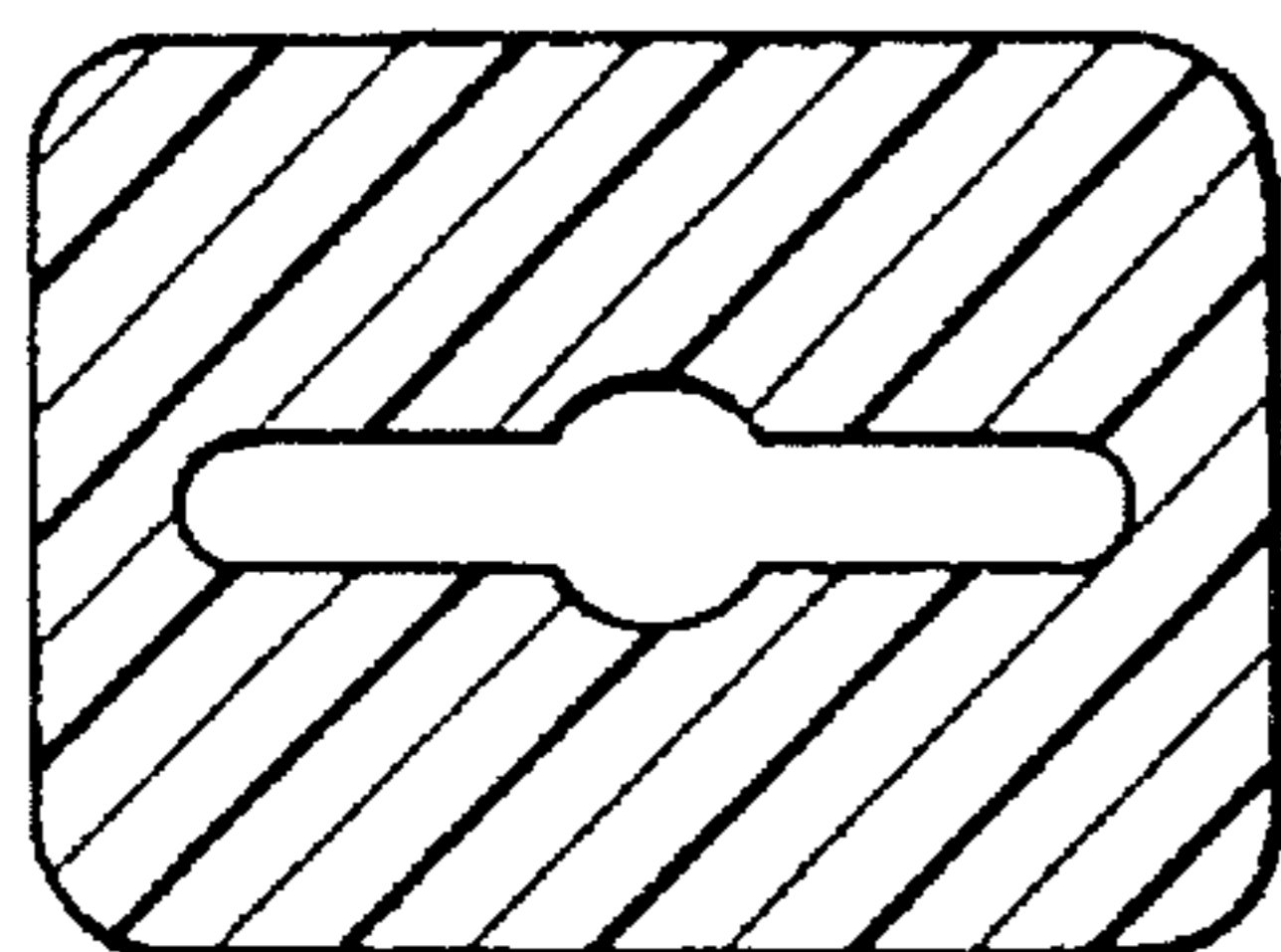


FIG. 6

