



US00D378814S

United States Patent [19]

[11] Patent Number: **Des. 378,814**

Adams et al.

[45] Date of Patent: ****Apr. 15, 1997**

[54] **ELECTRICAL LOAD CONTROL PANEL**

[75] Inventors: **Jason O. Adams**, Emmaus; **Russel J. Jacobs**, Center Valley; **Noel Mayo**, Philadelphia; **Brett A. Pierce**, Hellertown; **Joel S. Spira**, Coopersburg, all of Pa.

[73] Assignee: **Lutron Electronics Co., Ltd.**, Coopersburg, Pa.

[**] Term: **14 Years**

[21] Appl. No.: **15,541**

[22] Filed: **Nov. 15, 1993**

Related U.S. Application Data

[62] Division of Ser. No. 754,435, Sep. 3, 1991, Pat. No. Des. 344,068.

[52] **U.S. Cl.** **D13/164**

[58] **Field of Search** D13/162, 164; D8/353; 315/291, 295; 174/66, 67

[56] References Cited

U.S. PATENT DOCUMENTS

- D. 285,066 8/1986 Liss et al. D8/353 X
- D. 287,141 12/1986 Liss et al. D8/353 X
- D. 311,170 10/1990 Mayo et al. D13/164
- D. 311,485 10/1990 Jacoby et al. D13/164 X

- D. 313,738 1/1991 Mayo et al. D8/353
- D. 319,429 8/1991 D'Aleo et al. D13/162
- D. 323,488 1/1992 Darnell et al. D8/353 X
- D. 327,255 6/1992 D'Aleo et al. D13/162
- 4,810,833 3/1989 Meyers 174/67
- 4,893,062 1/1990 D'Aleo et al. 315/291
- 4,924,151 5/1990 D'Aleo et al. 315/292 X

Primary Examiner—Joel Sincavage
Attorney, Agent, or Firm—Ostrolenk, Faber, Gerb & Soffen, LLP

[57] CLAIM

The ornamental design for a electrical load control panel, as shown and described.

DESCRIPTION

FIG. 1 is a front plan view of a electrical load control panel showing a first embodiment of our new design; FIG. 2 is a right side elevational view of FIG. 1; FIG. 3 is a left side elevational view of FIG. 1; FIG. 4 is a top plan view of FIG. 1; FIG. 5 is a bottom plan view of FIG. 1; FIG. 6 is a front plan view of an electrical load control panel showing a second embodiment of our new design; FIG. 7 is a right side elevational view of FIG. 6; FIG. 8 is a left side elevational view of FIG. 6; FIG. 9 is a top plan view of FIG. 6; and, FIG. 10 is a bottom plan view of FIG. 6.

1 Claim, 2 Drawing Sheets

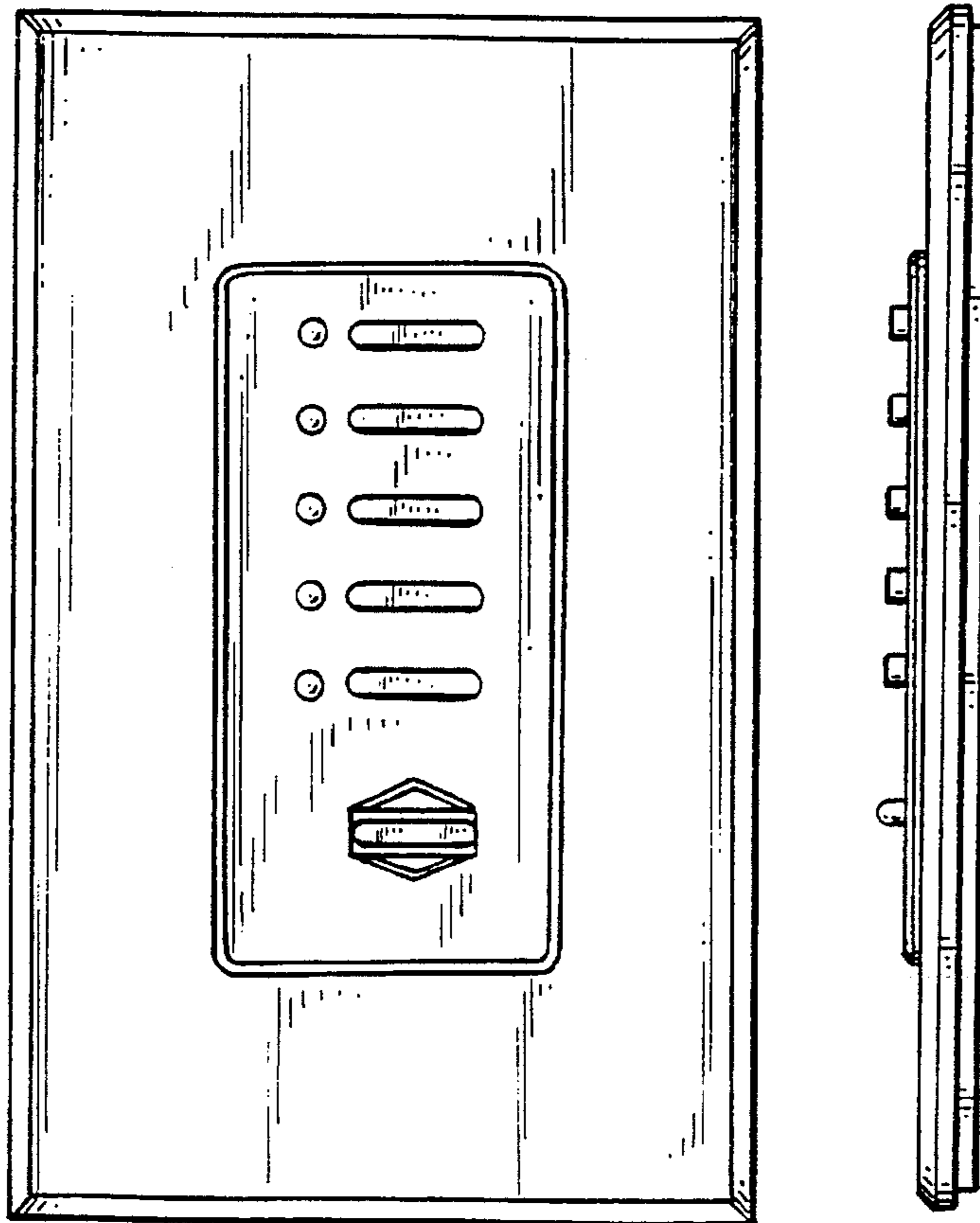


FIG. 4.

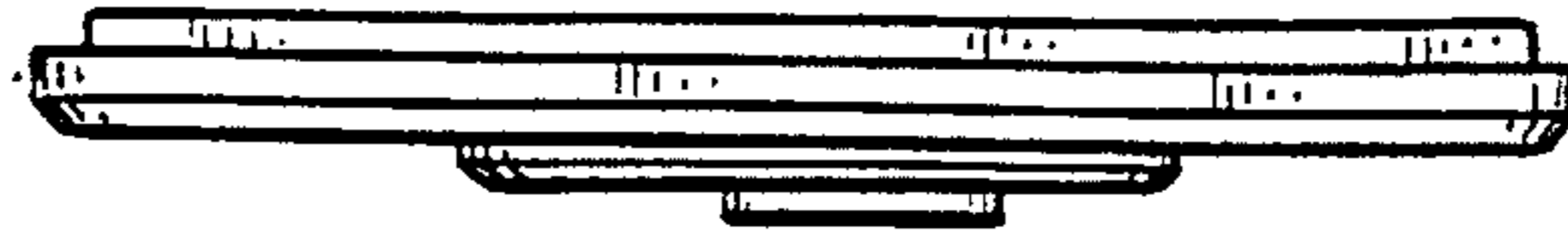


FIG. 3.

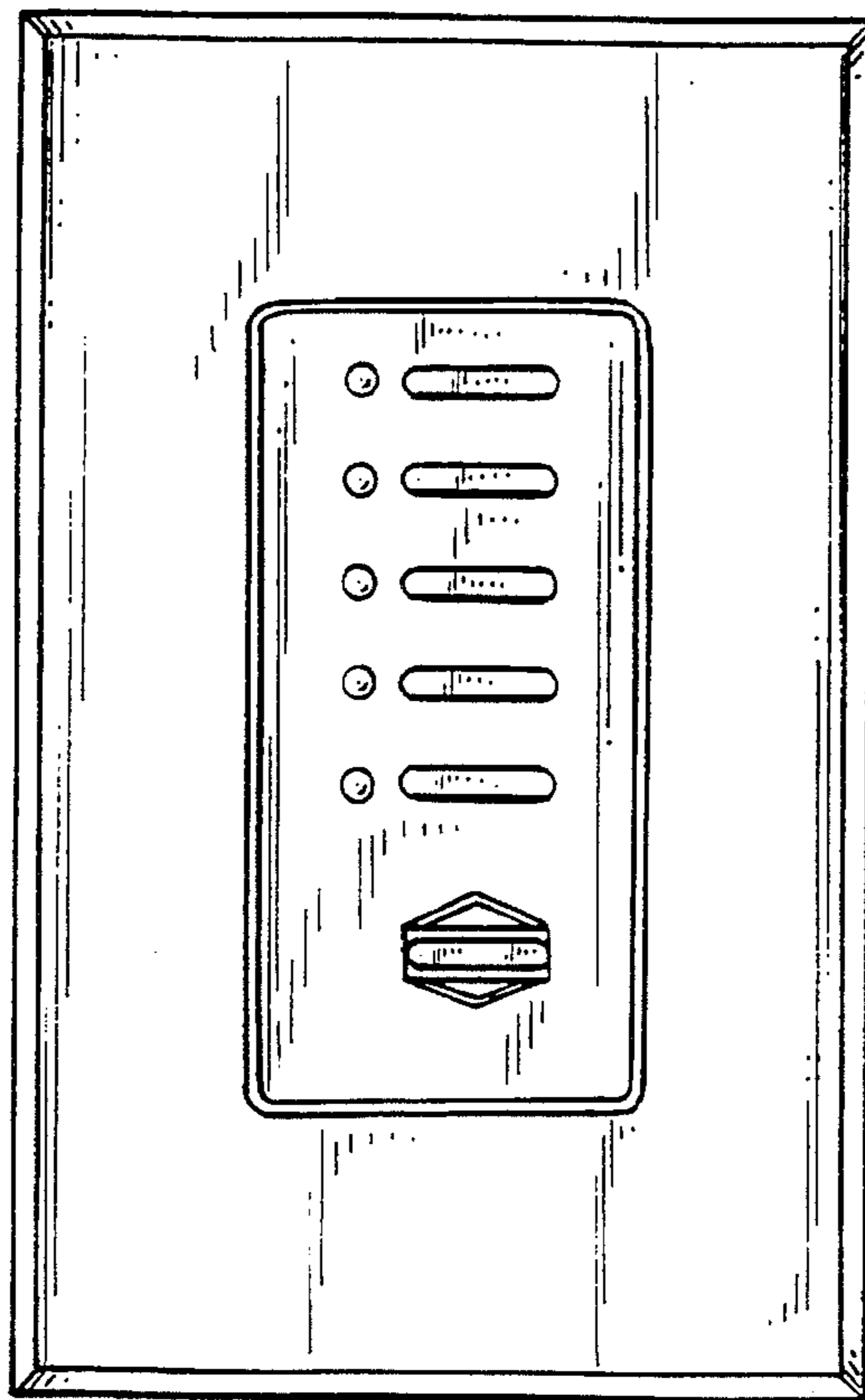


FIG. 1.



FIG. 2.

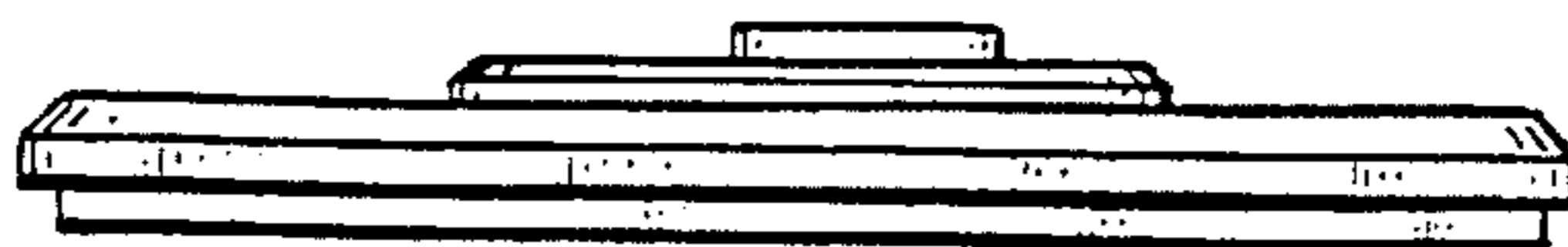


FIG. 5.

FIG. 9.

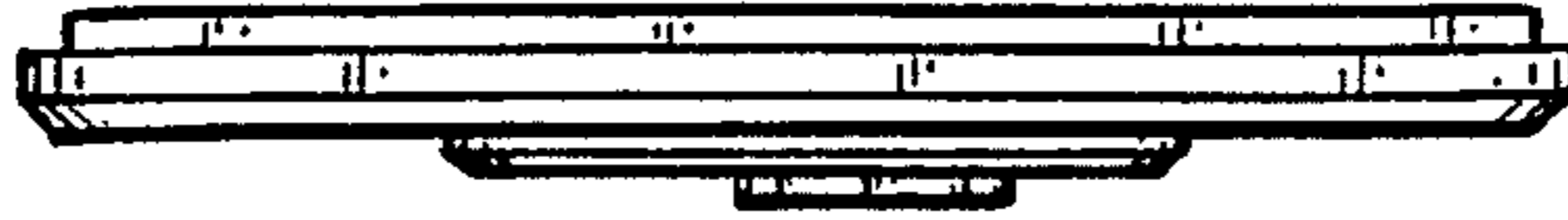


FIG. 8.

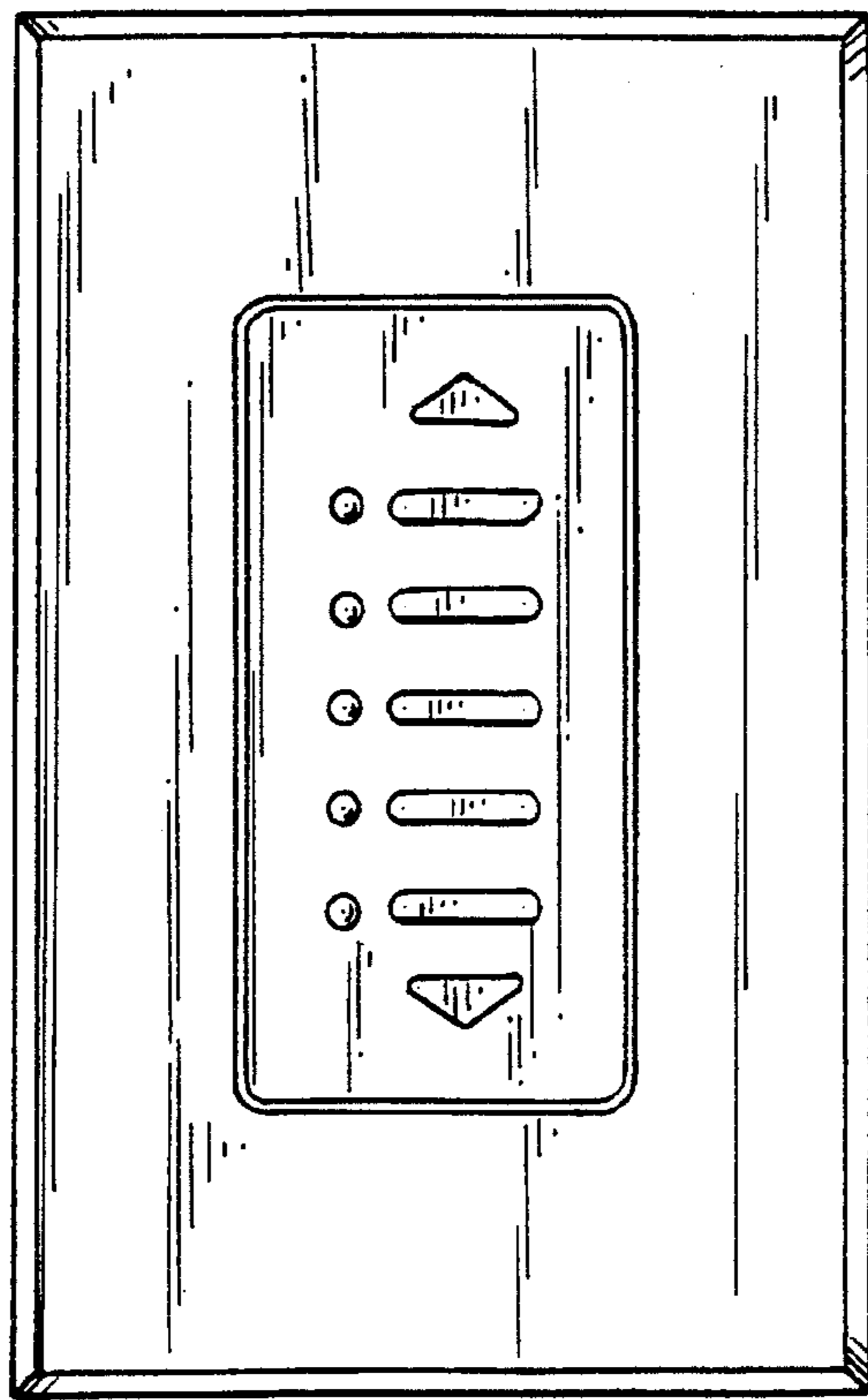


FIG. 6.



FIG. 7.

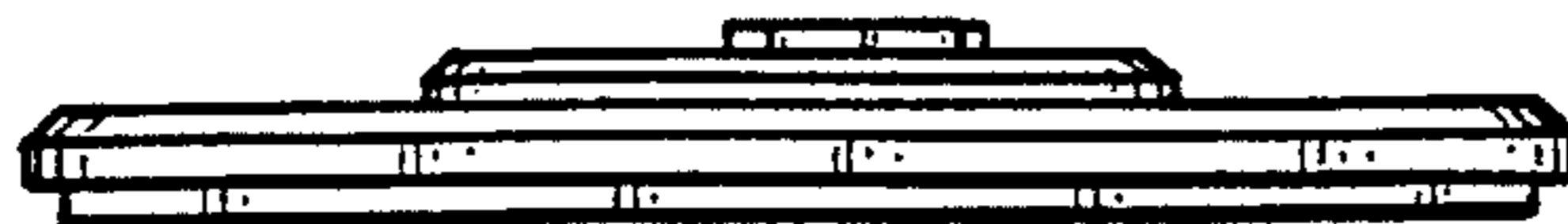


FIG. 10.