



US00D374010S

# United States Patent [19]

[11] Patent Number: Des. 374,010

Jue et al.

[45] Date of Patent: \*\*Sep. 24, 1996

[54] **DIGITAL SIGNAL PROCESSOR**

D. 259,565 6/1981 Watanabe ..... D14/197  
D. 283,710 5/1986 Wysong et al. .... D14/198

[75] Inventors: **Martin F. Jue; Steven S. Pan**, both of Starkville, Miss.

*Primary Examiner*—Sandra L. Morris

*Assistant Examiner*—Nanda Bondade

*Attorney, Agent, or Firm*—James L. Ewing, IV; Kilpatrick & Cody

[73] Assignee: **MFJ Enterprises, Inc.**, Starkville, Miss.

[\*\*] Term: **14 Years**

[57] **CLAIM**

The ornamental design for a digital signal processor, as shown and described.

[21] Appl. No.: **29,644**

**DESCRIPTION**

[22] Filed: **Oct. 12, 1994**

[52] U.S. Cl. .... **D14/188**

[58] Field of Search ..... D14/124, 137,  
D14/155, 163, 169, 188-198, 265, 299;  
455/344, 347, 350-351

FIG. 1 is a front elevational view of a digital signal processor showing our new design;

FIG. 2 is a rear elevational view thereof;

FIG. 3 is a right side elevational view thereof;

FIG. 4 is a left side elevational view thereof;

FIG. 5 is a top plan view thereof; and,

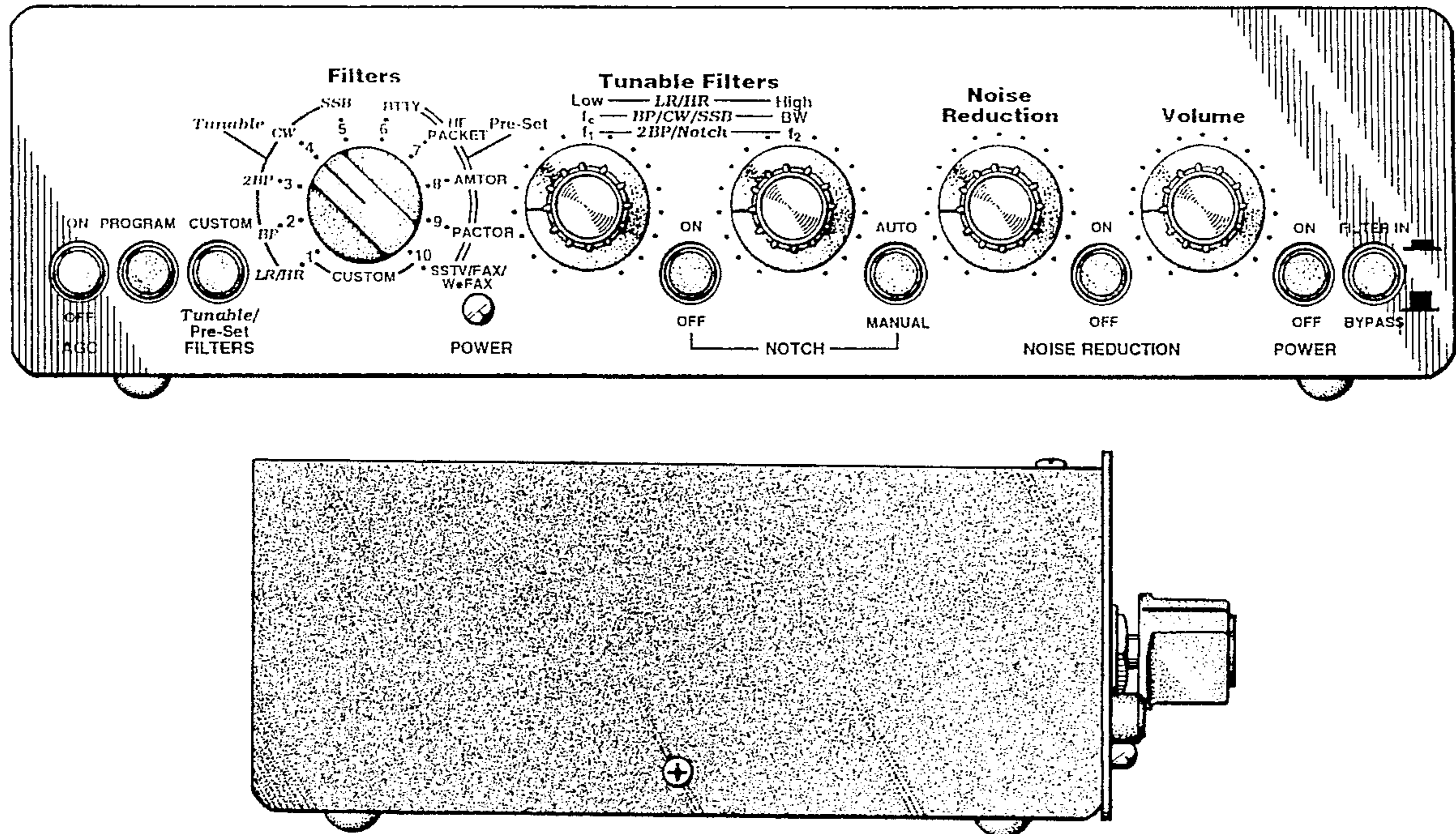
FIG. 6 is a bottom plan view thereof.

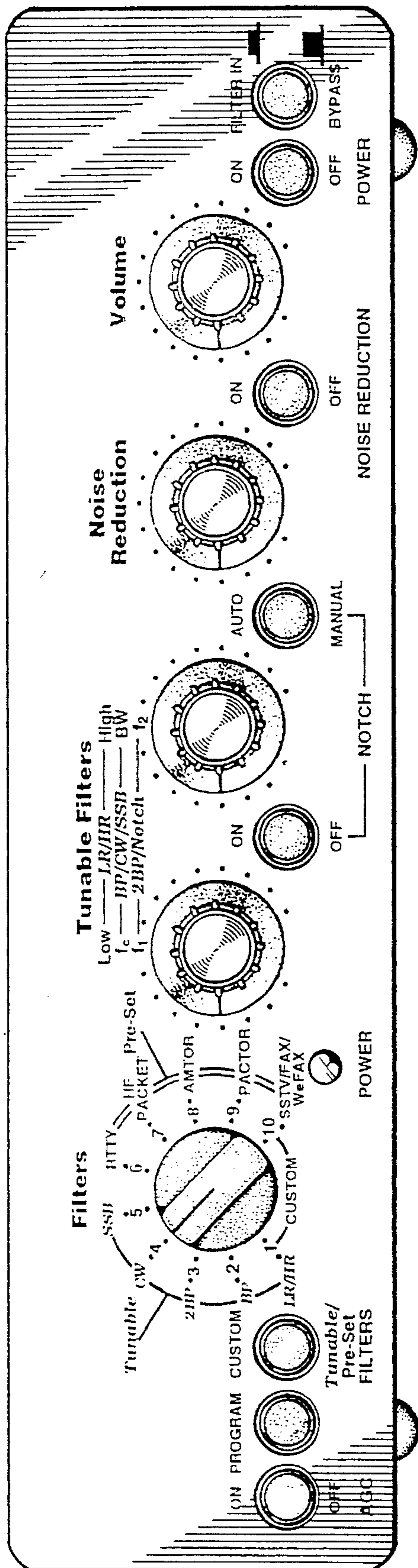
[56] **References Cited**

**U.S. PATENT DOCUMENTS**

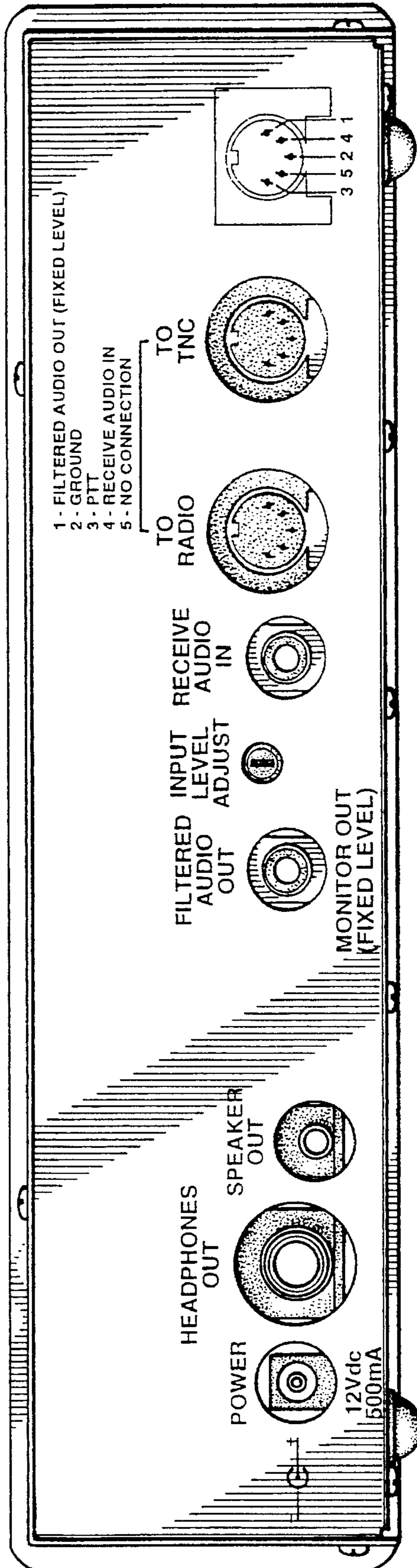
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**1 Claim, 4 Drawing Sheets**

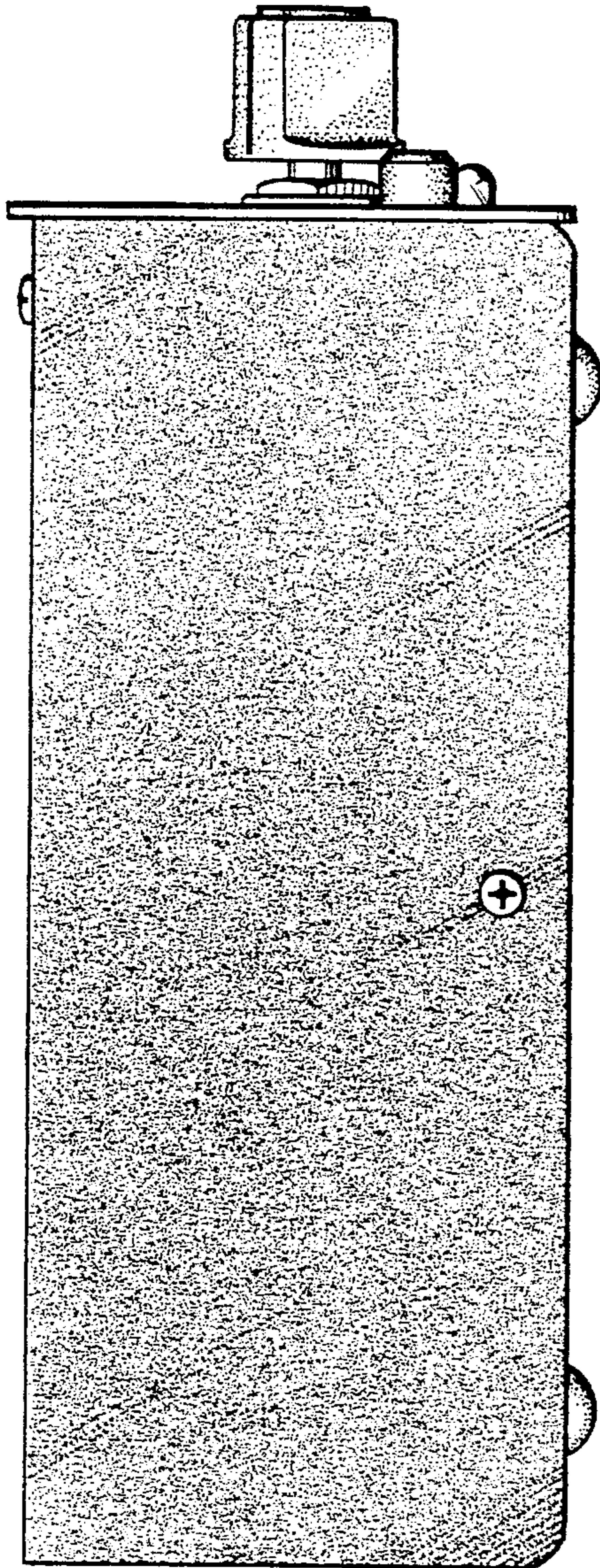




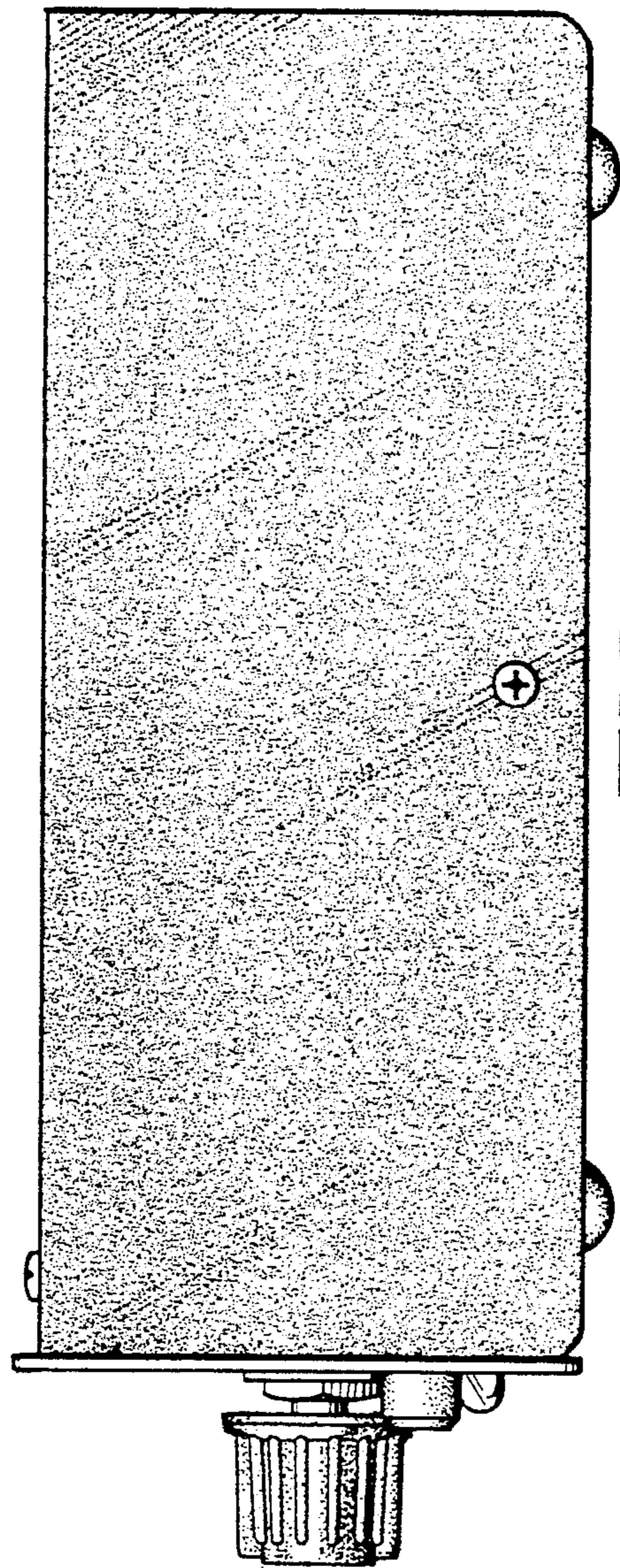
**FIG 1**



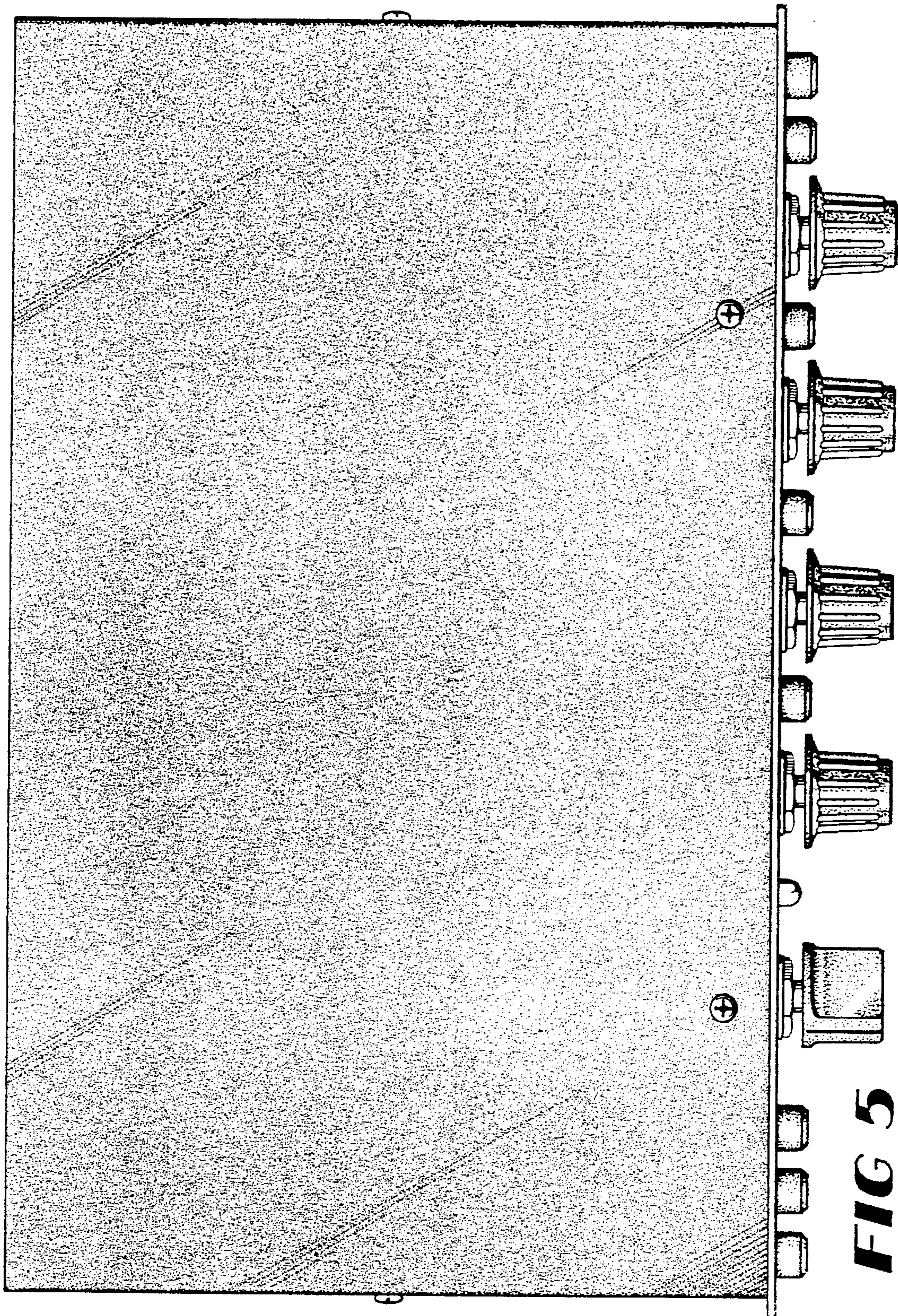
**FIG 2**



**FIG 3**



**FIG 4**



**FIG 5**

