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United States Patent [19]
Burroughs et al.

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[45] **Date of Patent:** ****Apr. 30, 1996**

[54] **VERTICAL PRINTED CIRCUIT BOARD JACK**
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Edward F. Sonsone, Coon Rapids, both
of Minn.
[73] Assignee: **ADC Telecommunications, Inc.**,
Minneapolis, Minn.
[**] Term: **14 Years**

[21] Appl. No.: **31,616**
[22] Filed: **Dec. 1, 1994**
[52] **U.S. Cl.** **D13/133**
[58] **Field of Search** D13/133; 439/63,
439/76, 544, 552, 557, 562, 567, 571, 581

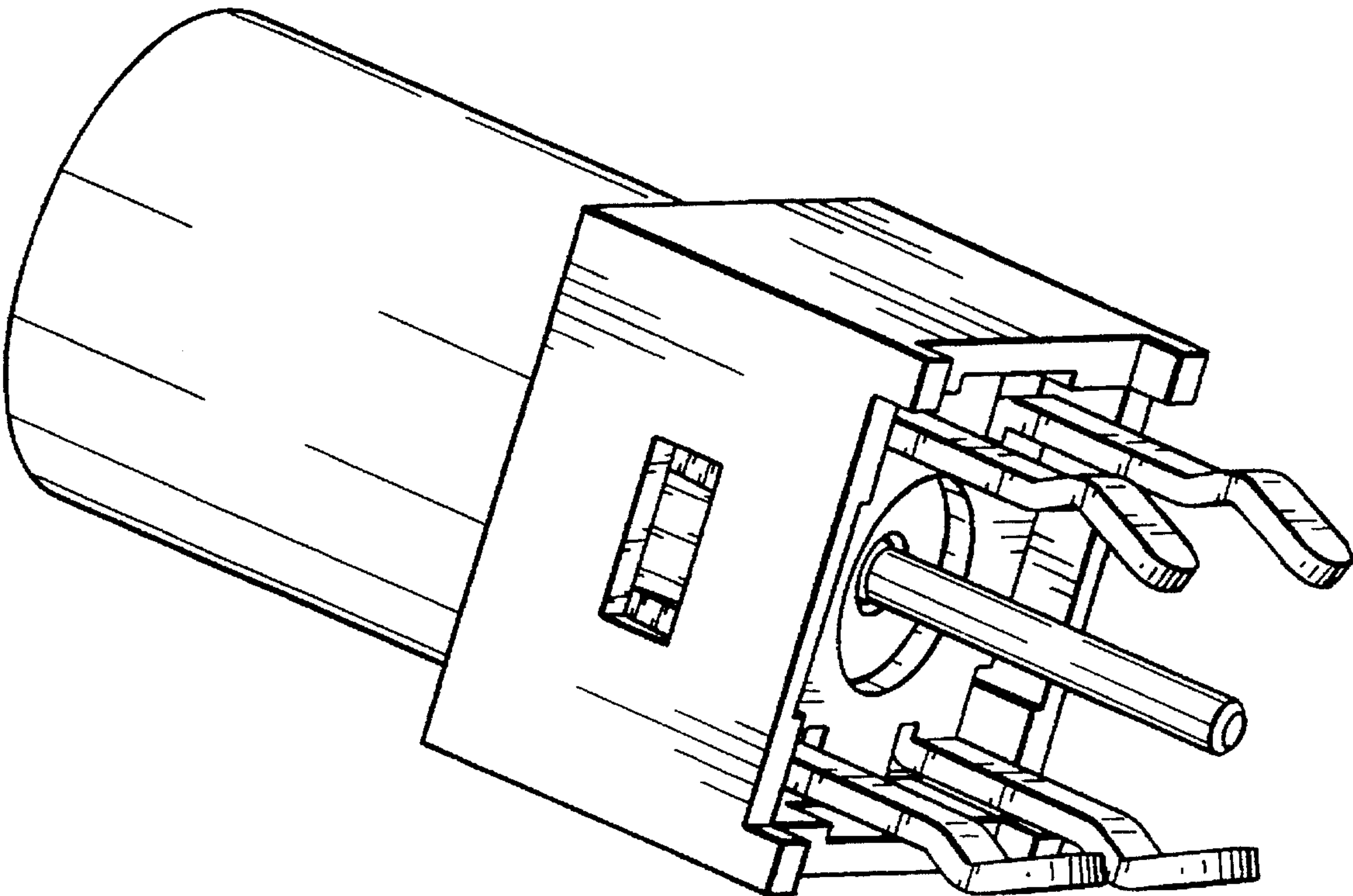
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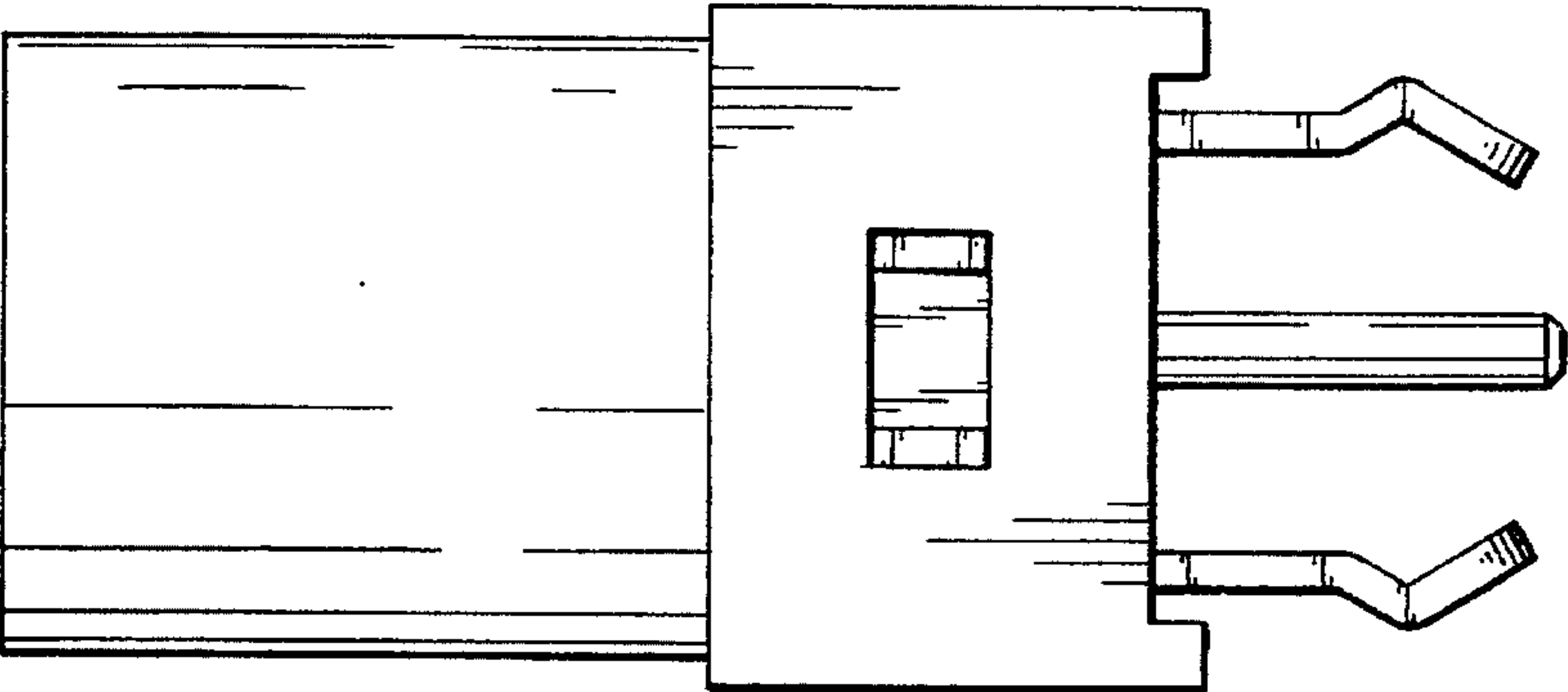
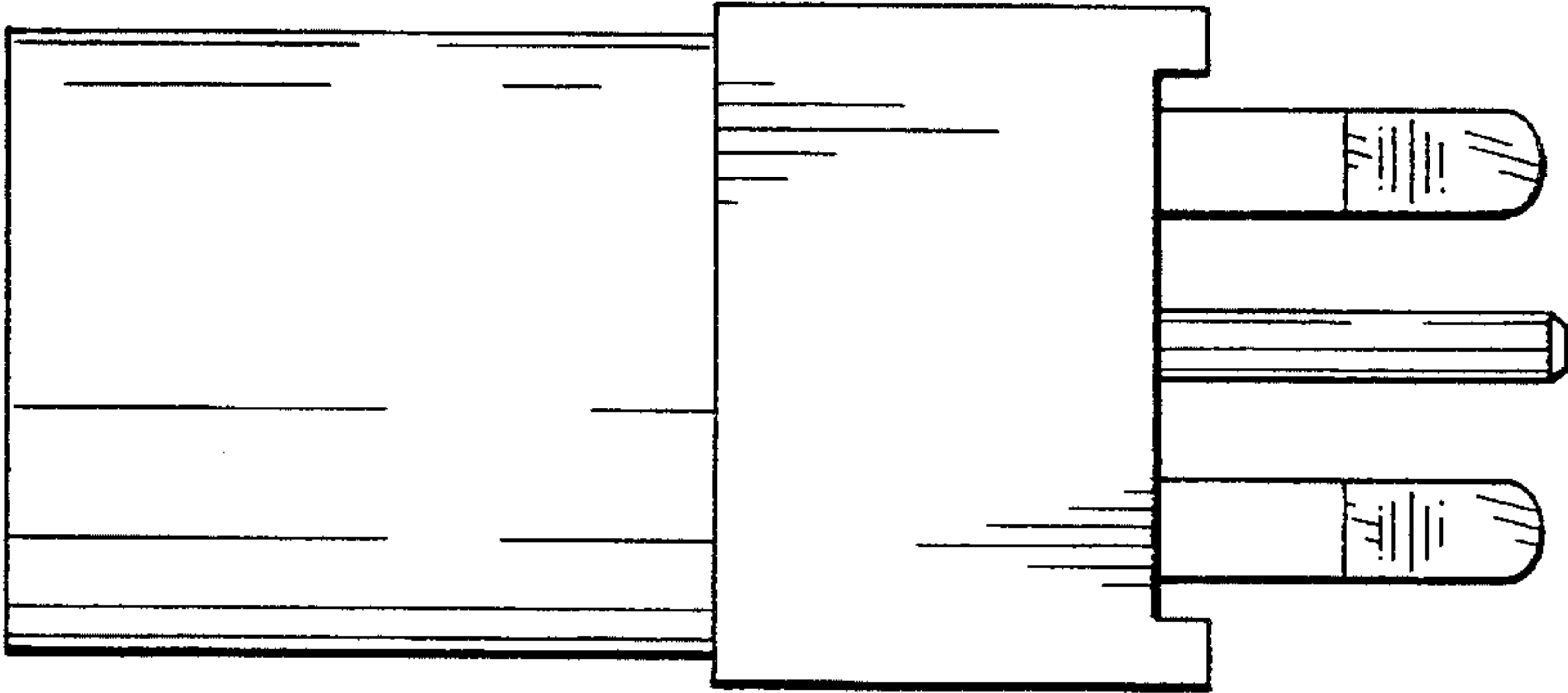
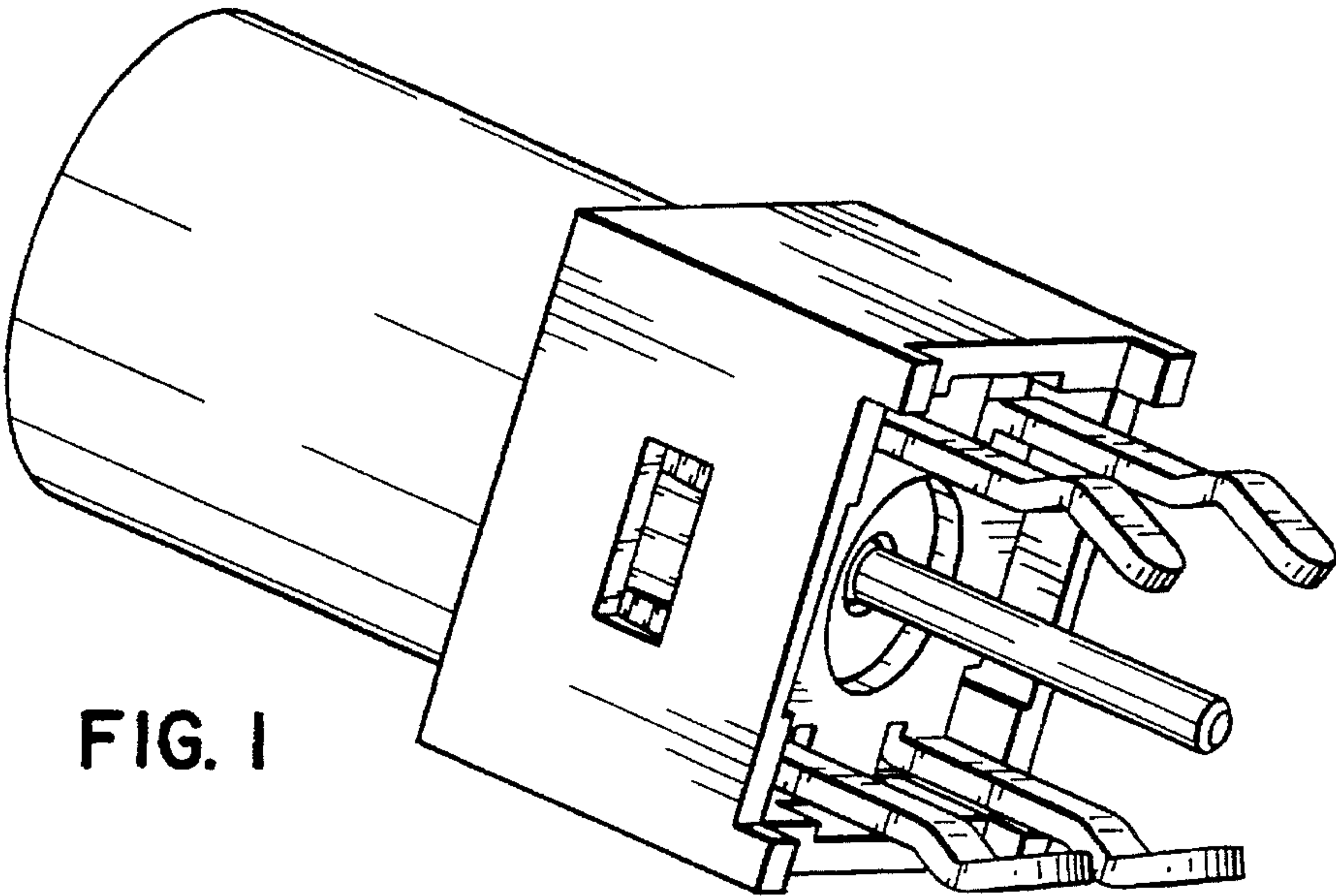
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[57] **CLAIM**
The ornamental design for a vertical printed circuit board
jack, as shown herein.

DESCRIPTION
FIG. 1 is a bottom, front and right side perspective view of
the vertical printed circuit board jack of the invention;
FIG. 2 is a front elevation view thereof with the opposite
side being substantially identical;
FIG. 3 is a right side elevation view thereof with the opposite
side being substantially identical;
FIG. 4 is a bottom plan view thereof; and,
FIG. 5 is a top plan view thereof.

1 Claim, 2 Drawing Sheets





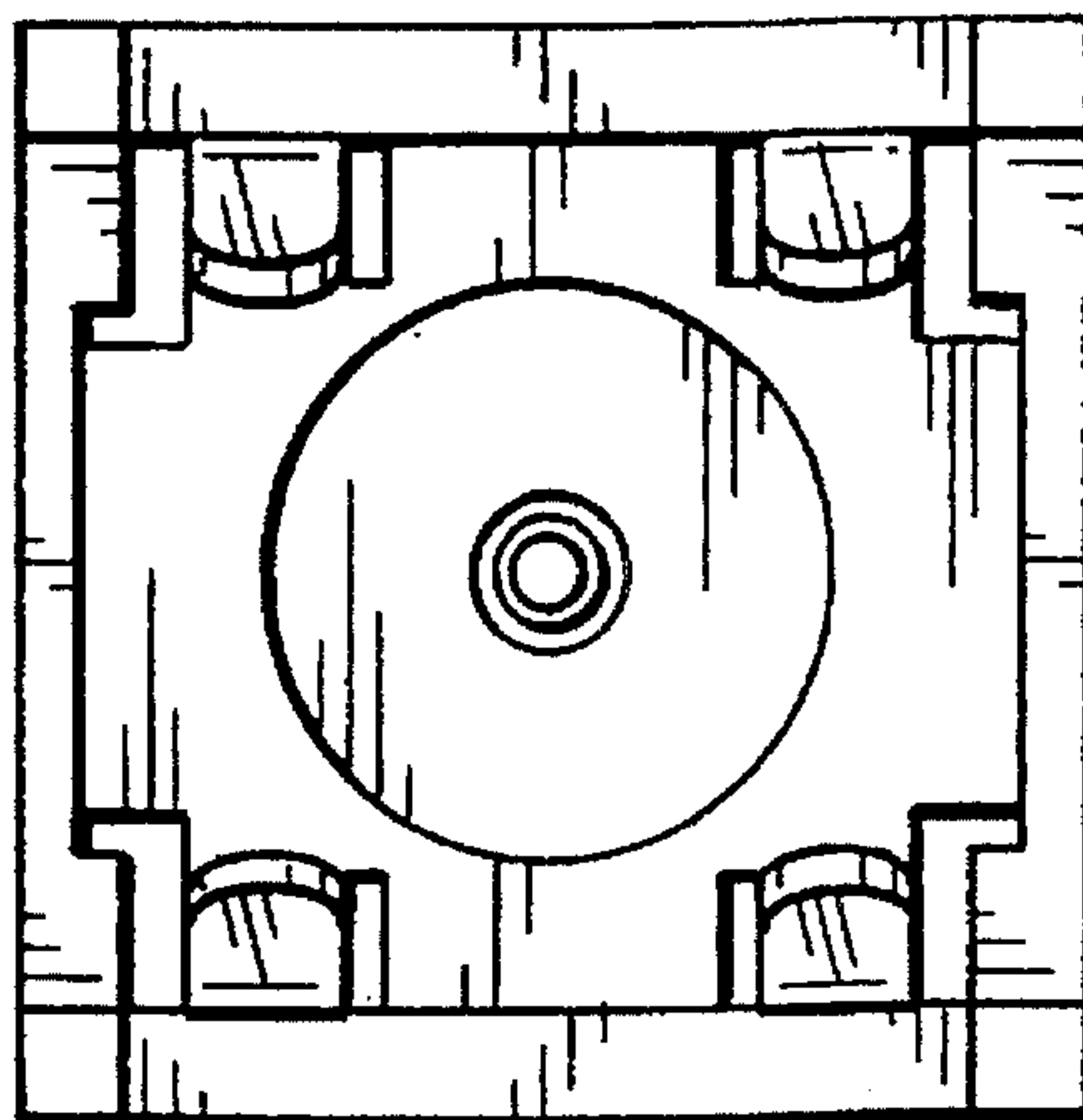


FIG. 4

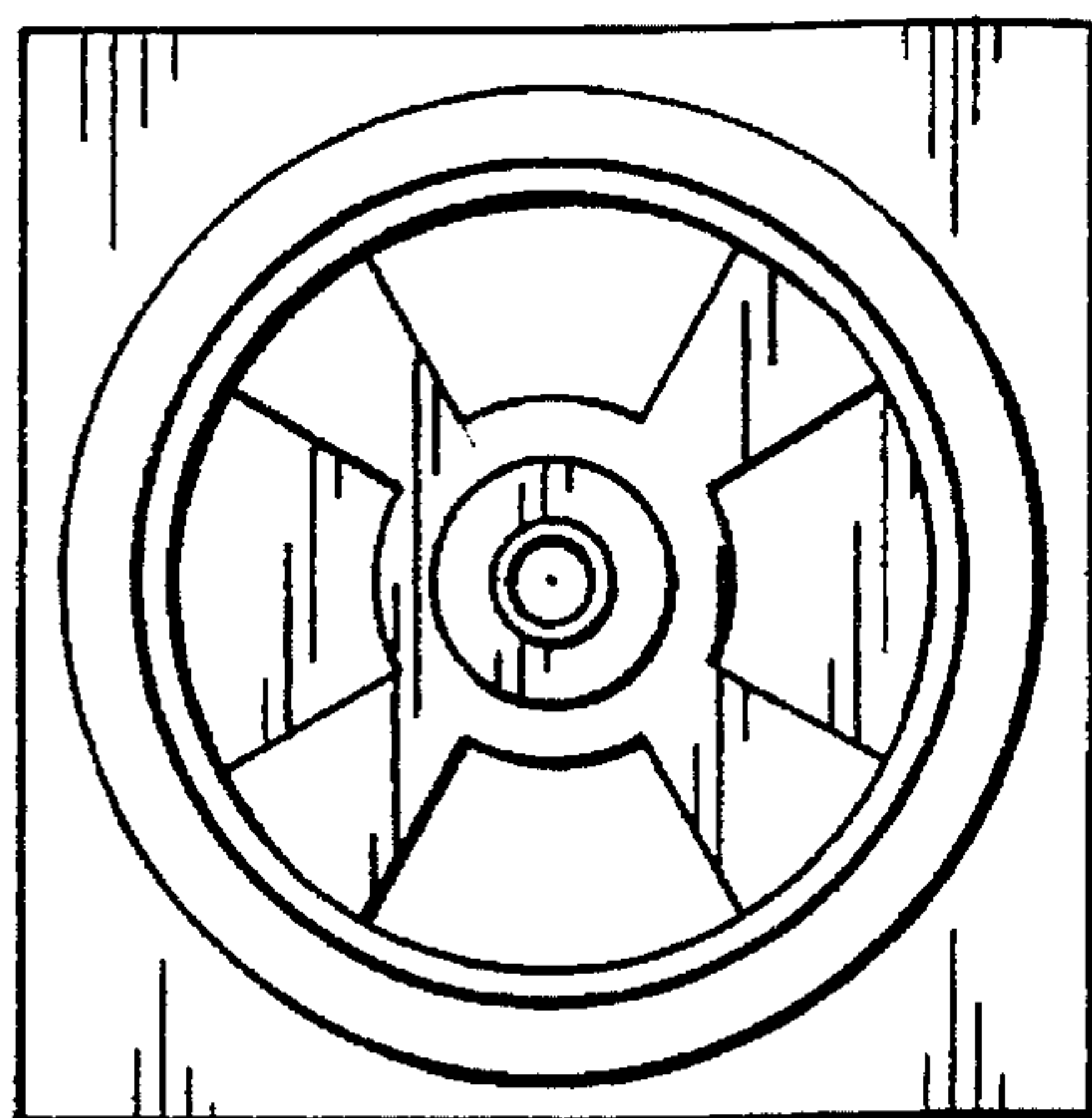


FIG. 5