



US00D368252S

United States Patent [19]
Nakai

[11] **Patent Number: Des. 368,252**
[45] **Date of Patent: **Mar. 26, 1996**

[54] **PROGRAMMABLE LOGIC CONTROLLER**

860498 3/1993 Japan .
883727 12/1993 Japan .

[75] Inventor: **Tatsunobu Nakai**, Osaka, Japan

OTHER PUBLICATIONS

[73] Assignee: **Idec Izumi Corporation**, Osaka, Japan

Programmable controllers on cover of 1986 Mitsubishi literature C3344-A-NA-8609.

[**] Term: **14 Years**

Mitsubishi programmable controller on p. 1242 of *Elektrotechnische Zeitschrift*, Sep. 1992.

[21] Appl. No.: **20,581**

Micro-1 Micro Programmable Controller For Small Machine Control, Idic Izumi Corporation, Cat. No. EP589-1, Dec. 1992, 5T Printed In Japan.

[22] Filed: **Mar. 29, 1994**

[30] **Foreign Application Priority Data**

Oct. 4, 1993 [JP] Japan 5-30187

[52] **U.S. Cl.** **D13/162**

[58] **Field of Search** D13/162, 164;
361/686, 752

Primary Examiner—Joel Sincavage

Attorney, Agent, or Firm—Pollock, Vande Sande & Priddy

[57] **CLAIM**

The ornamental design for a programmable logic controller, as shown and described.

[56] **References Cited**

DESCRIPTION

U.S. PATENT DOCUMENTS

- D. 290,694 7/1987 Shimizu et al. D13/162
- D. 316,247 4/1991 Llabres et al. D13/162
- D. 351,827 10/1994 Shimizu et al. D13/162

FIG. 1 is a front elevational view of a programmable logic controller showing my new design;
 FIG. 2 is a rear elevational view thereof;
 FIG. 3 is a top plan view thereof;
 FIG. 4 is a bottom plan view thereof;
 FIG. 5 is a left side elevational view thereof;
 FIG. 6 is a right side elevational view thereof;
 FIG. 7 is a front, left and bottom perspective view thereof;
 and,
 FIG. 8 is a perspective view thereof as seen from the rear and upper right.

FOREIGN PATENT DOCUMENTS

- 753559 1/1989 Japan .
- 813212 5/1991 Japan .
- 818985 9/1991 Japan .
- 748730 1/1992 Japan .
- 827391 1/1992 Japan .
- 826244 1/1992 Japan .
- 846663 9/1992 Japan .
- 851363 11/1992 Japan .

1 Claim, 4 Drawing Sheets

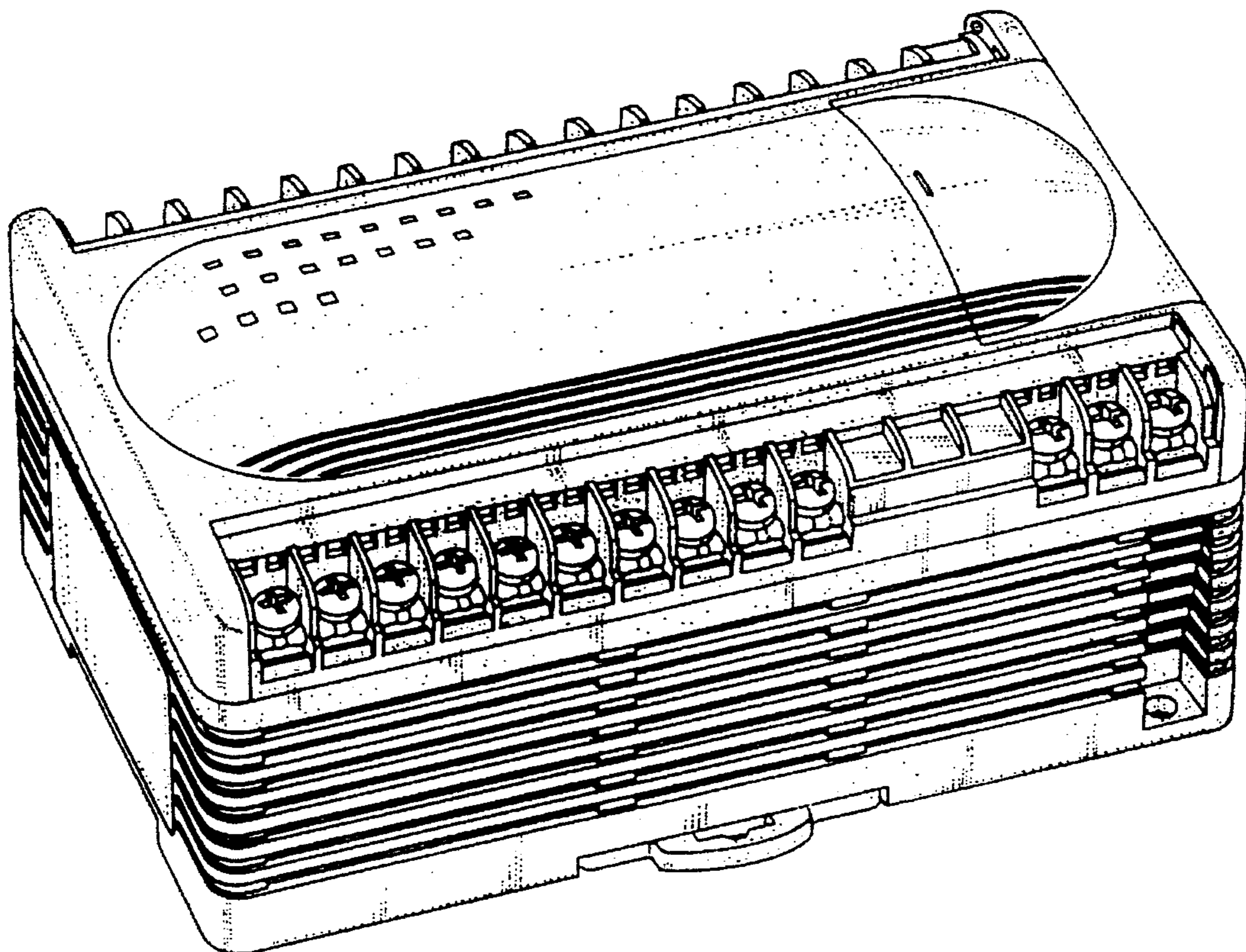


FIG. 1

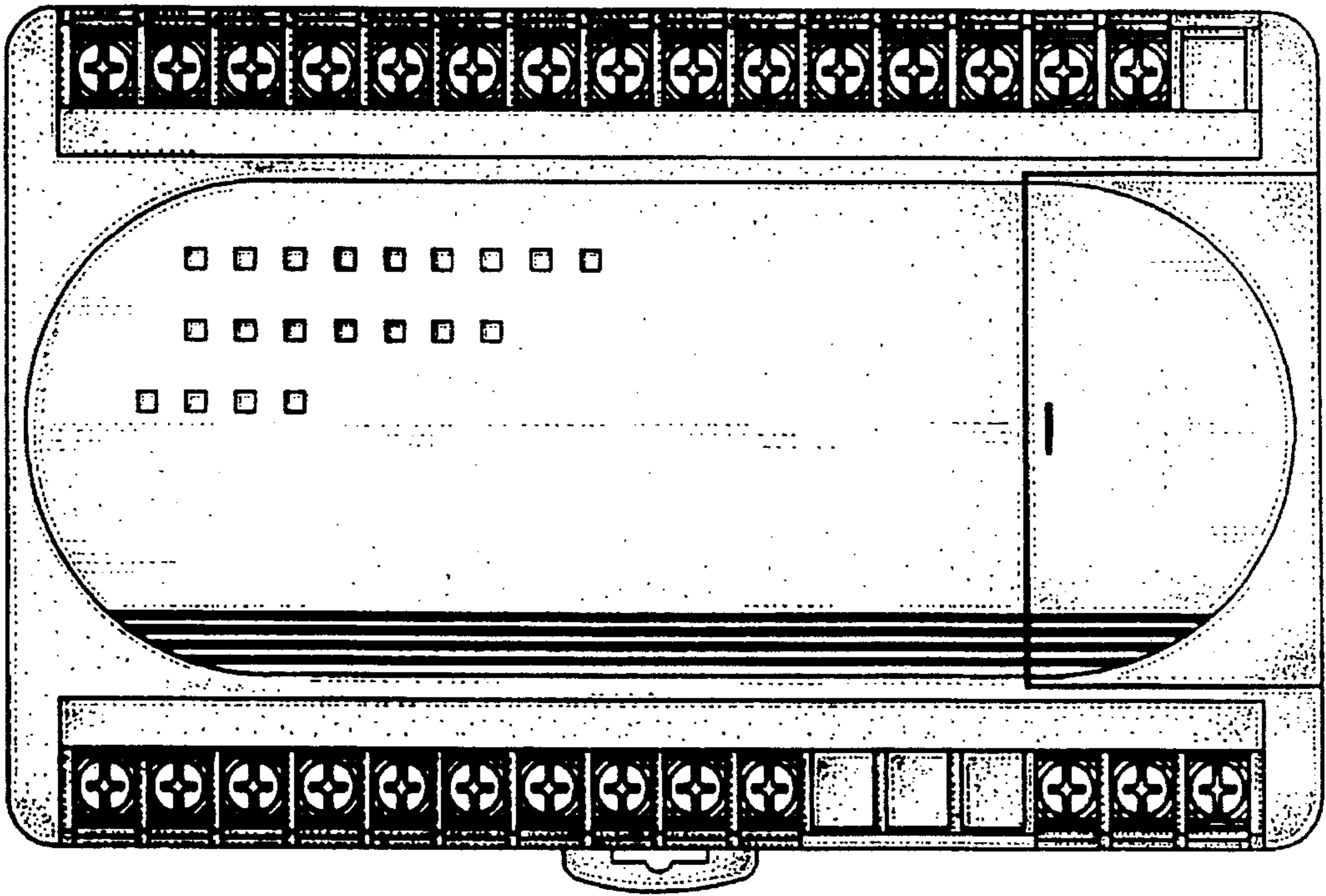


FIG. 2

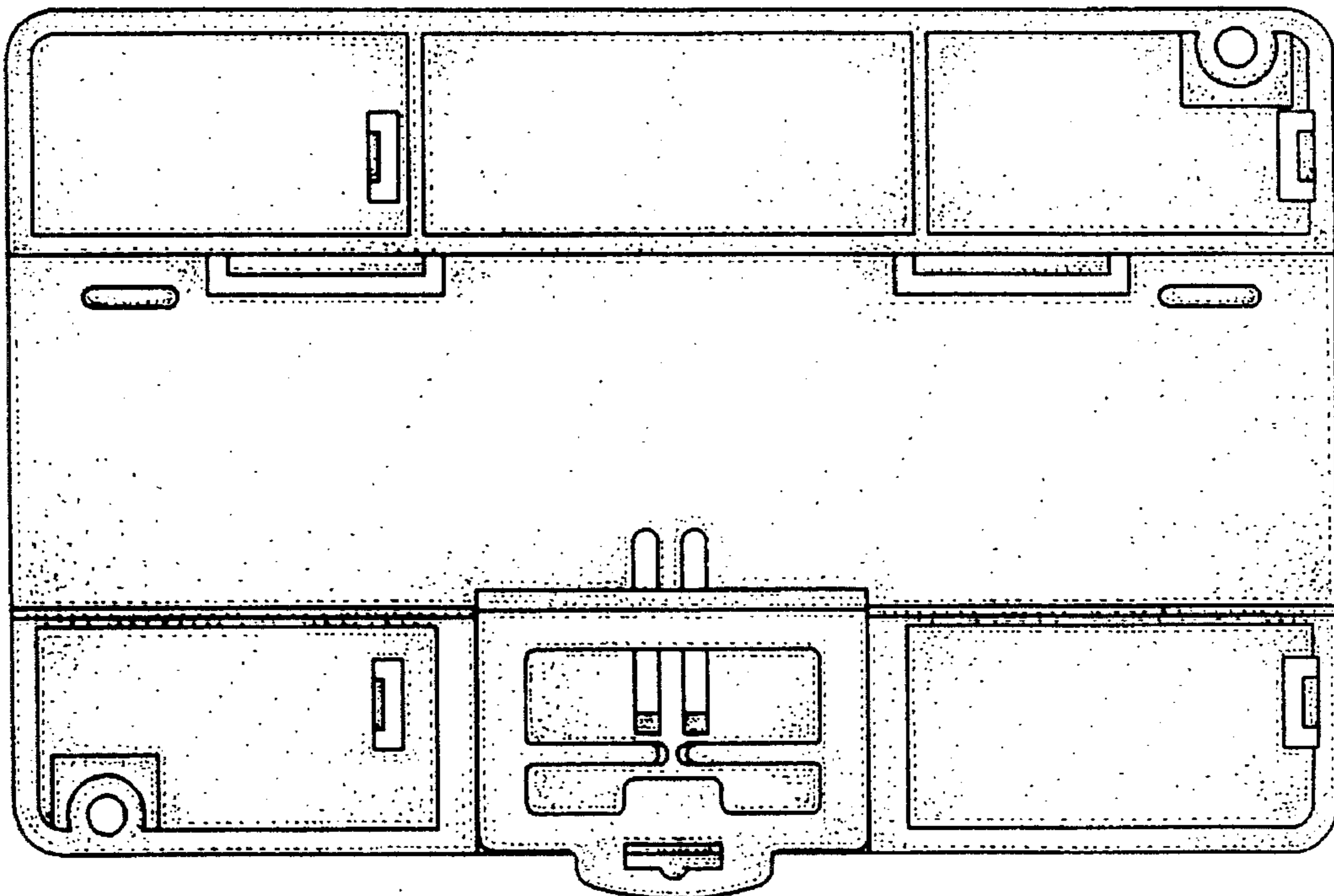


FIG. 3

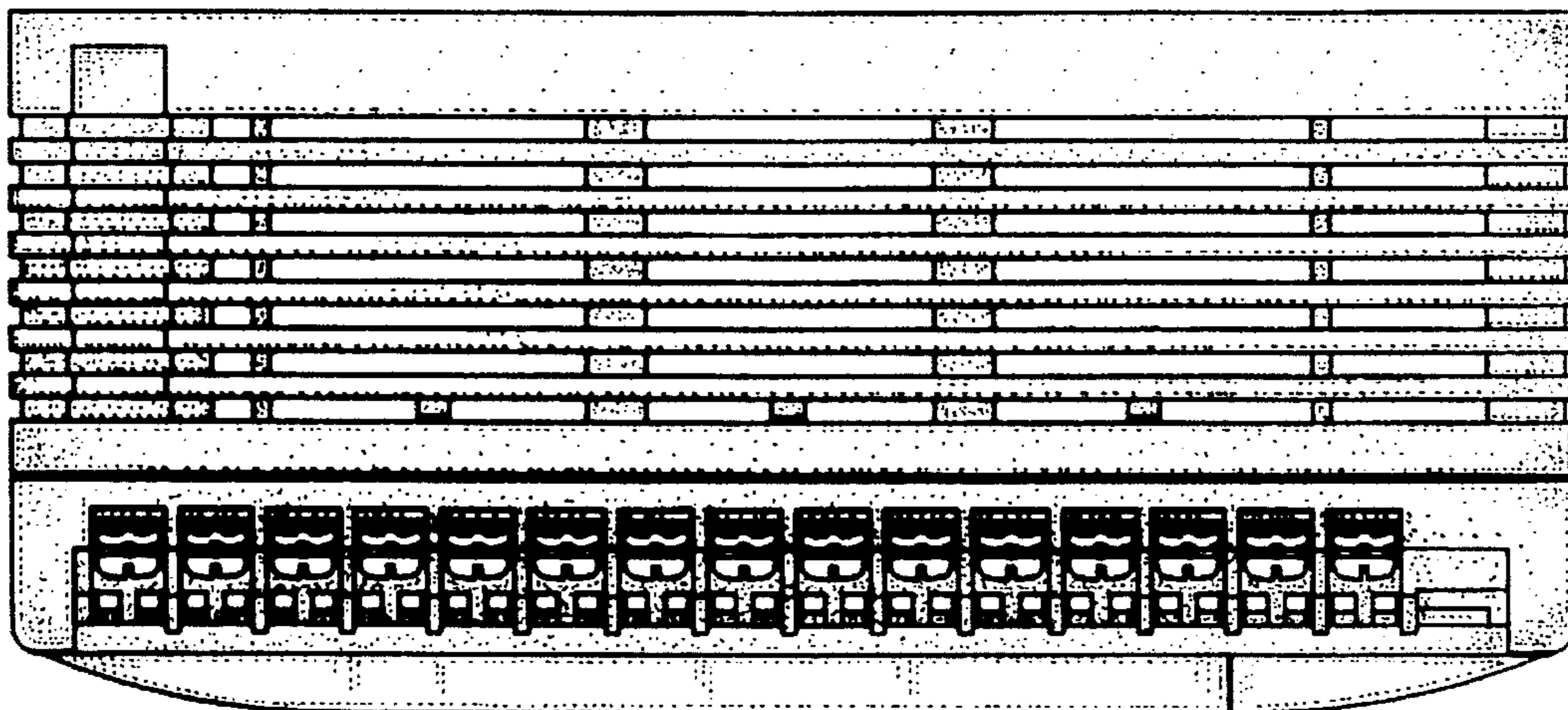


FIG. 4

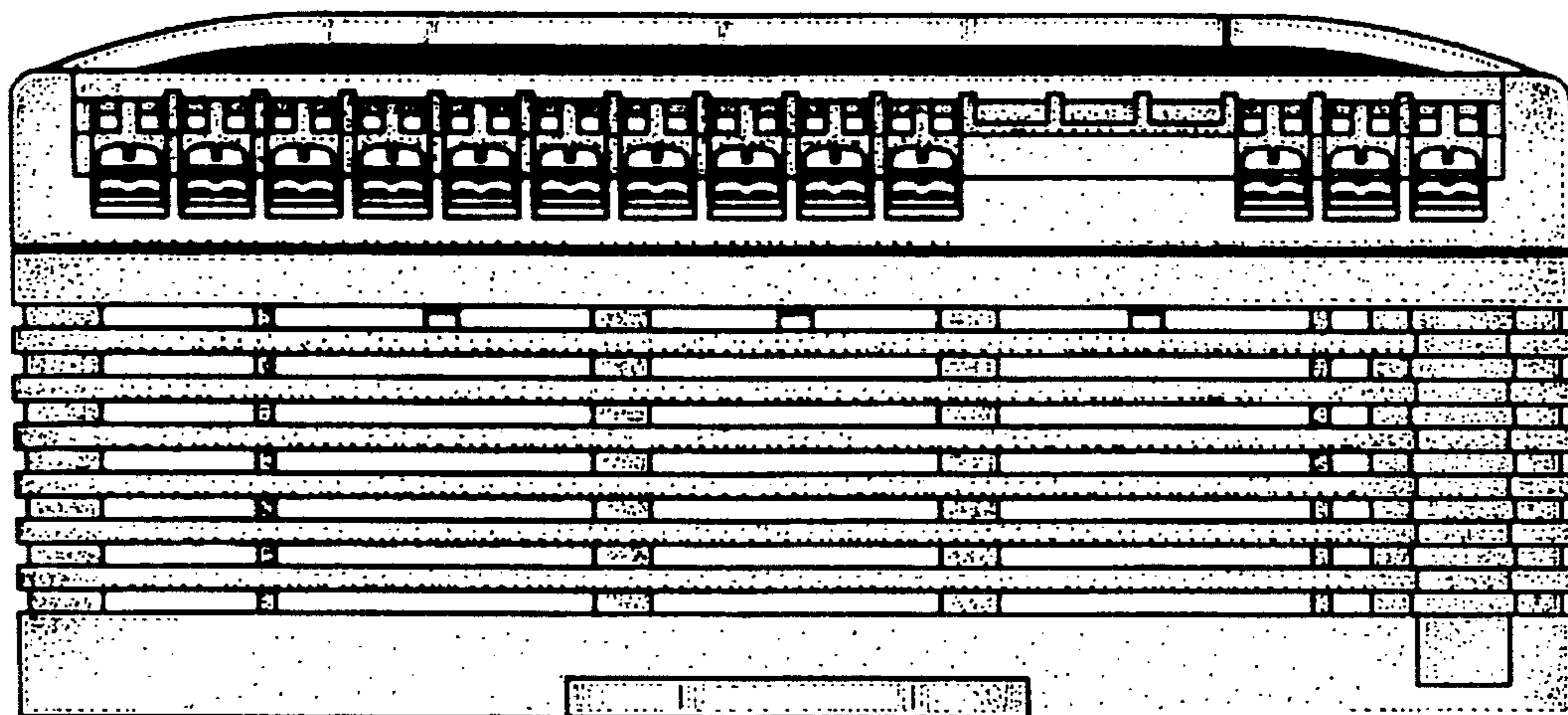


FIG. 5

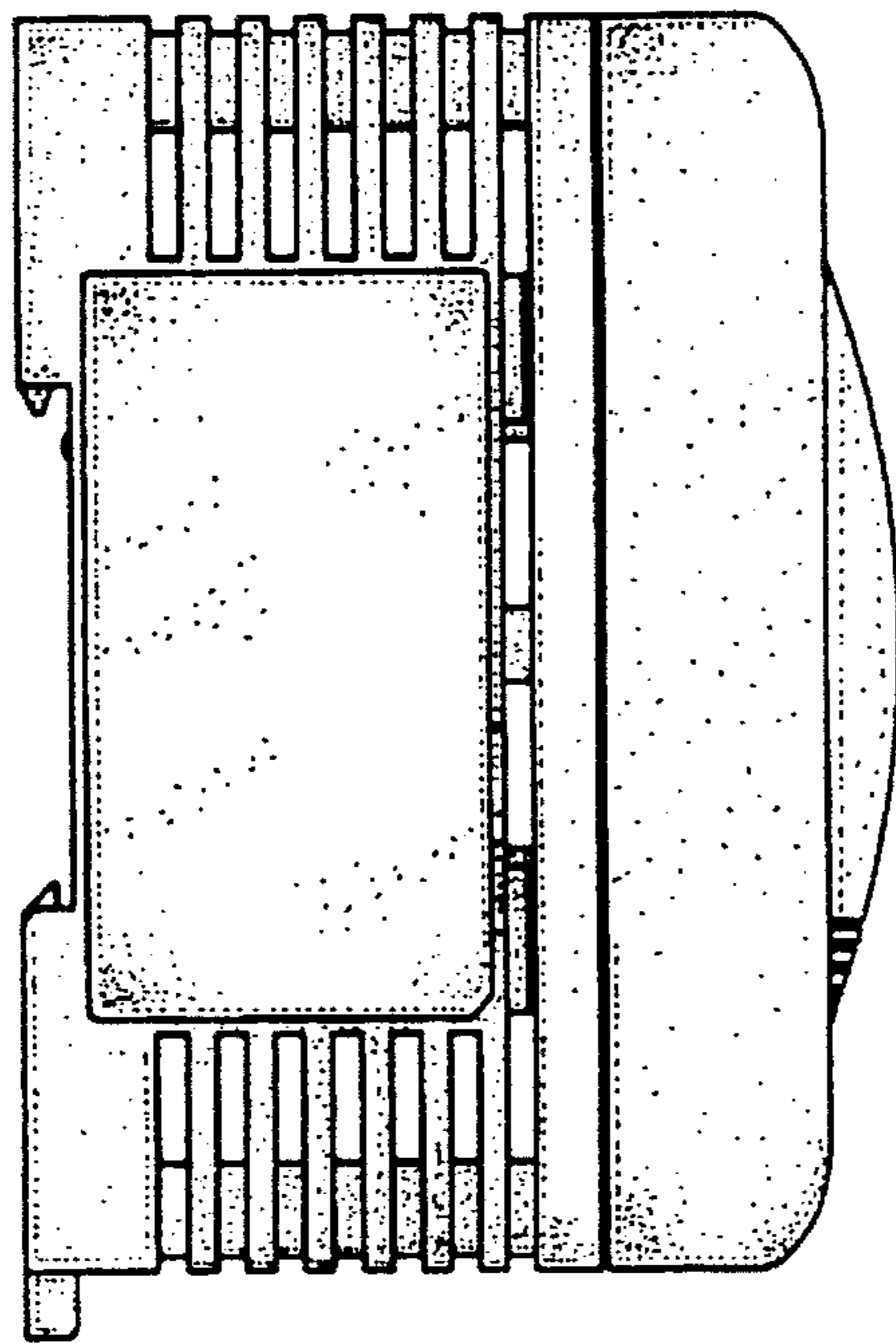


FIG. 6

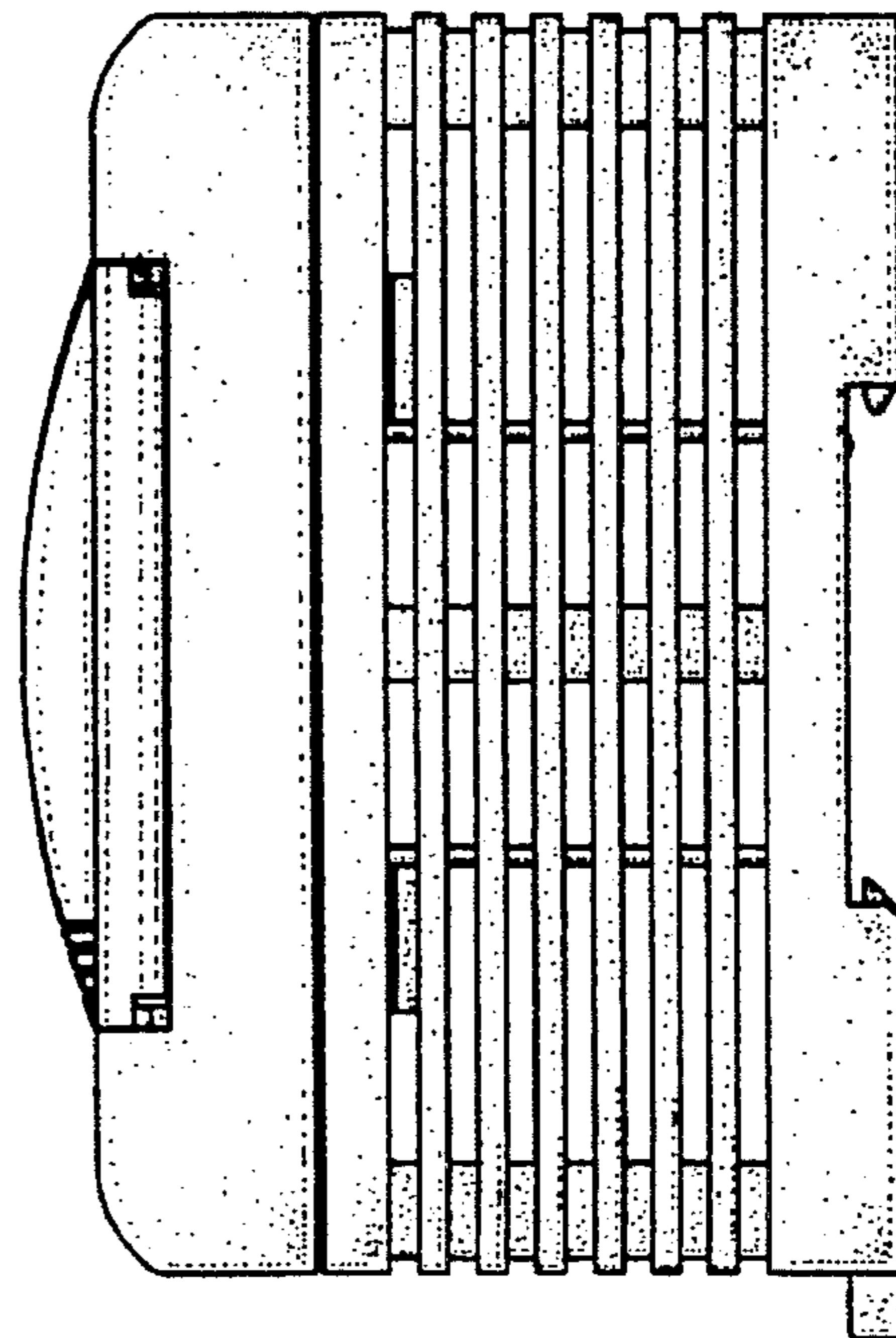
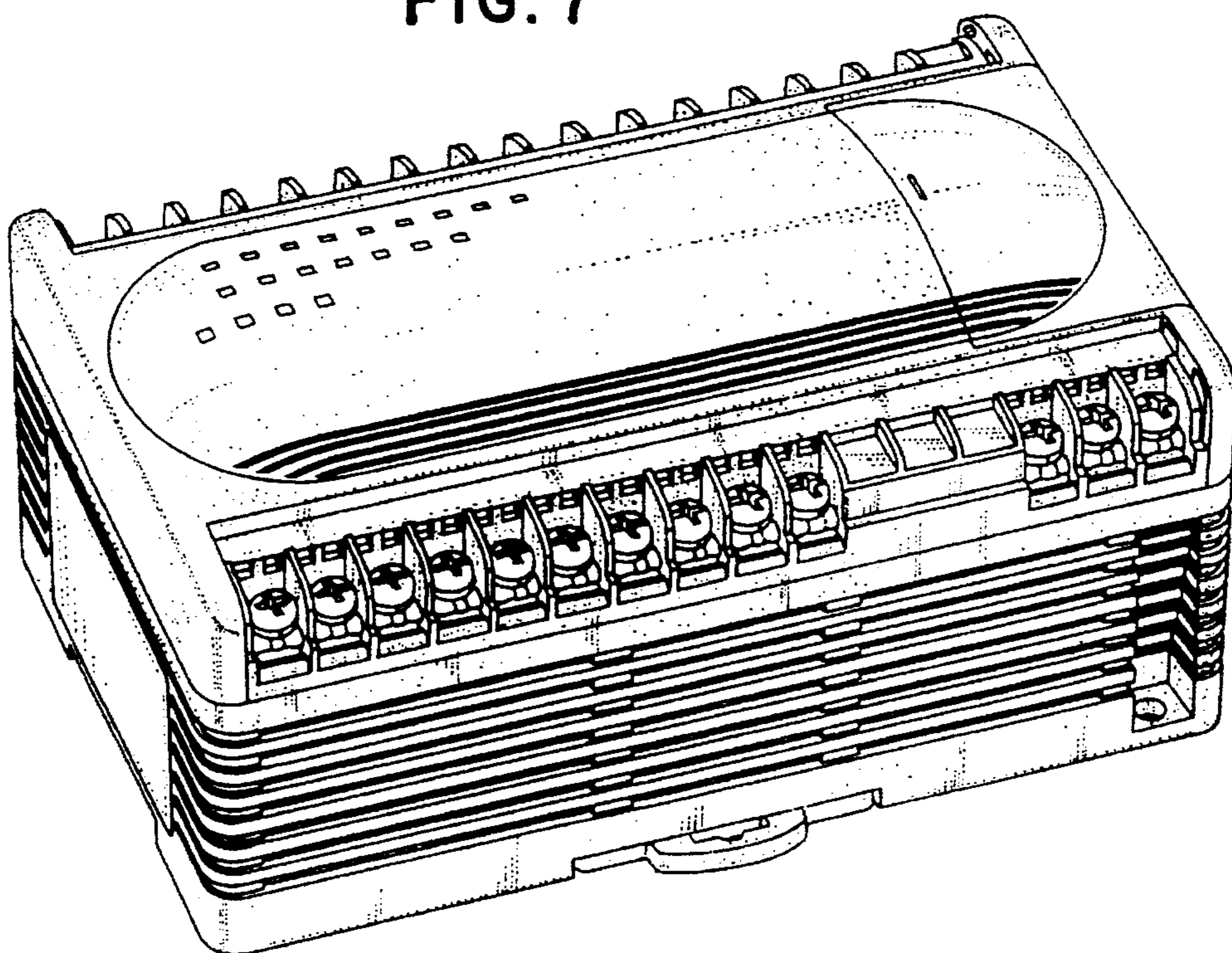


FIG. 7



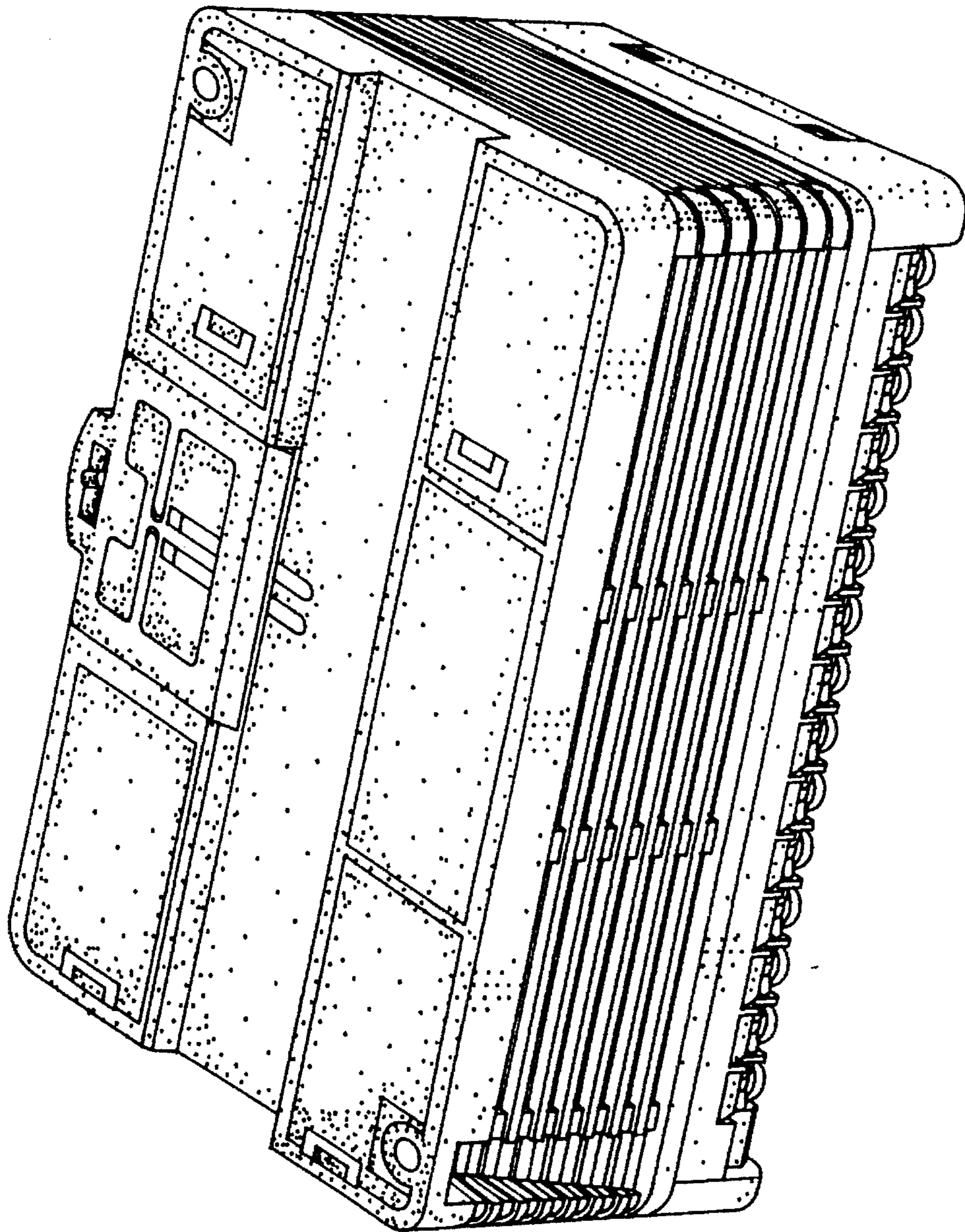


FIG. 8