



US00D368076S

# United States Patent [19]

[11] Patent Number: **Des. 368,076**

Kuramitsu et al.

[45] Date of Patent: **\*\*Mar. 19, 1996**

[54] **ELECTRONIC COMPUTER**

D. 315,724	3/1991	Kirchner et al.	.....	D14/102 X
D. 335,285	5/1993	Utsuki et al.	.....	D14/102
D. 353,366	12/1994	Bock et al.	.....	D14/102

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[73] Assignee: **NEC Corporation**, Tokyo, Japan

[\*\*] Term: **14 Years**

## [57] CLAIM

The ornamental design for an electronic computer, as shown.

[21] Appl. No.: **38,291**

## DESCRIPTION

[22] Filed: **May 2, 1995**

### [30] Foreign Application Priority Data

Nov. 2, 1994 [JP] Japan ..... 6-33542

[52] U.S. Cl. .... **D14/100**; D14/102

[58] Field of Search ..... D14/100, 102, D14/107-109; D13/162, 184, 199; D6/432, 436, 445, 448; 312/208.1, 223.2, 223.3, 194, 293; 360/97.01, 97.04, 98.01, 99.01, 99.12; 361/694

FIG. 1 is a perspective view of the top, front and right side of an electronic computer showing our new design;

FIG. 2 is a top plan view thereof;

FIG. 3 is a front elevational view thereof;

FIG. 4 is a rear elevational view thereof;

FIG. 5 is a right side elevational view thereof;

FIG. 6 is a left side elevational view thereof; and,

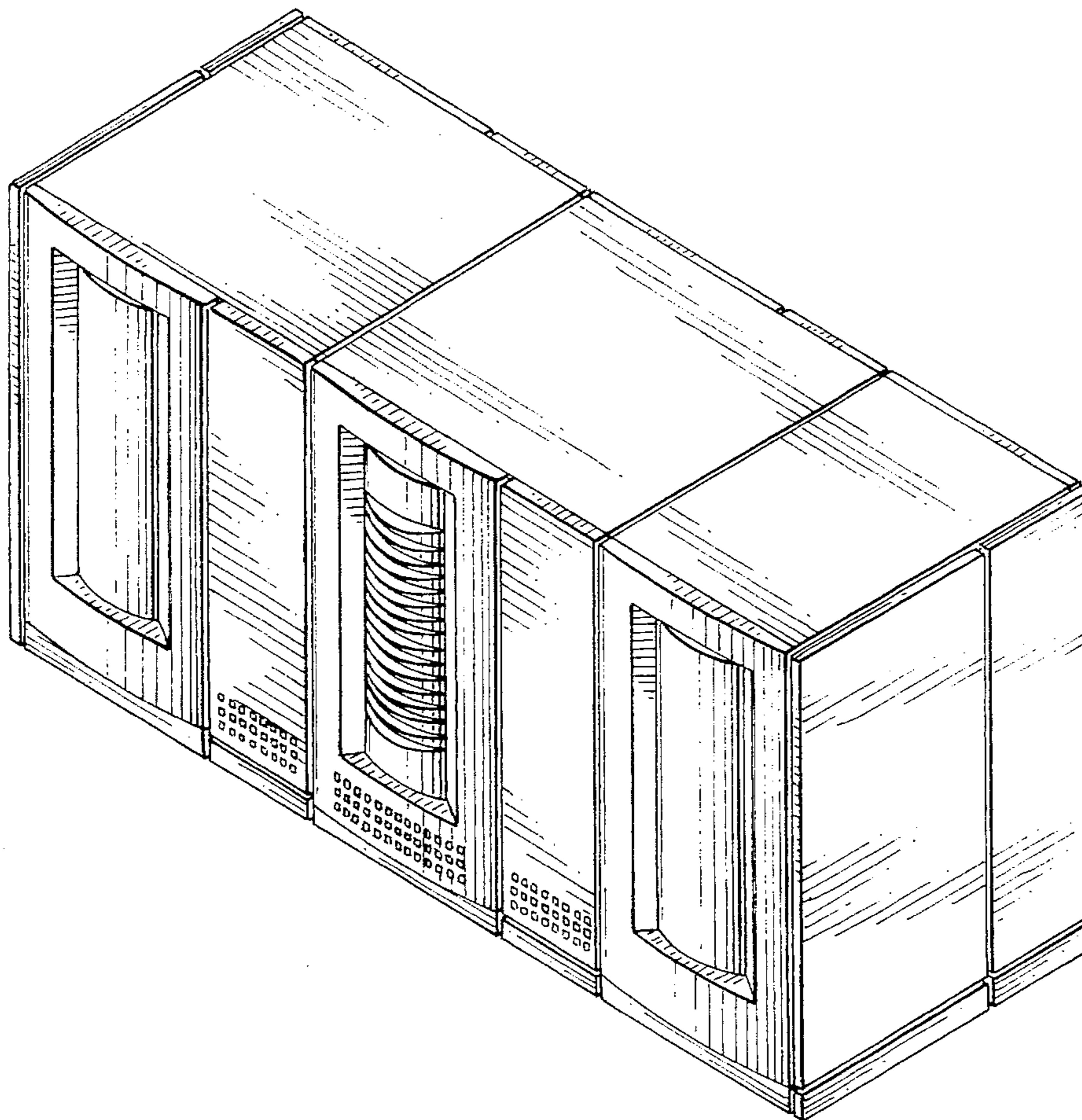
FIG. 7 is a bottom plan view thereof.

### [56] References Cited

#### U.S. PATENT DOCUMENTS

D. 283,817 5/1986 Sakamoto ..... D14/102

**1 Claim, 3 Drawing Sheets**



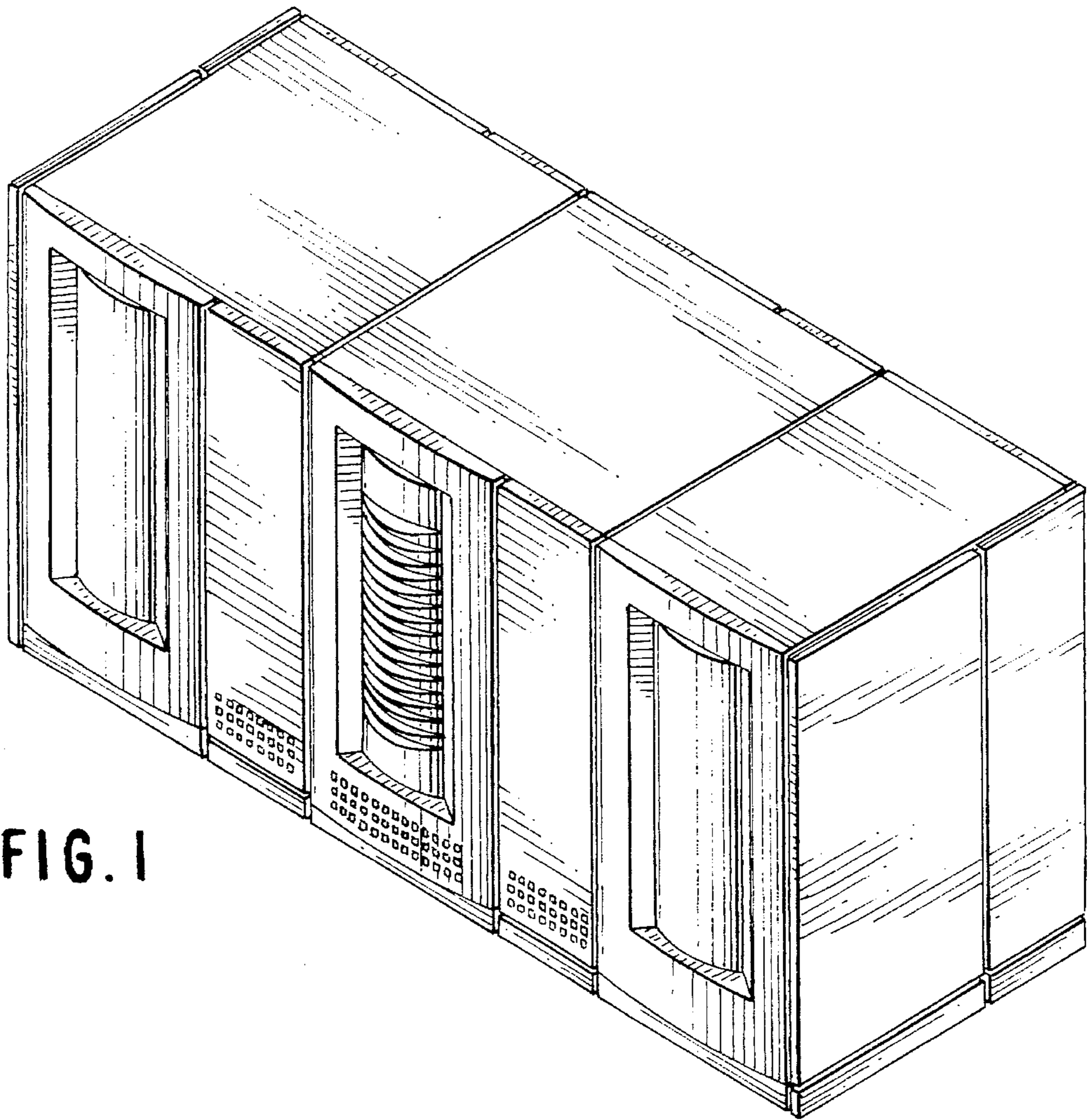


FIG. 1

FIG. 2

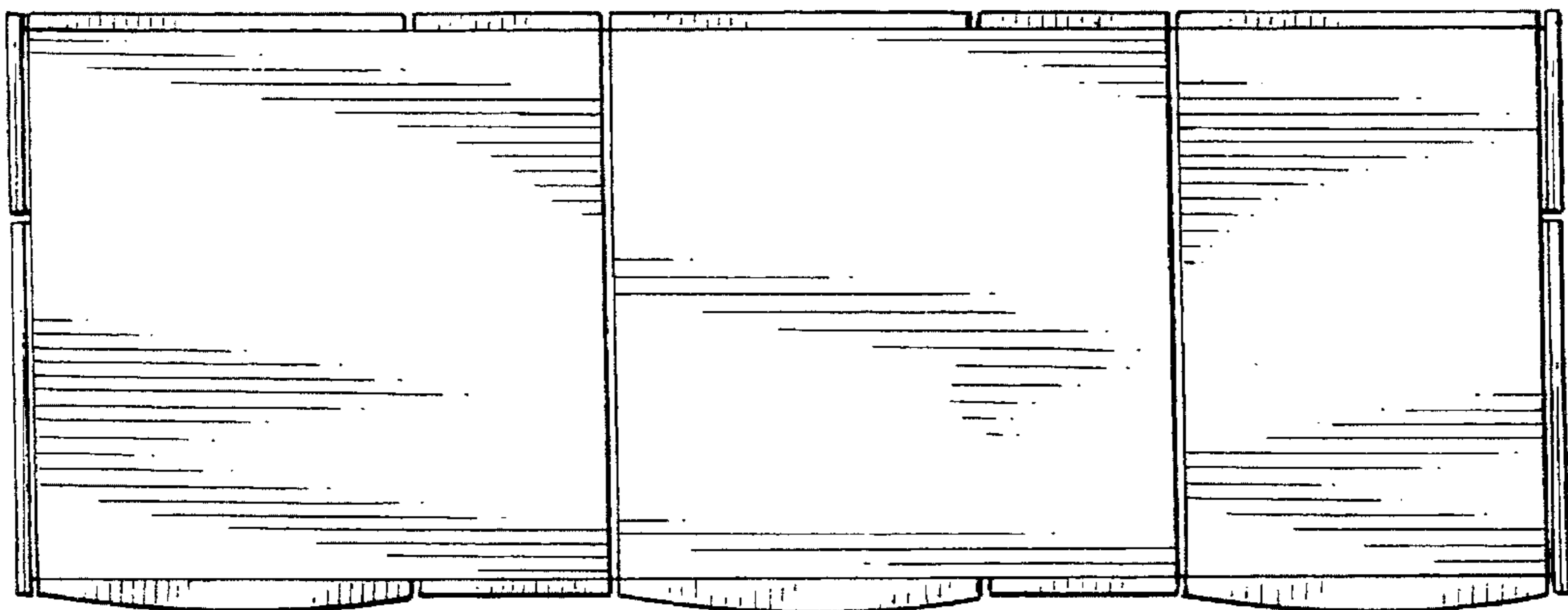


FIG. 3

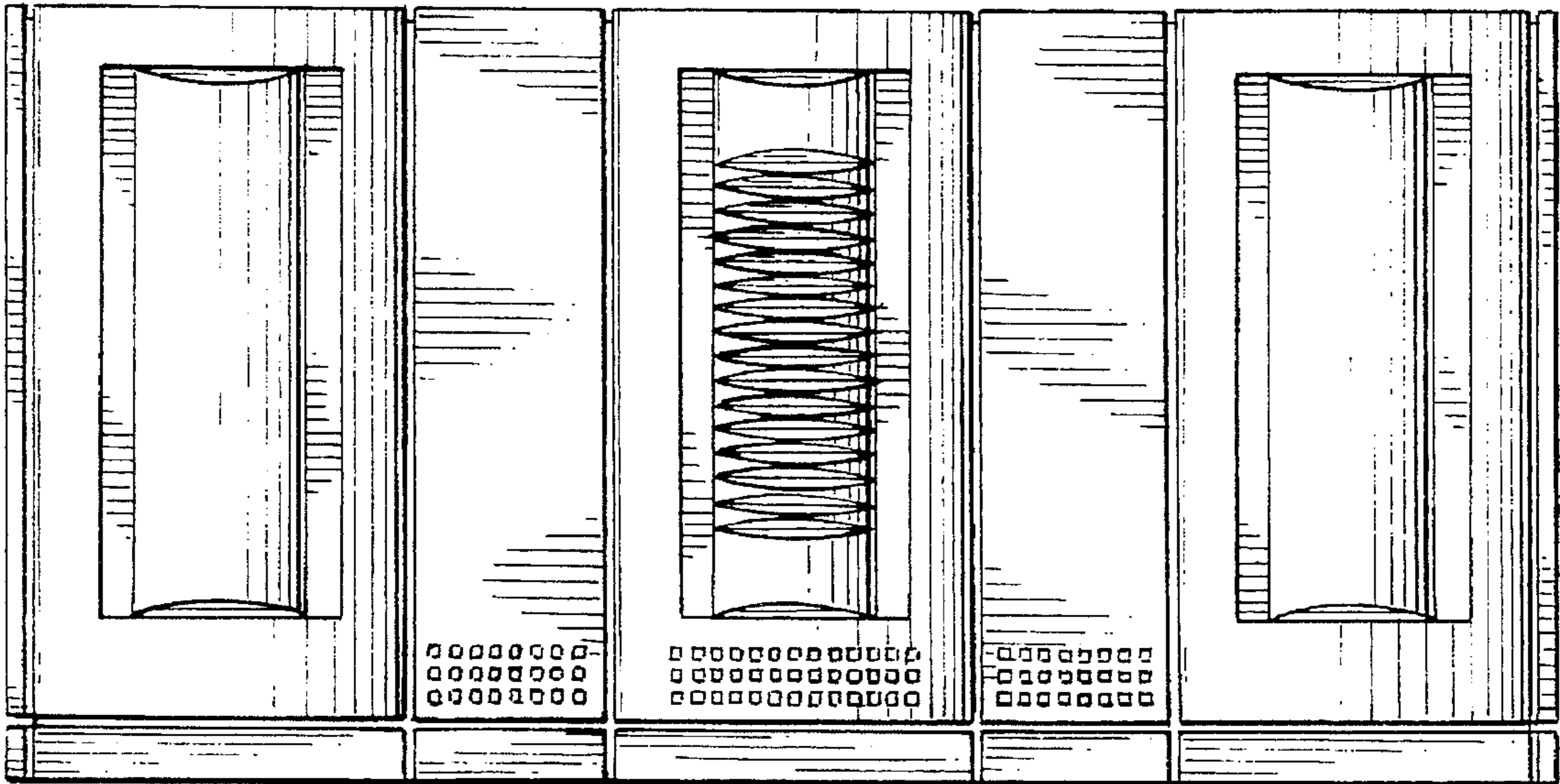


FIG. 4

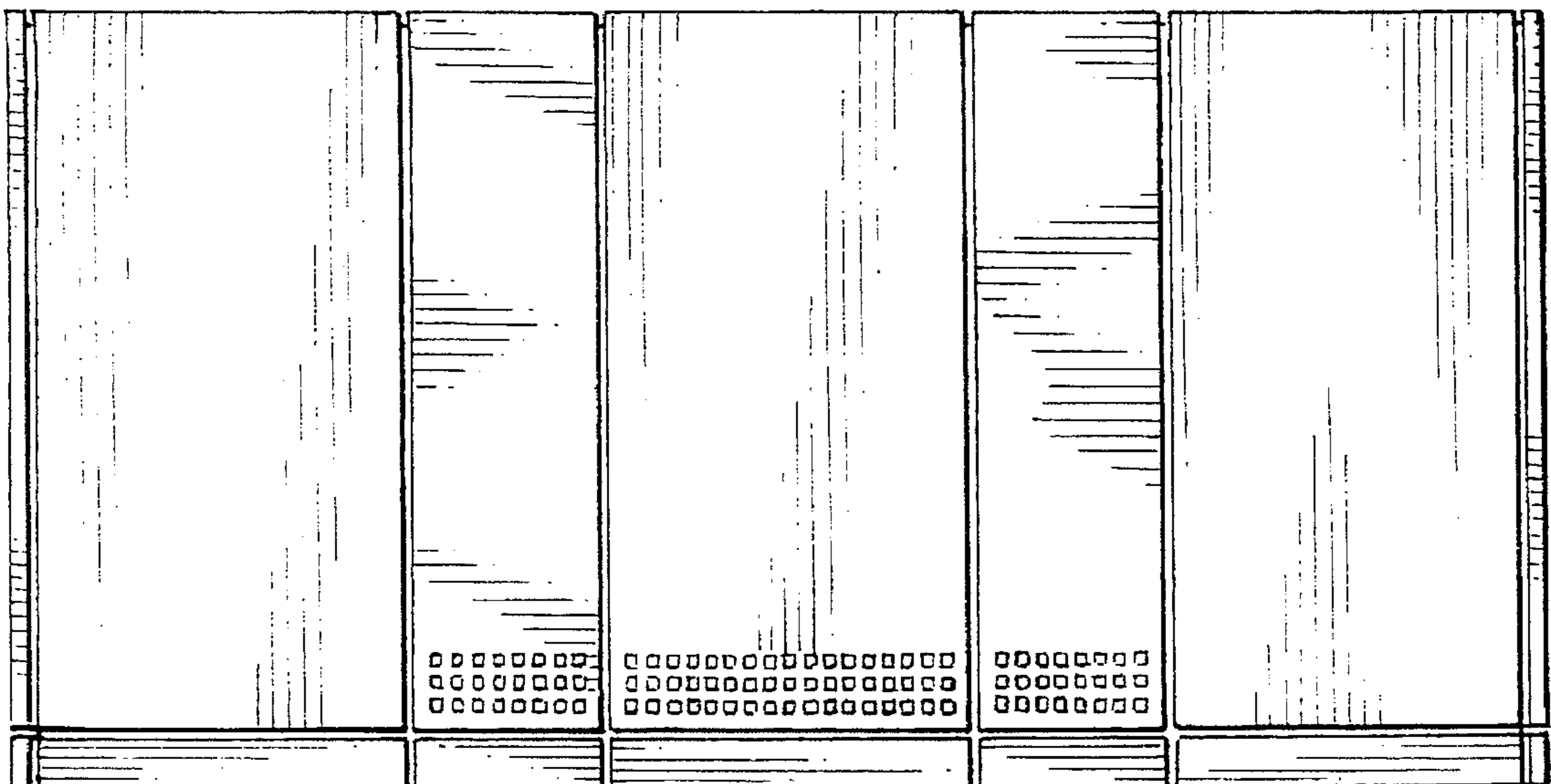


FIG. 5

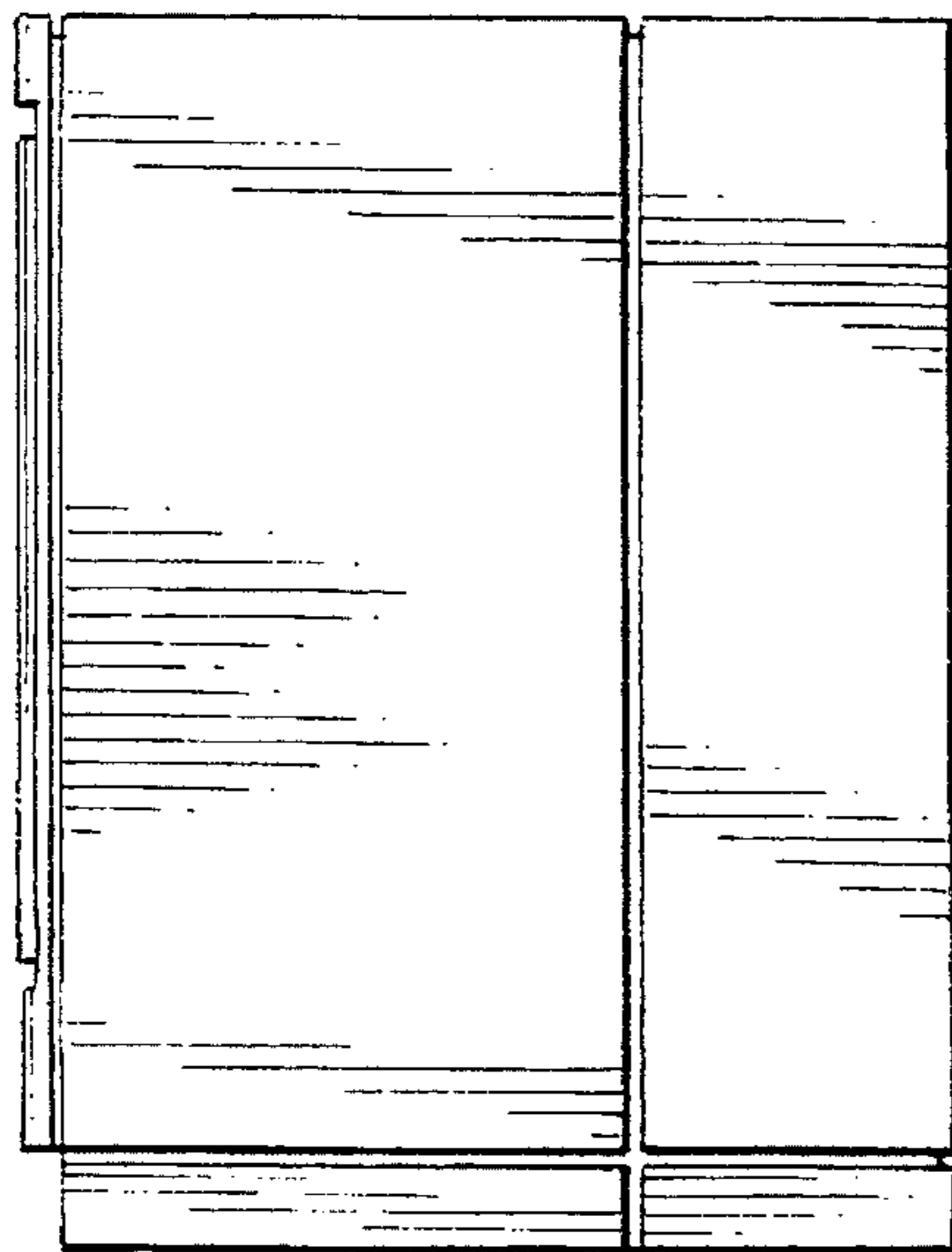


FIG. 6

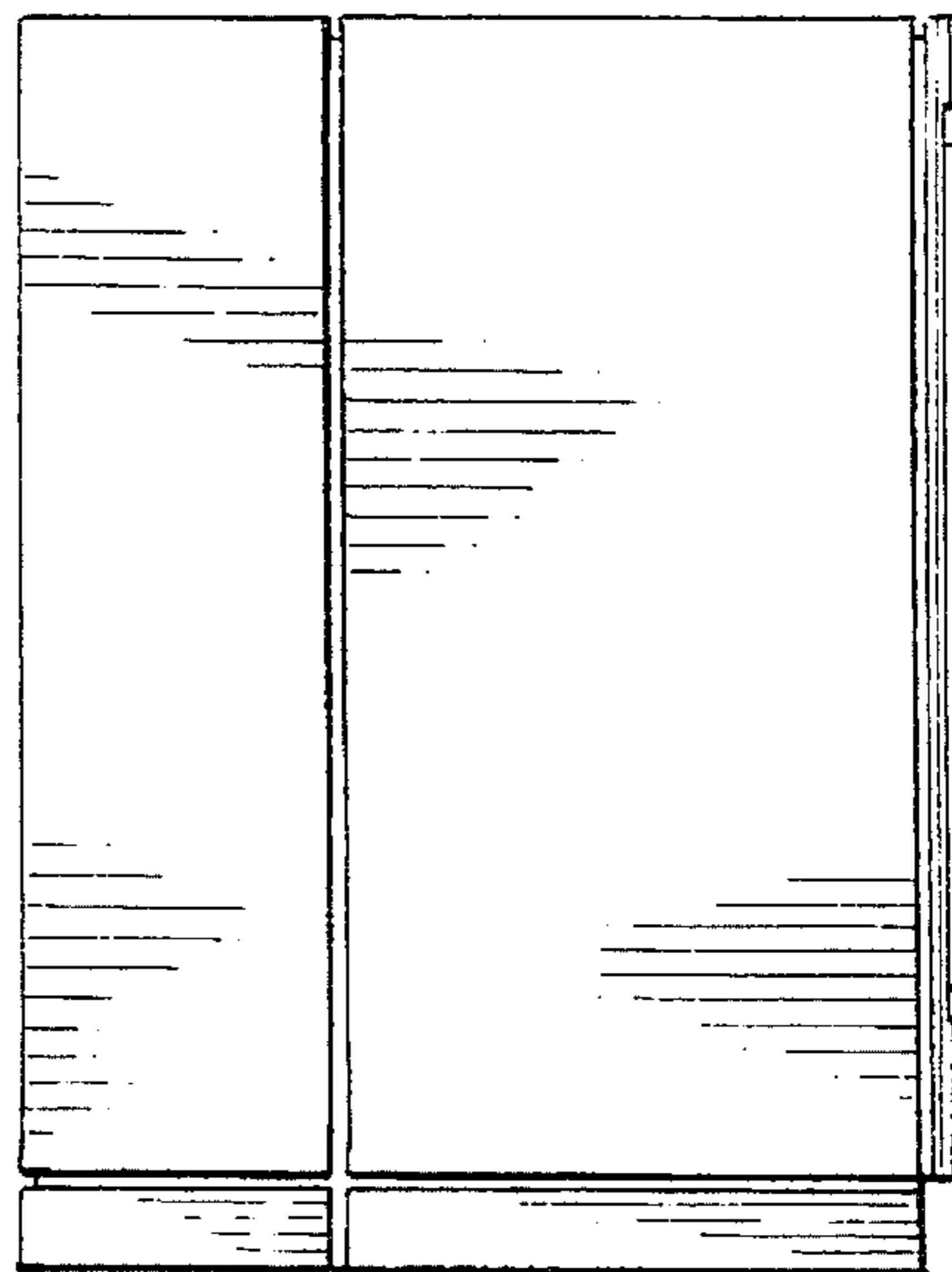


FIG. 7

