



US00D367856S

United States Patent [19]

[11] **Patent Number: Des. 367,856**

Neuss

[45] **Date of Patent: **Mar. 12, 1996**

[54] **PARALLEL PROCESSOR DISPLAY PANEL**

OTHER PUBLICATIONS

[75] Inventor: **Daniel A. Neuss**, Park City, Utah

Henry Dreyfuss, *Symbol Sourcebook*, © 1972, pp. 111, 176. Marshall Field & Co advertisement, Moorish Frets.

[73] Assignee: **Unisys Corporation**, Blue Bell, Pa.

Primary Examiner—M. H. Tung

[**] Term: **14 Years**

Attorney, Agent, or Firm—Charles J. Fassbender; Mark T. Starr; Robert R. Axenfeld

[21] Appl. No.: **22,412**

[57] **CLAIM**

[22] Filed: **May 3, 1994**

The ornamental design for a parallel processor display panel, as shown and described.

[52] **U.S. Cl.** **D14/114**

[58] **Field of Search** D14/114, 239; D10/125; D20/10, 11, 27; D11/95-96, 114; D5/4, 62-63, 39, 58

DESCRIPTION

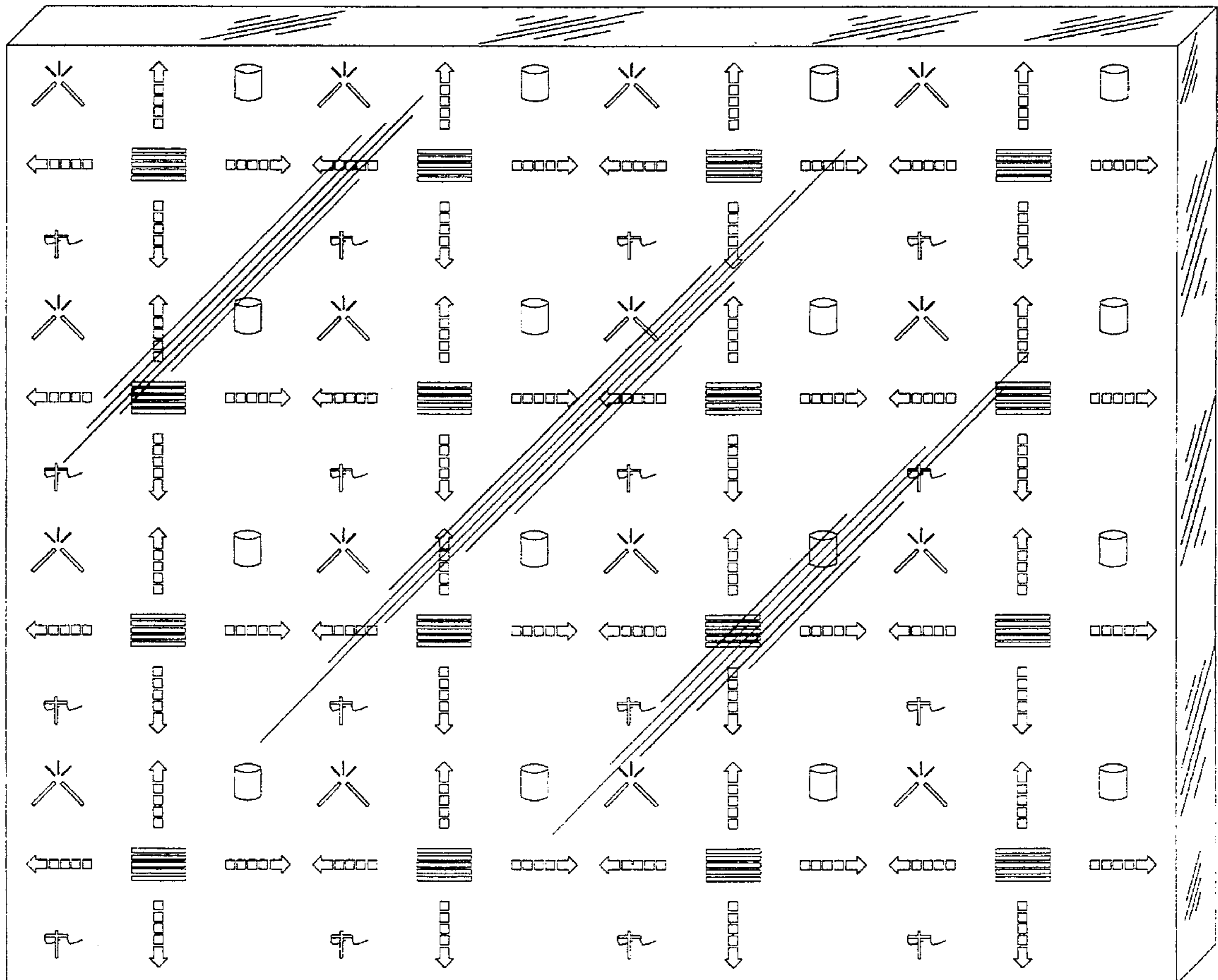
[56] **References Cited**

U.S. PATENT DOCUMENTS

D. 12,929	5/1882	Samson	D5/47
D. 48,106	11/1915	Friedman	D11/114
D. 59,590	11/1921	Heathcote	D11/114
D. 277,876	3/1985	Gordon	D20/11
D. 278,320	4/1985	Primoff	D11/96
D. 335,125	4/1993	Frankel	D20/11 X
D. 344,732	3/1994	Kannes	D14/239

FIG. 1 is a front perspective view of a parallel processor display panel showing my new design; FIG. 2 is an enlarged view of a portion of the design which repeats in the FIG. 1 parallel processor display panel; FIG. 3 is a view of the top, and bottom, of the parallel processor display panel in FIG. 1; and, FIG. 4 is a view of the left side, and right side, of the parallel processor display panel in FIG. 1; When the FIG. 1 panel is viewed from the rear, the design in FIG. 1 is seen as its mirror image.

1 Claim, 3 Drawing Sheets



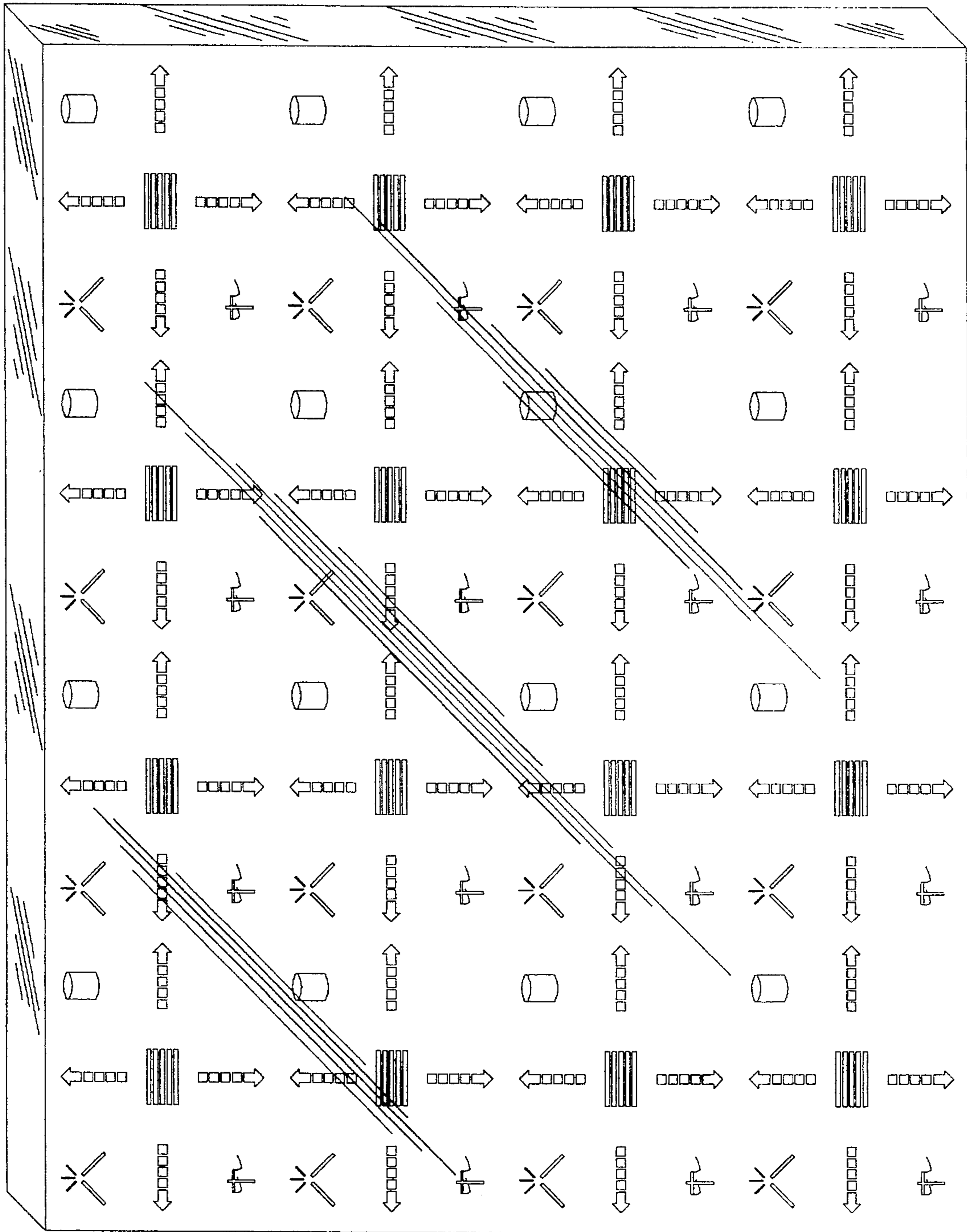


FIG. 1

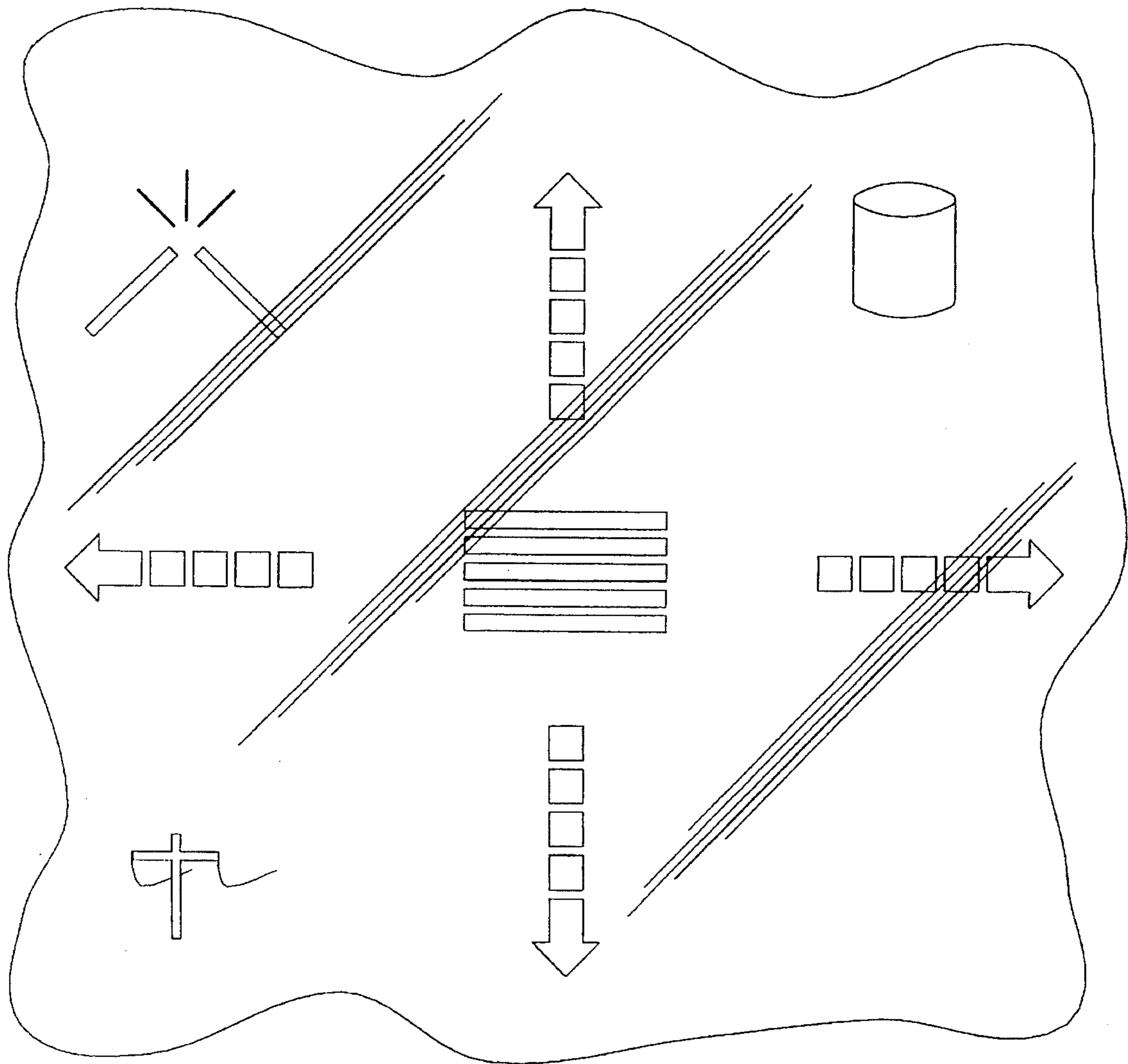


FIG. 2



FIG. 4



FIG. 3