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United States Patent [19]

Sakashita et al.

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[54] CIRCUIT BOARD WITH MOUNTING FLANGE

[75] Inventors: Hiroshi Sakashita; Yukio Fukuda; Jun Yamashita, all of Nagano, Japan

[73] Assignee: Sankyo Seiki Mfg. Co., Ltd., Nagano, Japan

[**] Term: 14 Years

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[30] Foreign Application Priority Data

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[52] U.S. Cl. D13/182

[58] Field of Search D13/182; 361/748, 752, 361/809; 174/250, 253, 255

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Primary Examiner—Joel Sincavage

Attorney, Agent, or Firm—McAulay Fisher Nissen
Goldberg & Kiel

[57] CLAIM

The ornamental design for a circuit board with mounting flange, as shown and described.

DESCRIPTION

FIG. 1 is a top view of a circuit board with mounting flange showing our new design;

FIG. 2 is a side view thereof;

FIG. 3 is a side view thereof taken orthogonal to FIG. 4;

FIG. 4 is a bottom plan view thereof;

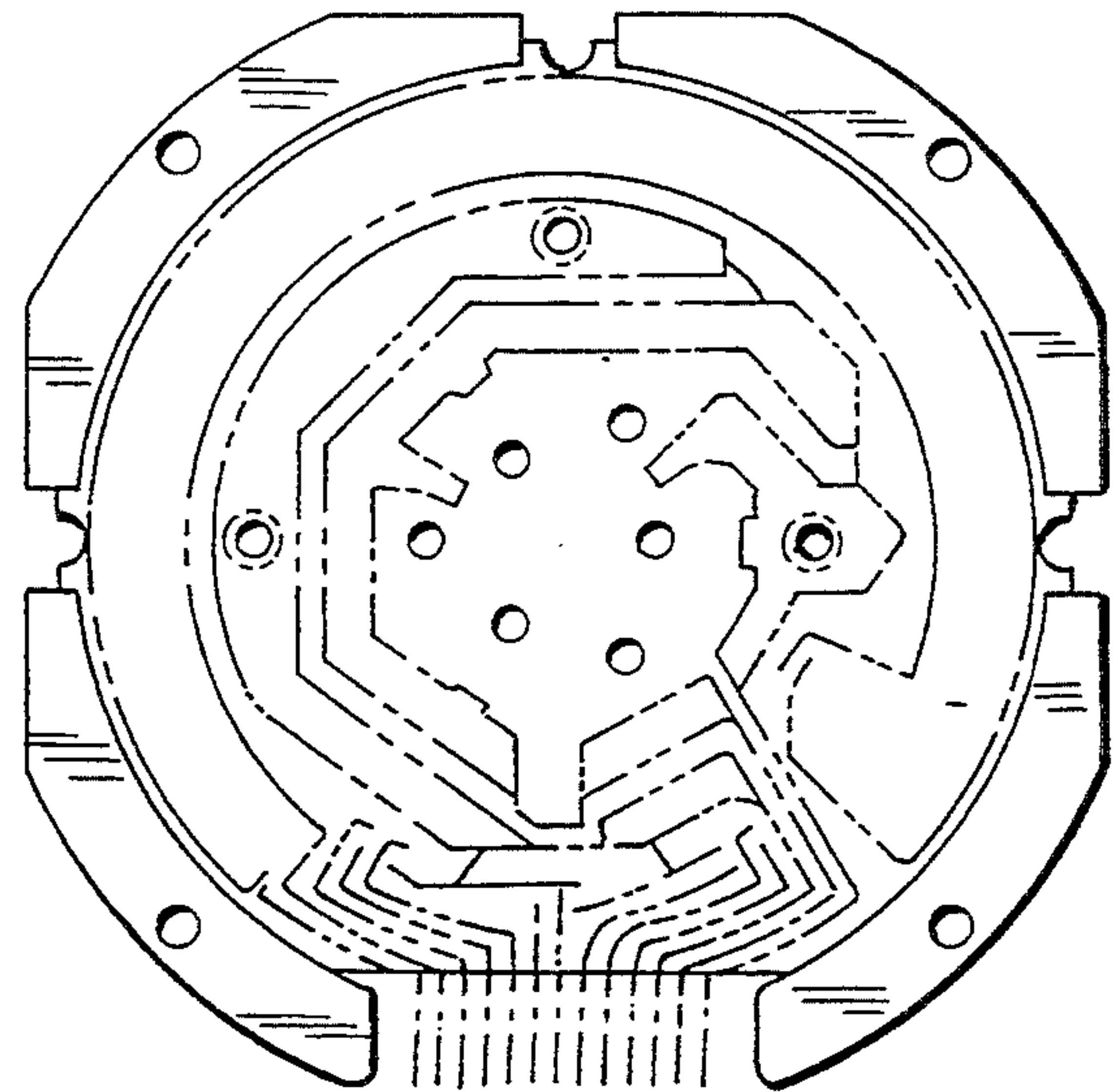
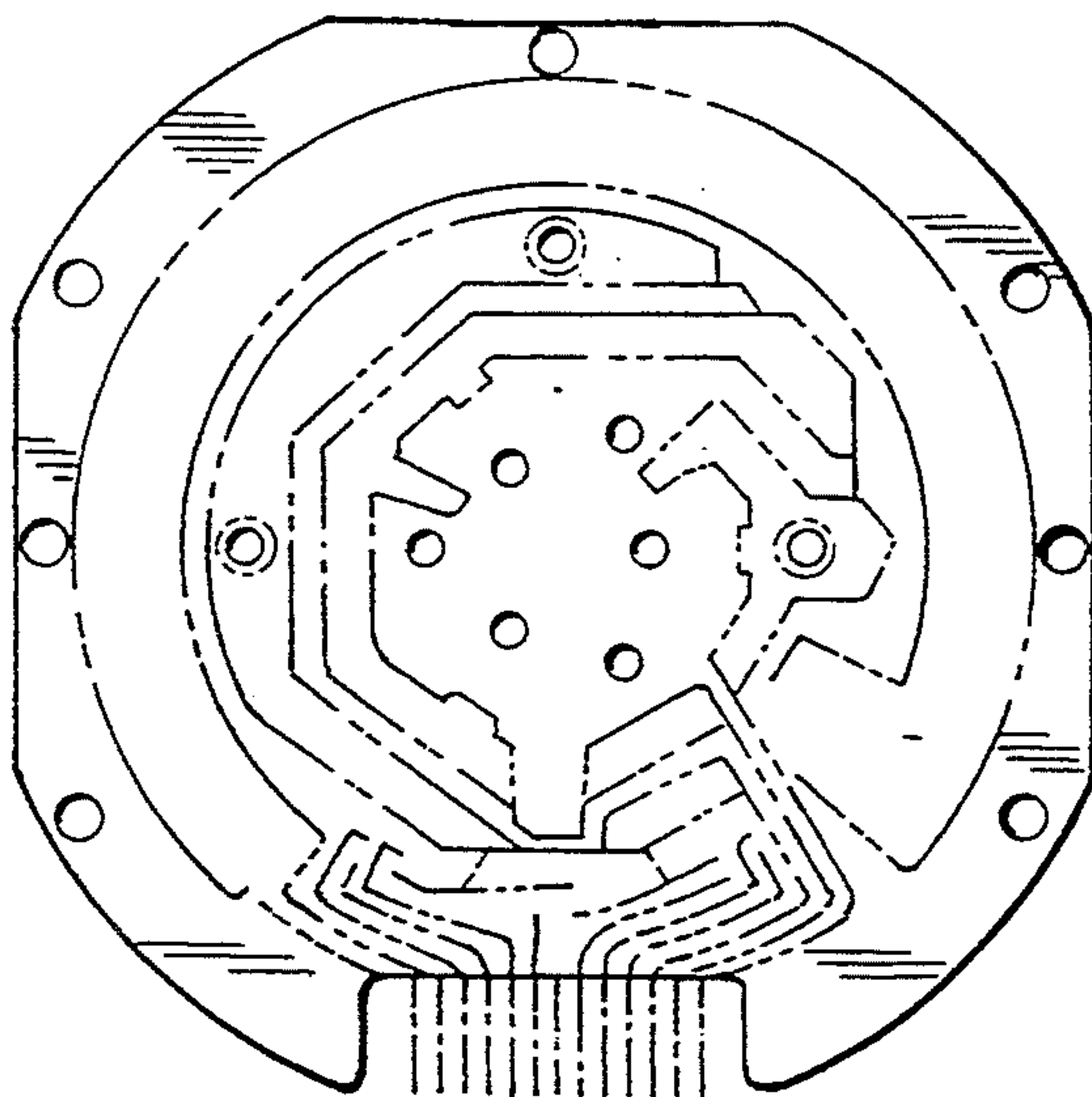
FIG. 5 is a top view of a circuit board with mounting flange showing a second embodiment of our new design;

FIG. 6 is a side view thereof;

FIG. 7 is a side view thereof taken orthogonal to FIG. 8; and,

FIG. 8 is a bottom plan view thereof.

The broken line showing is included for the purpose of illustrating environmental structure only and forms no part of the claimed design.



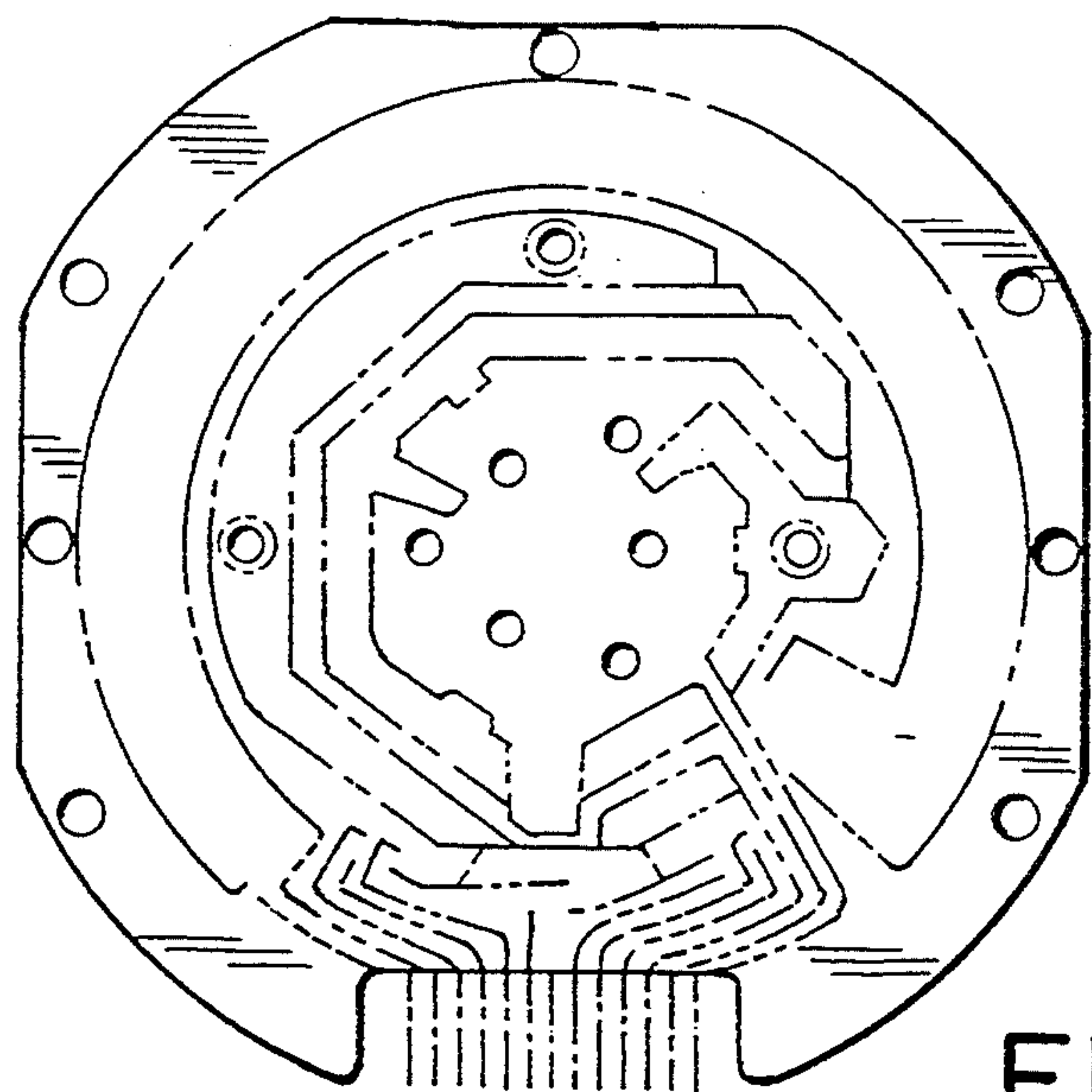


FIG. 1



FIG. 2

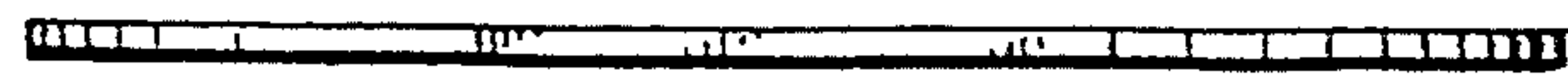


FIG. 3

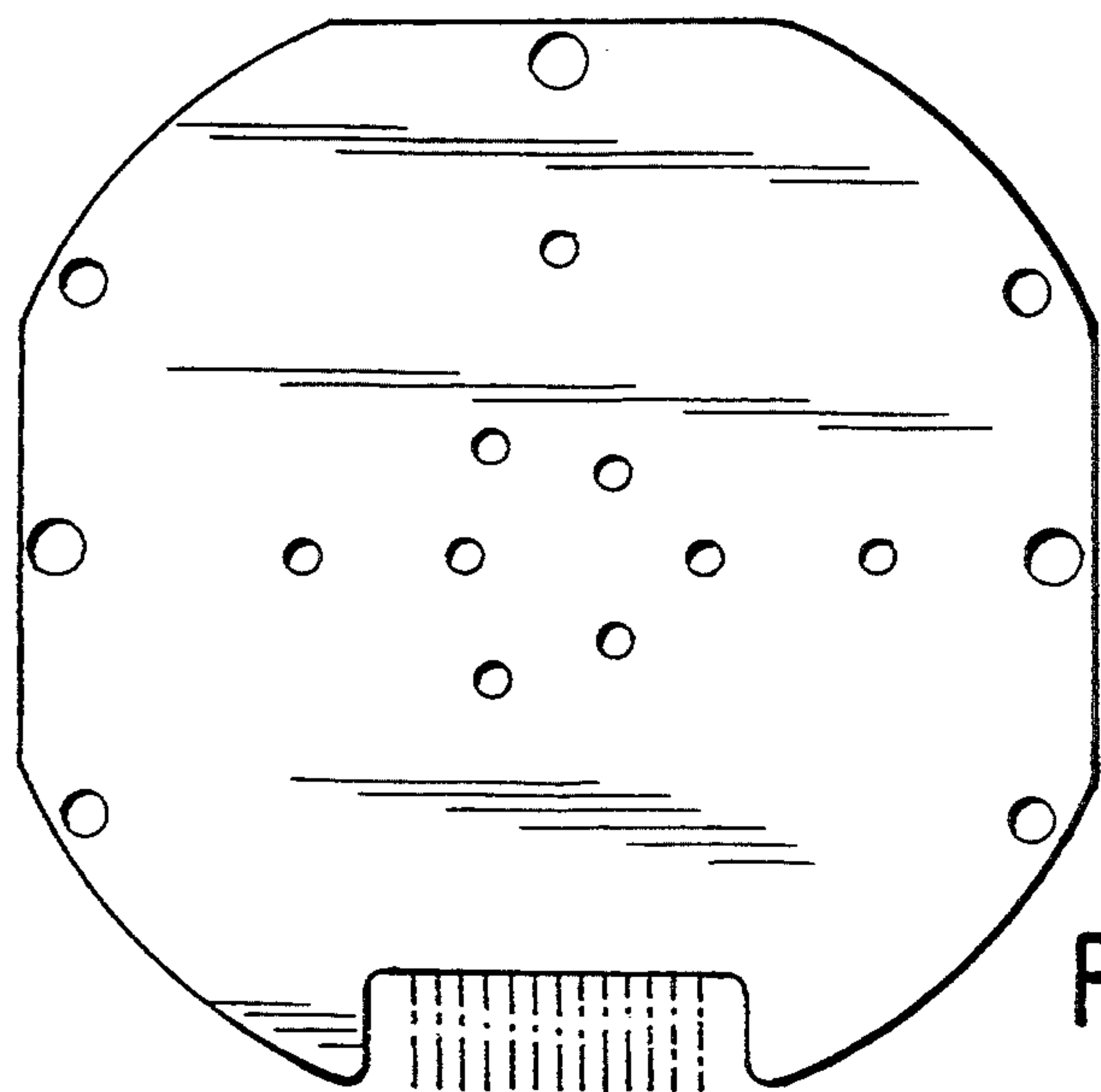


FIG. 4

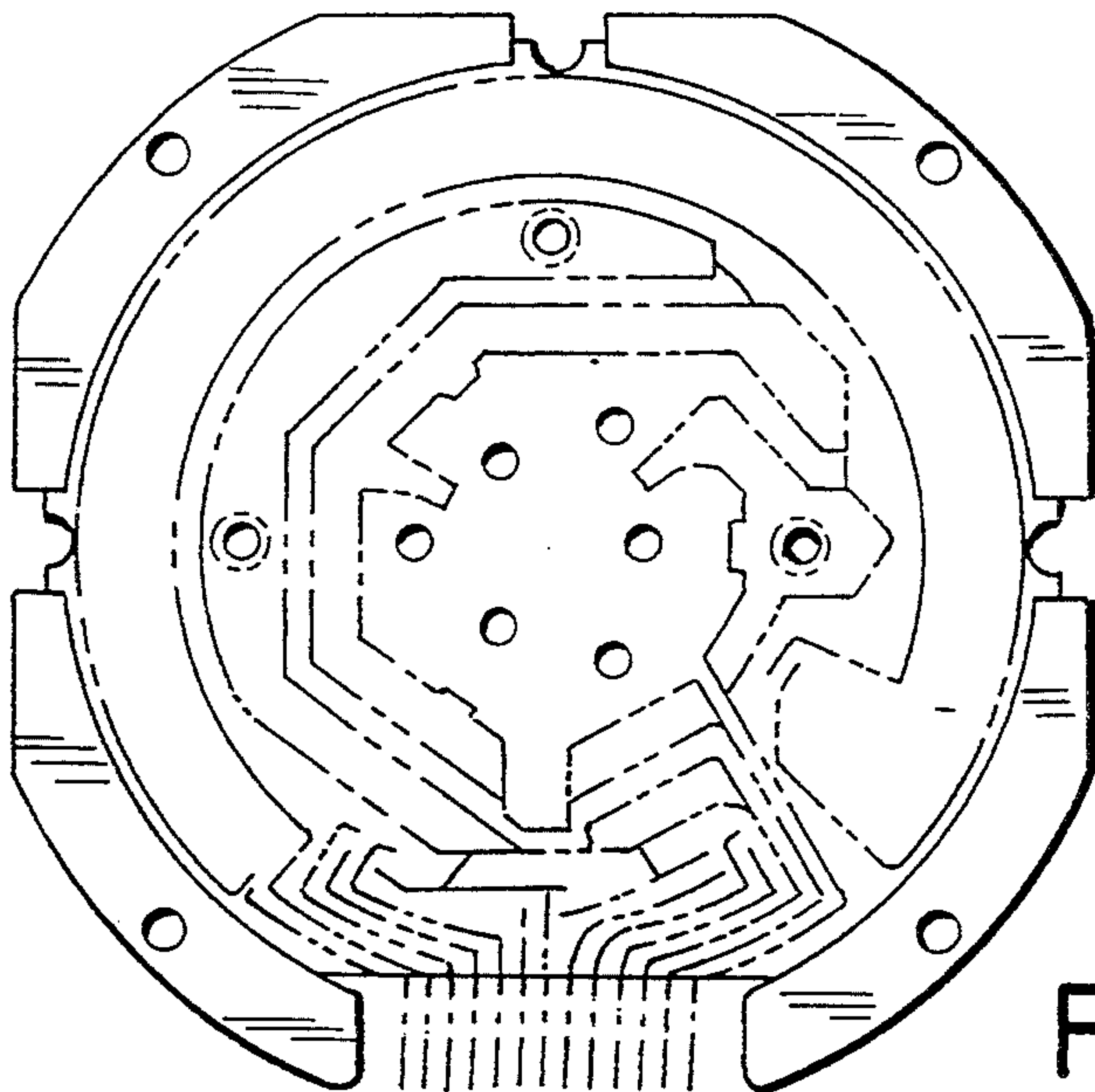


FIG. 5



FIG. 6



FIG. 7

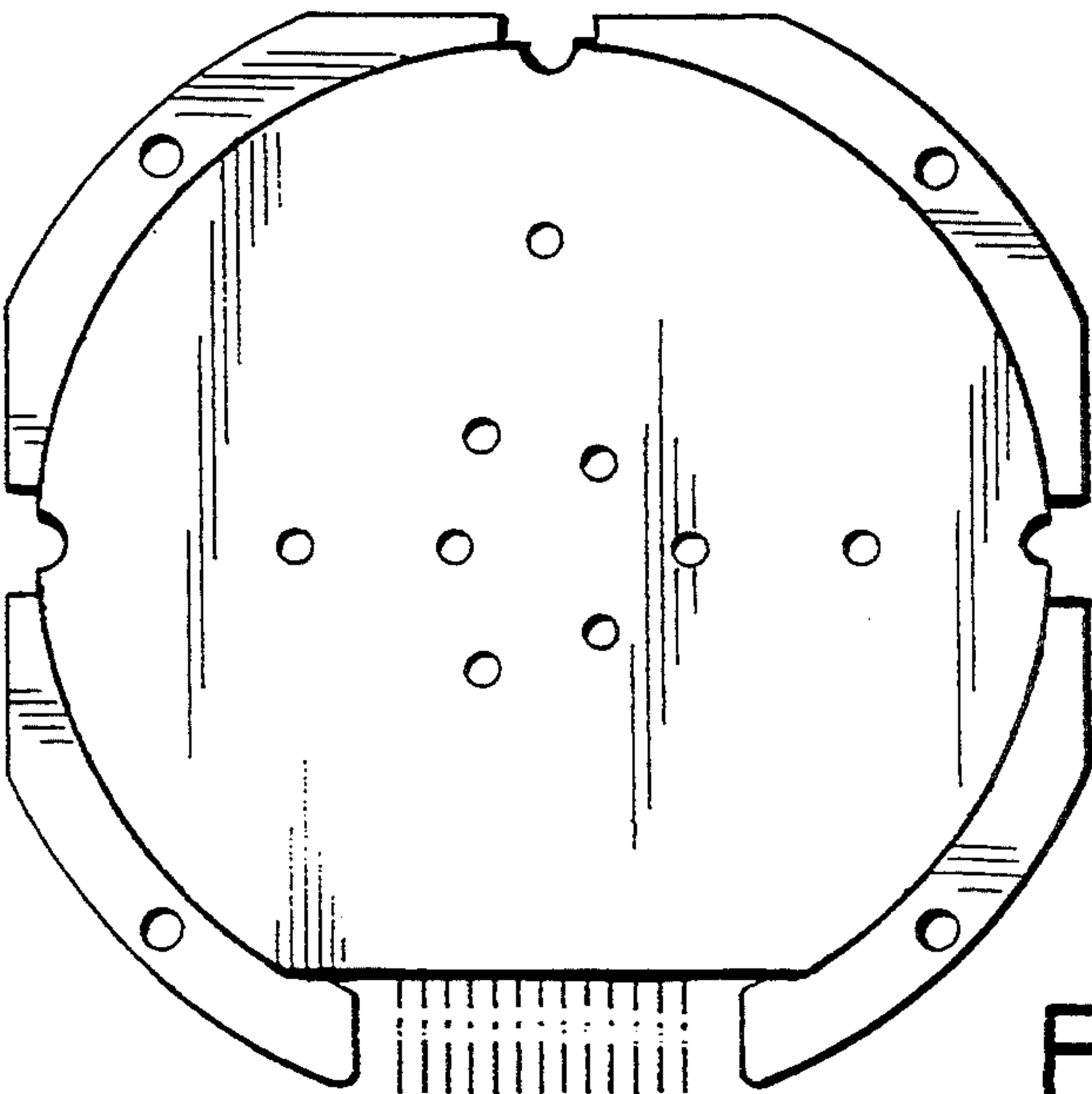


FIG. 8