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# United States Patent [19]

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Pecukonis et al.

[45] Date of Patent: **\*\* Feb. 21, 1995**

[54] **CIRCUIT TRACER SIGNAL GENERATOR**

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Colo.

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[\*\*] Term: **14 Years**

[57] **CLAIM**

[21] Appl. No.: **14,082**

The ornamental design for a circuit tracer signal generator, as shown and described.

[22] Filed: **Oct. 12, 1993**

**DESCRIPTION**

[52] U.S. Cl. .... **D10/78**

[58] Field of Search ..... D10/46, 78; 324/66,  
324/67, 508, 156, 529, 602, 713; 181/141;  
340/384.6, 693, 656; 379/21, 419, 433

FIG. 1 is a perspective view of a circuit tracer signal generator showing our new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a top view thereof;

FIG. 4 is a rear elevational view thereof;

FIG. 5 is a right side elevational view thereof; and,

FIG. 6 is a left side elevational view thereof.

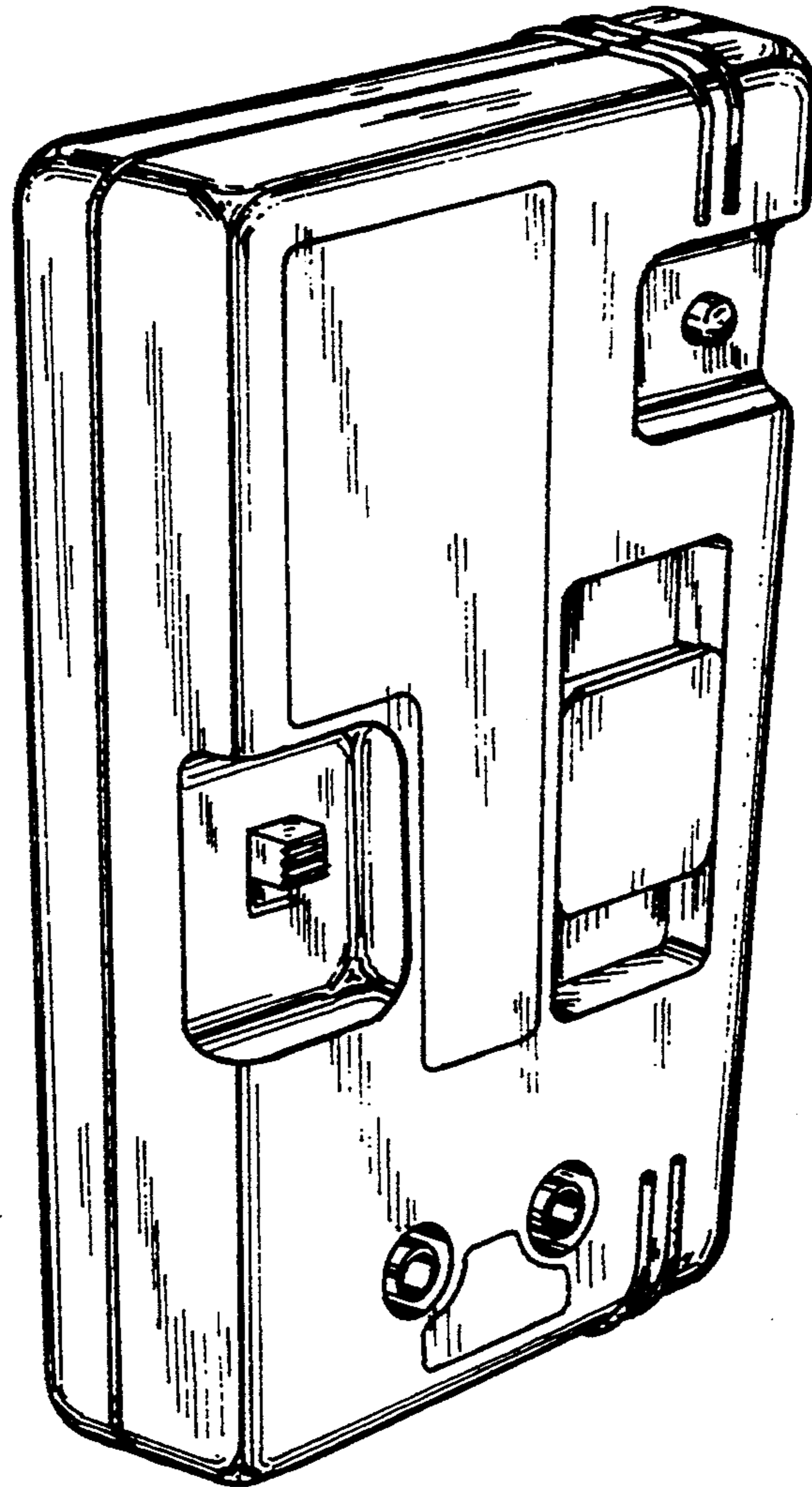
The bottom view thereof is identical to the top view shown in FIG. 3.

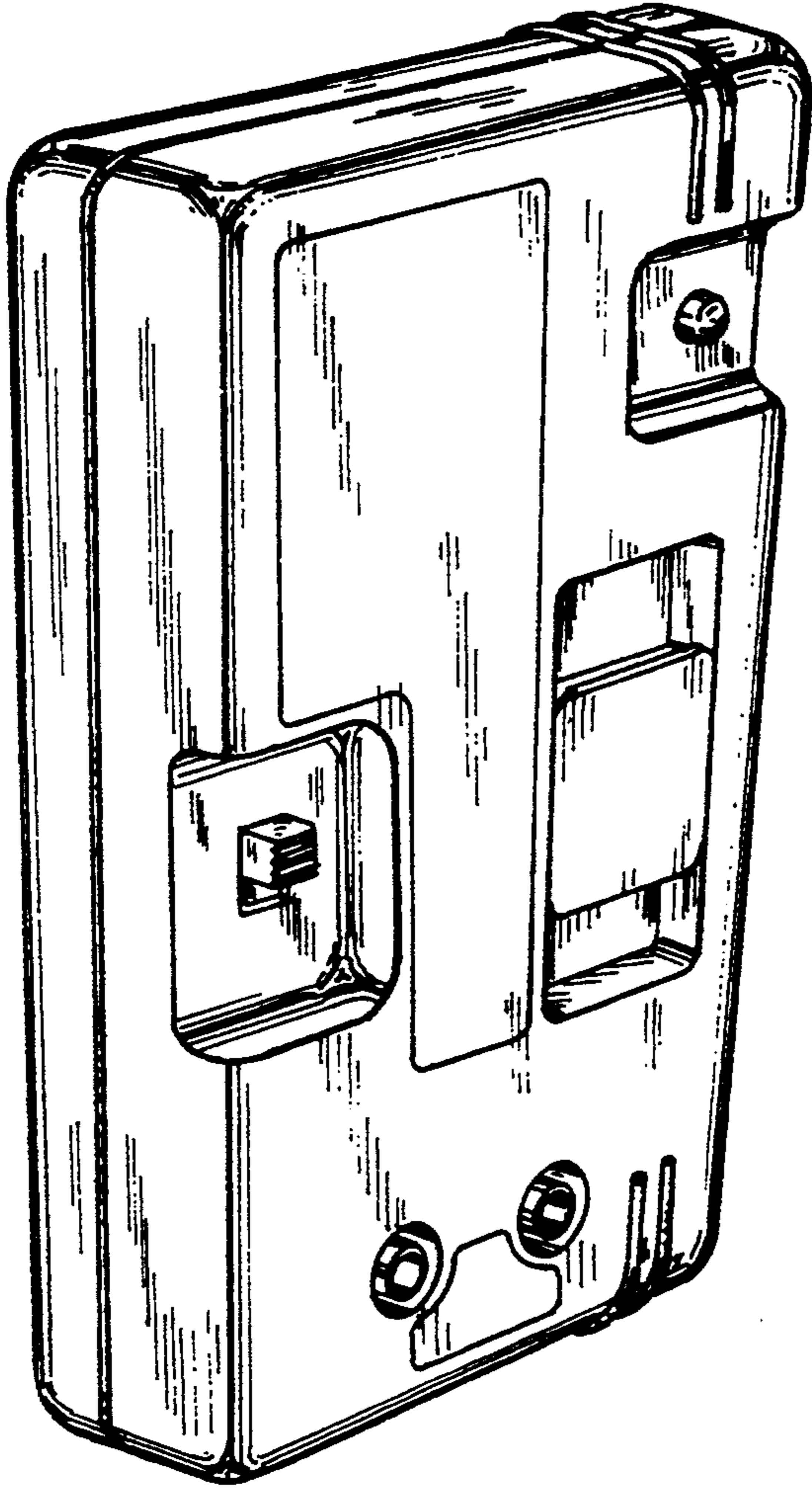
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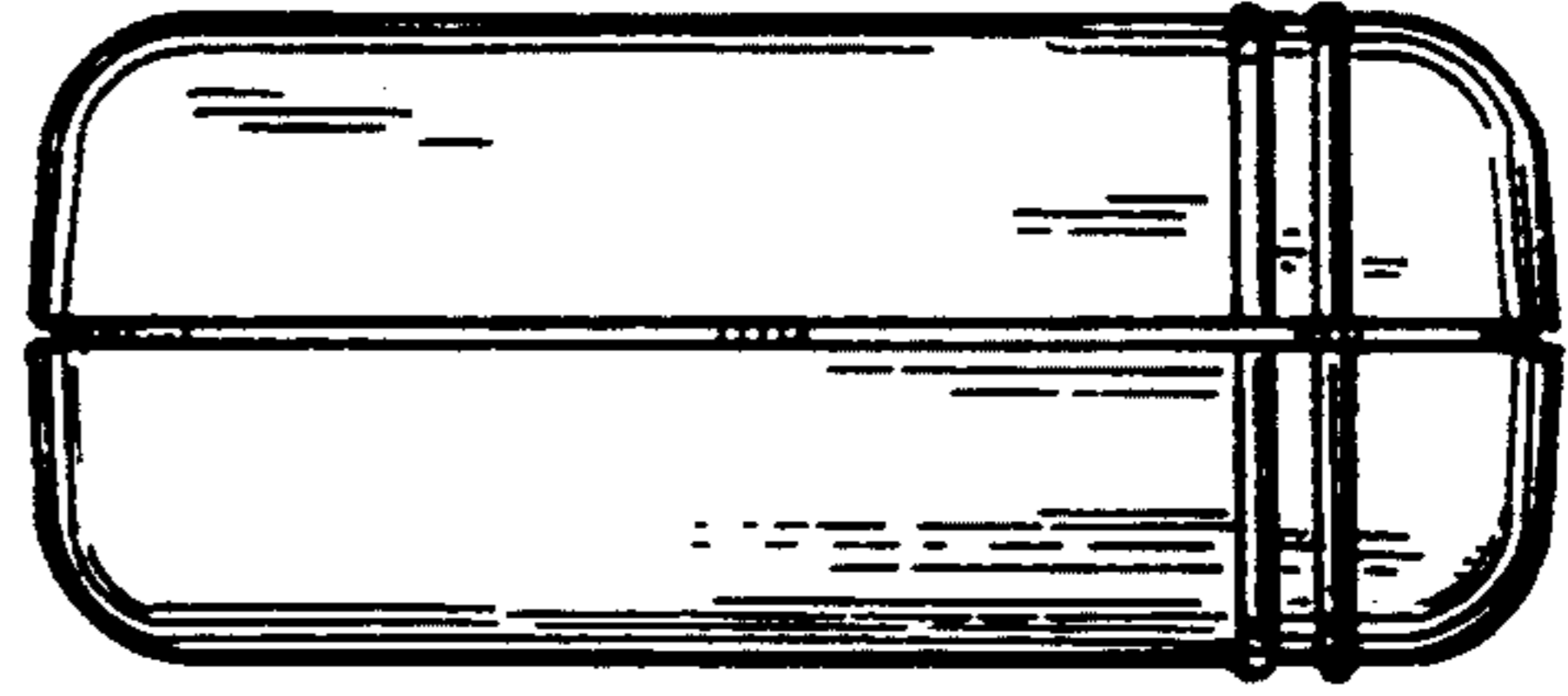
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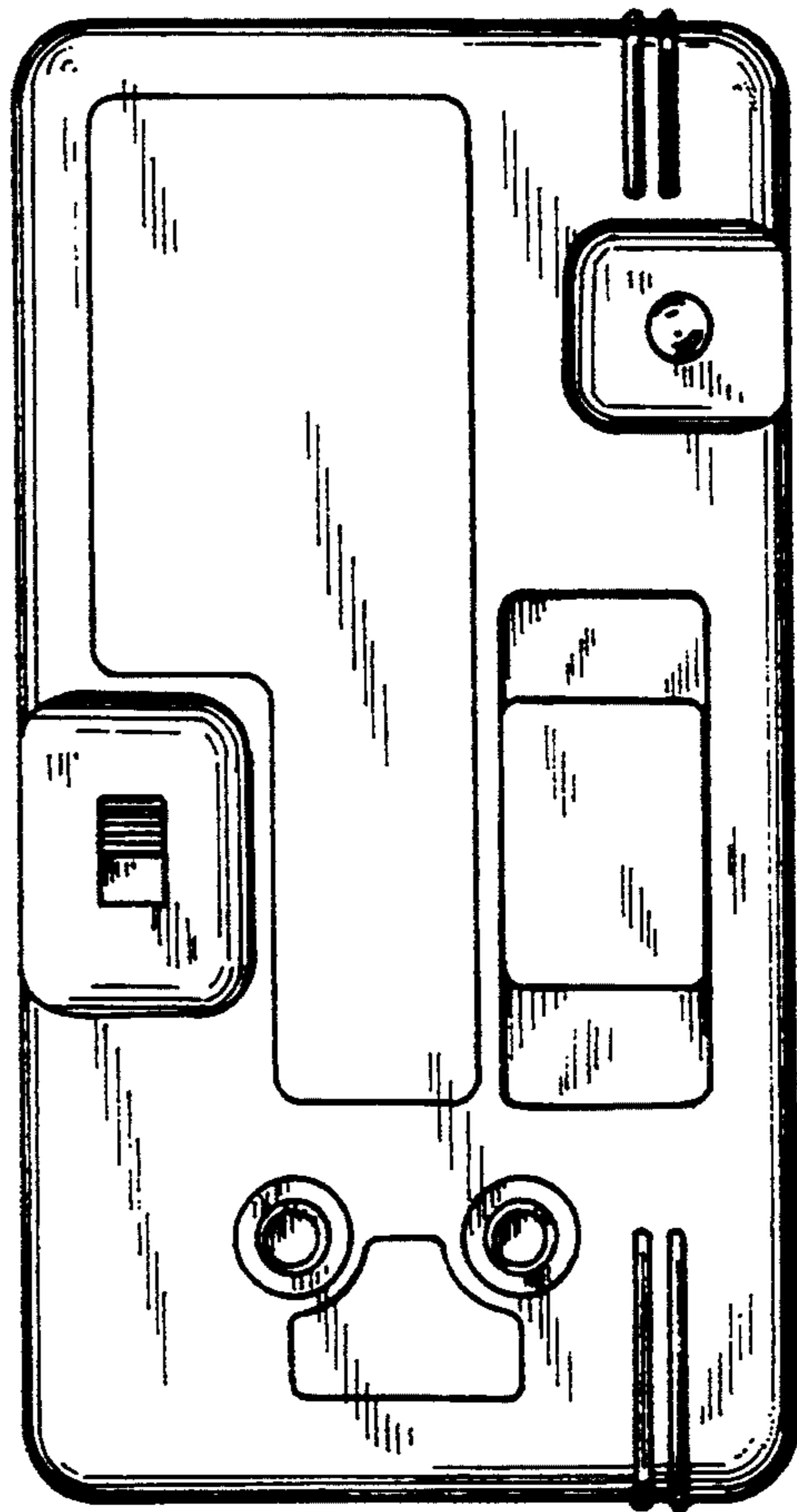




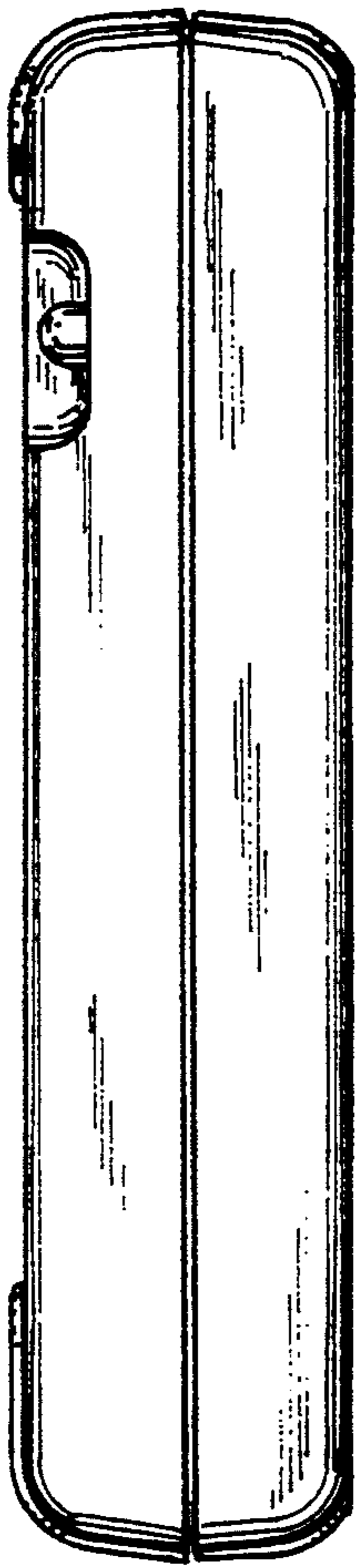
*Fig. 1*



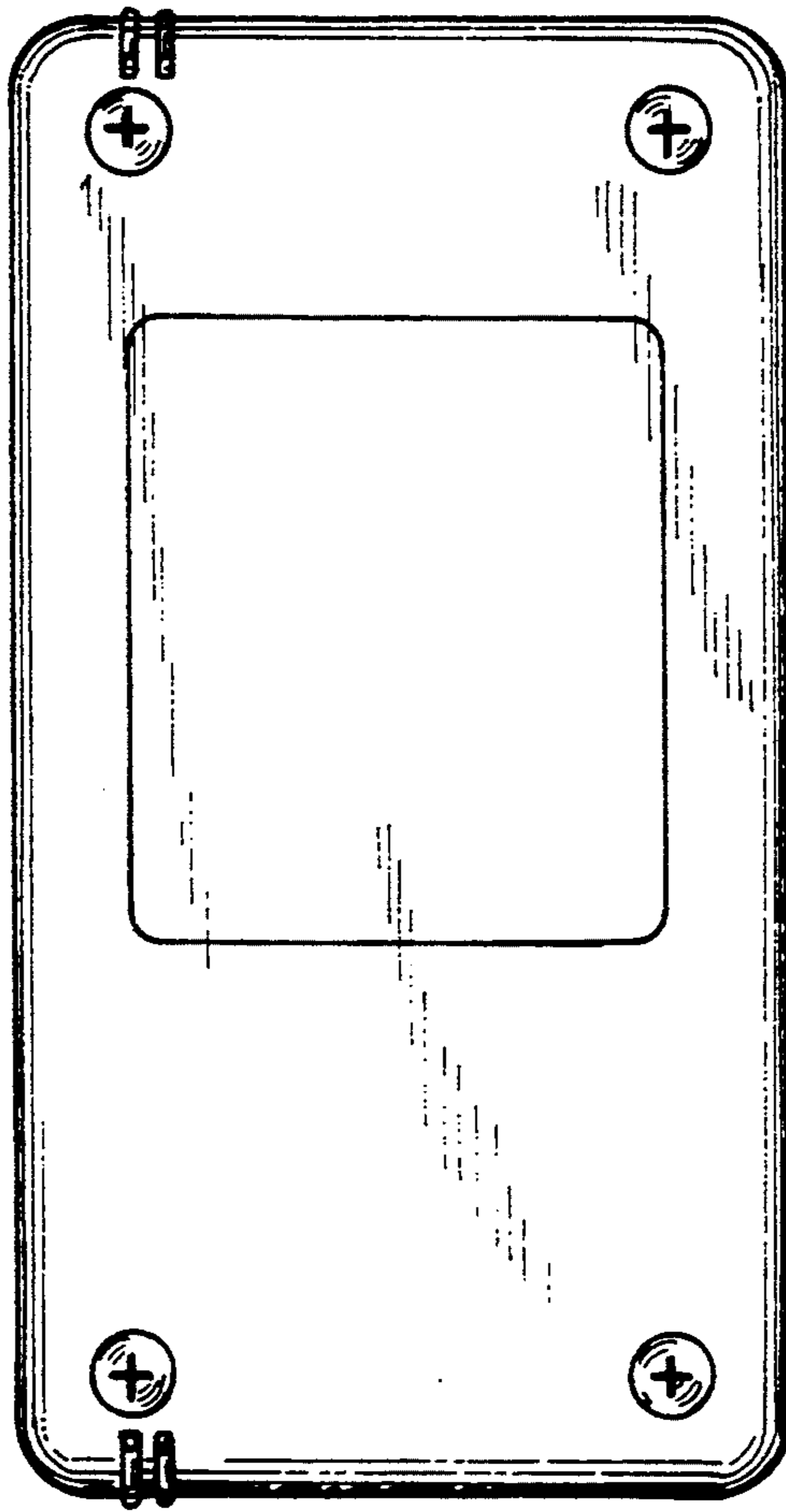
*Fig. 3*



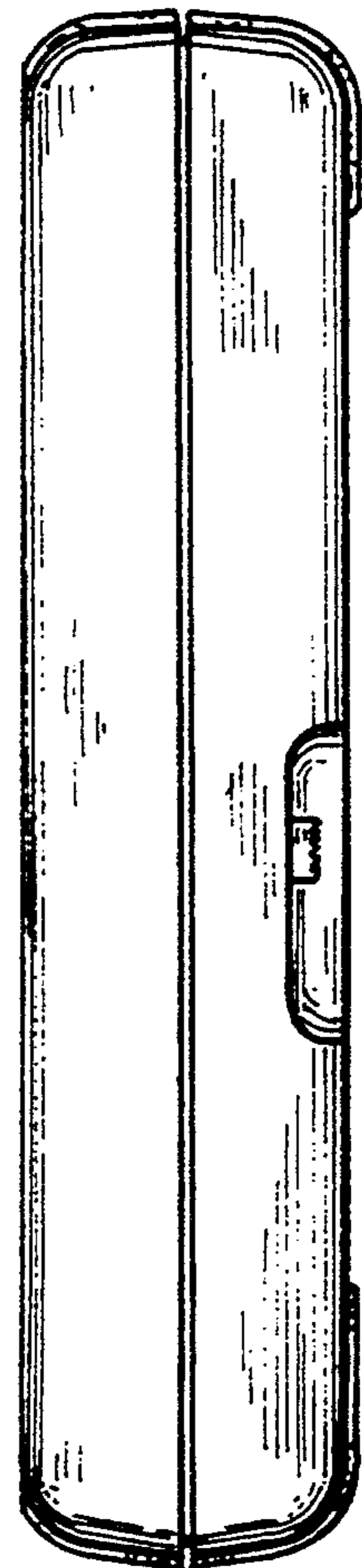
*Fig. 2*



*Fig\_5*



*Fig\_4*



*Fig\_6*