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United States Patent [19]

Vieira

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[54] **MICROPROCESSOR CONTROLLED ROAD MAP**

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[**] Term: **14 Years**

[21] Appl. No.: **6,613**

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[52] **U.S. Cl.** **D14/100;** D10/46

[58] **Field of Search** D14/100, 106, 113, 115; D12/192; D10/46, 65, 78; 364/436, 443, 708.1, 424.01, 424.02, 424.04, 444, 449, 460; 345/104, 156, 168, 173, 905; 340/988, 989, 990–996; 382/59; 342/451

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Attorney, Agent, or Firm—Terry M. Gernstein

[57] **CLAIM**

The ornamental design for a microprocessor controlled road map, as shown and described.

DESCRIPTION

FIG. **1** is a front, top and side perspective view of the microprocessor controlled road map showing my new design;

FIG. **2** is a front elevational view thereof;

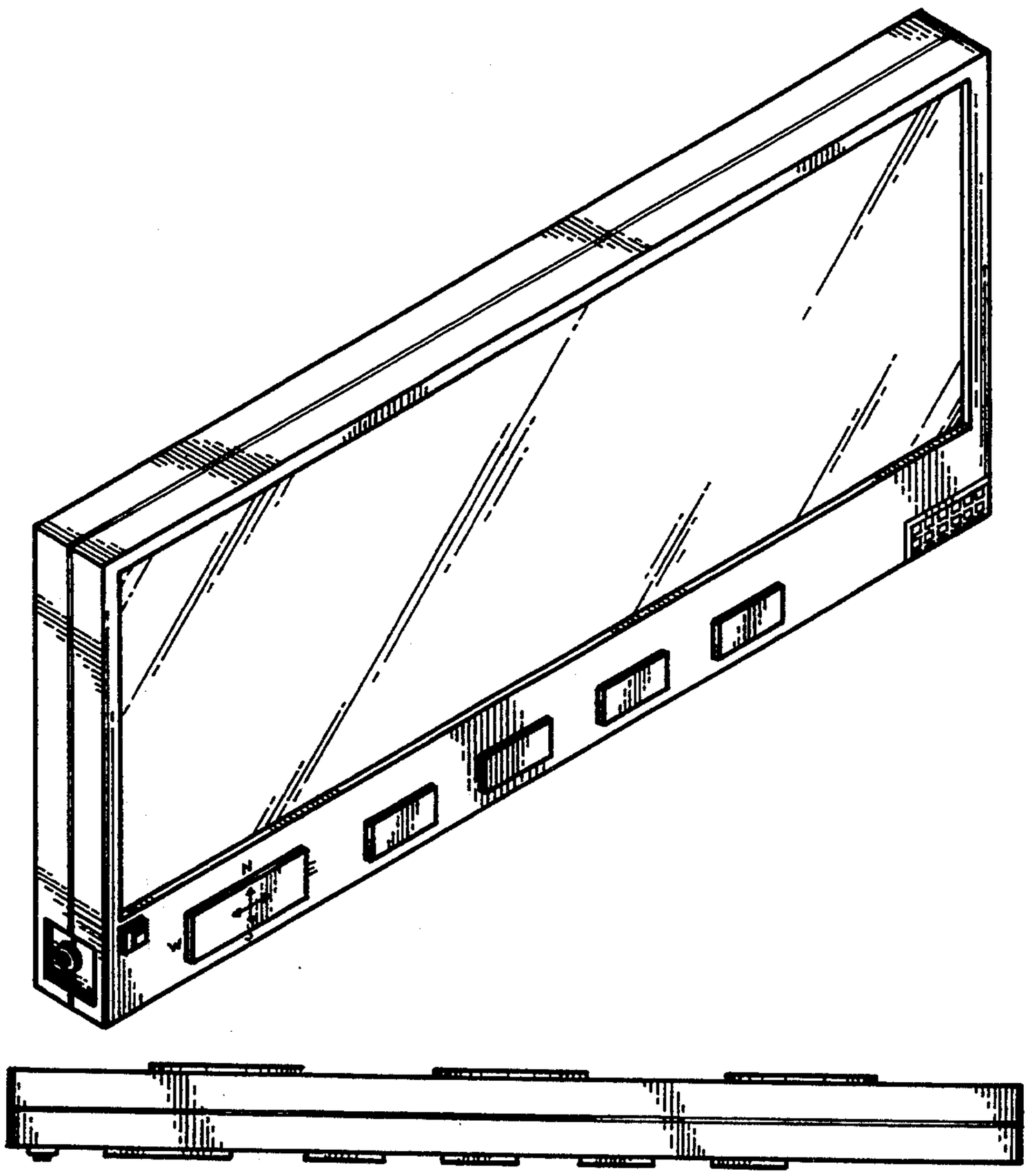
FIG. **3** is a rear elevational view thereof;

FIG. **4** is a side elevational view thereof;

FIG. **5** is an opposite side elevational view thereof;

FIG. **6** is a top plan view thereof; and,

FIG. **7** is a bottom plan view thereof.



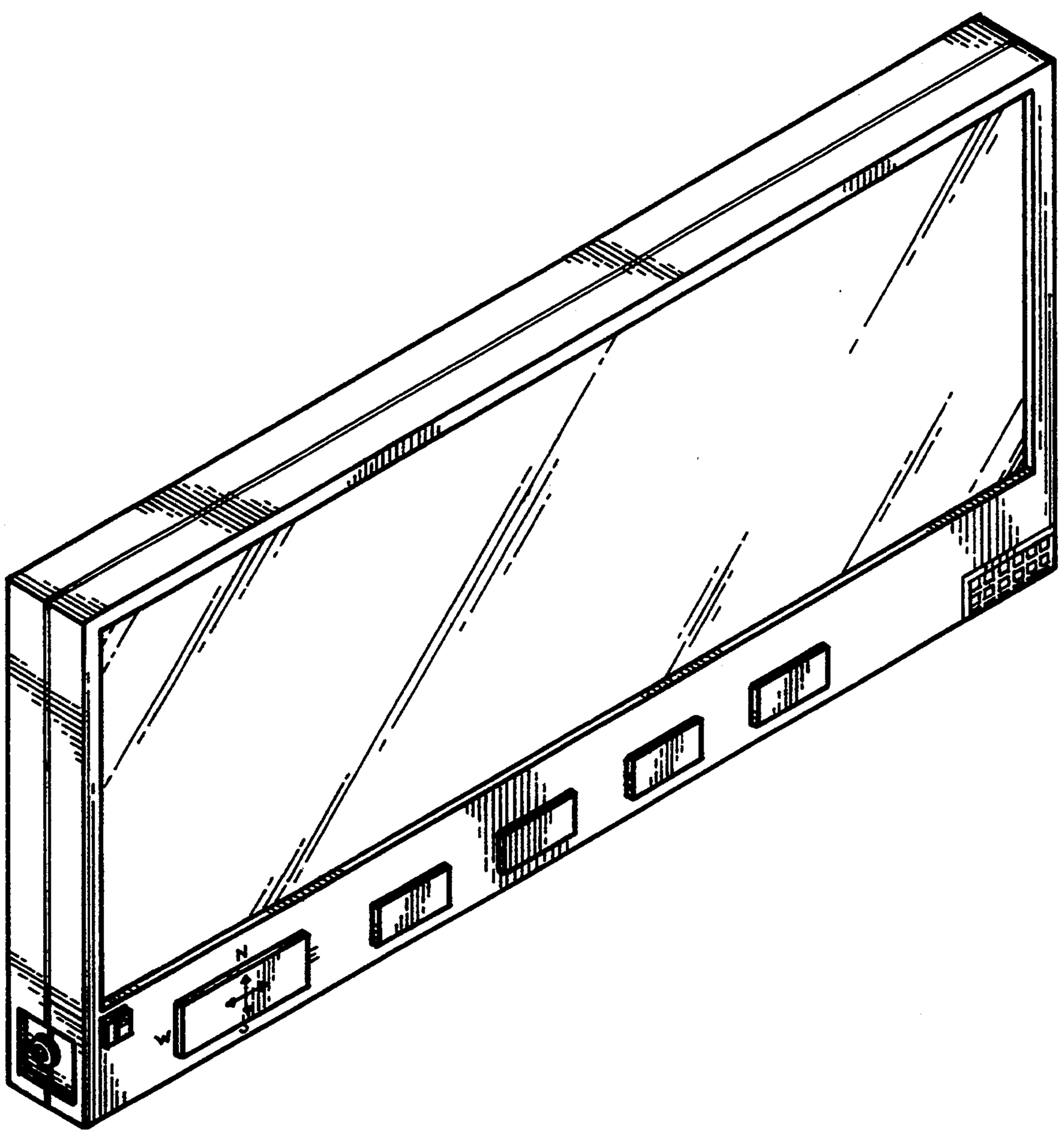


FIG. 1

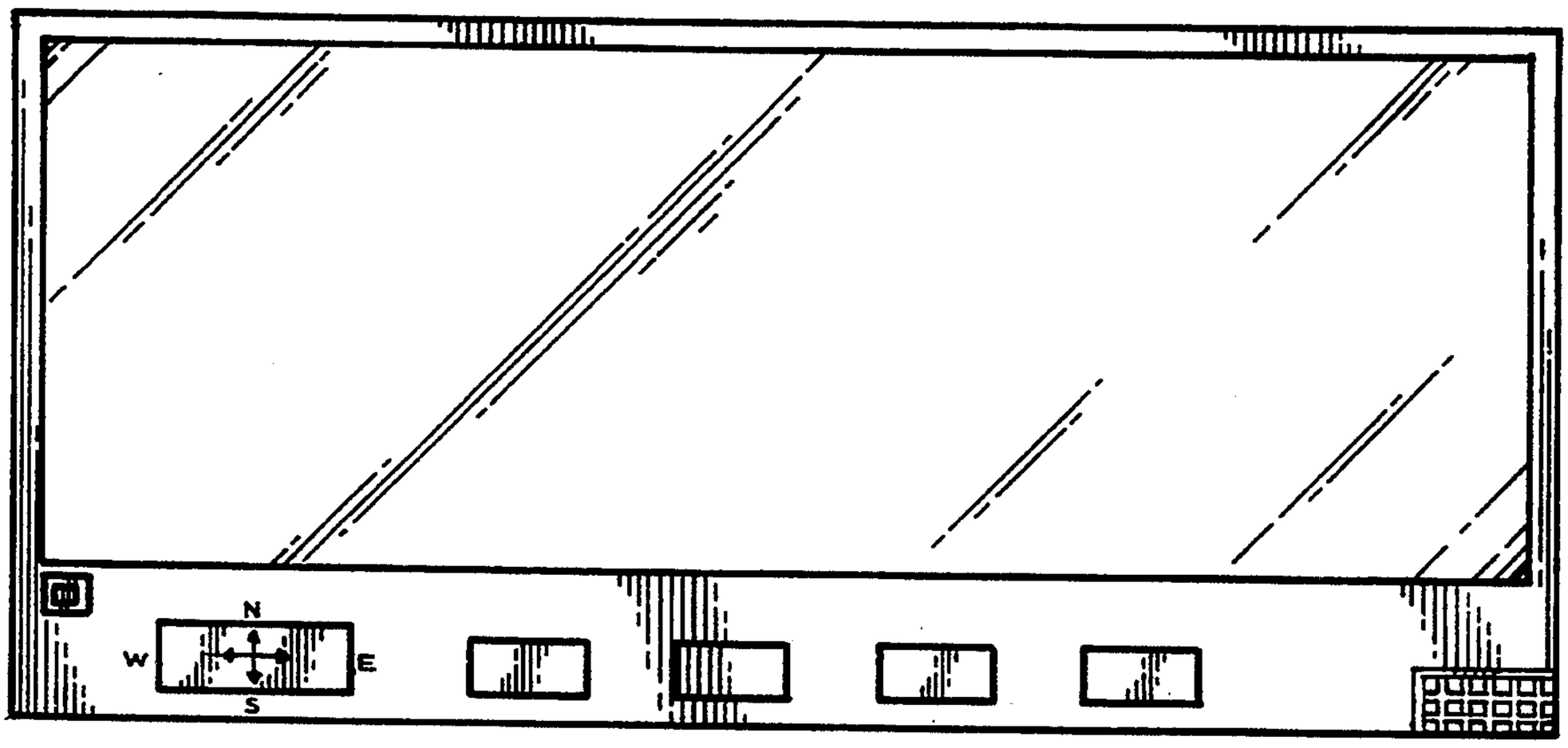


FIG. 2

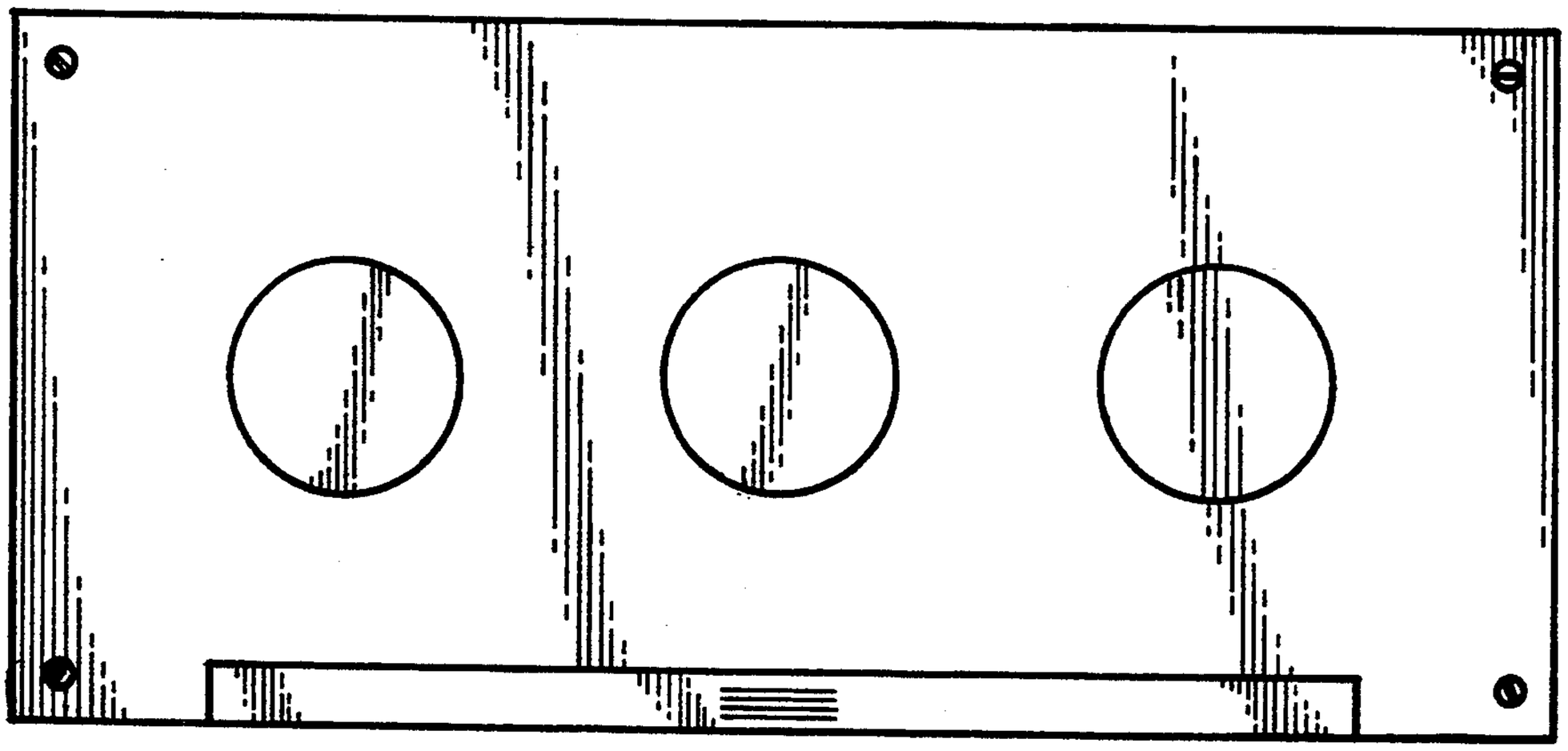


FIG. 3

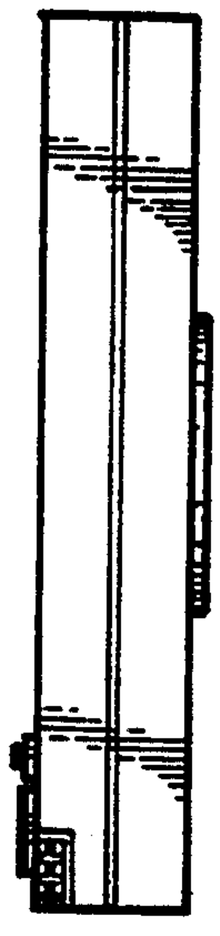


FIG. 4

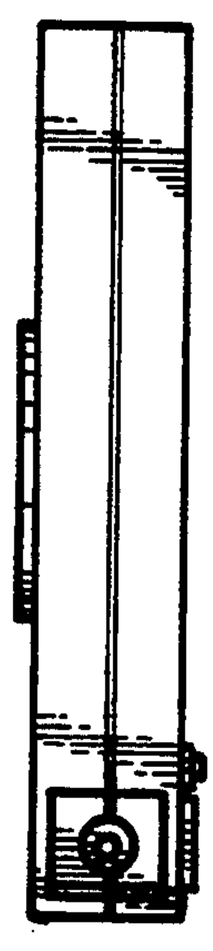


FIG. 5

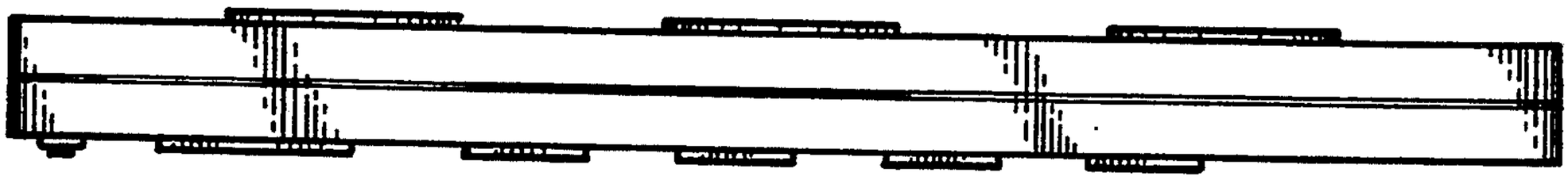


FIG. 6

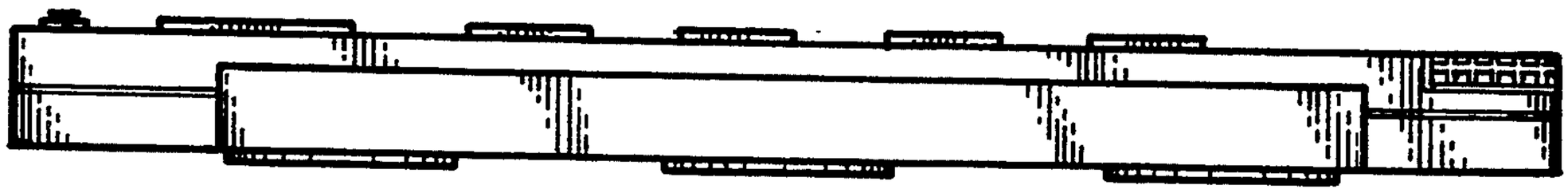


FIG. 7