



US00D354743S

United States Patent [19]

[11] Patent Number: **Des. 354,743**

Vieira

[45] Date of Patent: **** Jan. 24, 1995**

[54] **MICROPROCESSOR CONTROLLED ROAD MAP**

4,862,374	8/1989	Ziemann	364/449
4,890,230	12/1989	Tanoshima et al.	382/59 X
4,935,871	6/1990	Grohsmeier	364/424.02
5,059,970	10/1991	Raubenheimer et al.	342/451

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FOREIGN PATENT DOCUMENTS

[**] Term: **14 Years**

0088565	2/1983	European Pat. Off. .	
59-99587	6/1984	Japan	345/173

[21] Appl. No.: **6,613**

Primary Examiner—Freda S. Nunn
Attorney, Agent, or Firm—Terry M. Gernstein

[22] Filed: **Apr. 2, 1993**

[52] U.S. Cl. **D14/100; D10/46**

[58] Field of Search **D14/100, 106, 113, 115; D12/192; D10/46, 65, 78; 364/436, 443, 708.1, 424.01, 424.02, 424.04, 444, 449, 460; 345/104, 156, 168, 173, 905; 340/988, 989, 990-996; 382/59; 342/451**

[57] CLAIM

The ornamental design for a microprocessor controlled road map, as shown and described.

DESCRIPTION

[56] References Cited

U.S. PATENT DOCUMENTS

D. 264,347	5/1982	Peroni	D14/113
D. 321,174	10/1991	Tsuchiya	D14/100
D. 333,463	2/1993	Battaglia	D14/113
D. 337,569	7/1993	Kando	D14/100
4,403,291	9/1983	Tomkewitsch	364/424
4,782,447	11/1988	Ueno	364/449

FIG. 1 is a front, top and side perspective view of the microprocessor controlled road map showing my new design;

FIG. 2 is a front elevational view thereof;

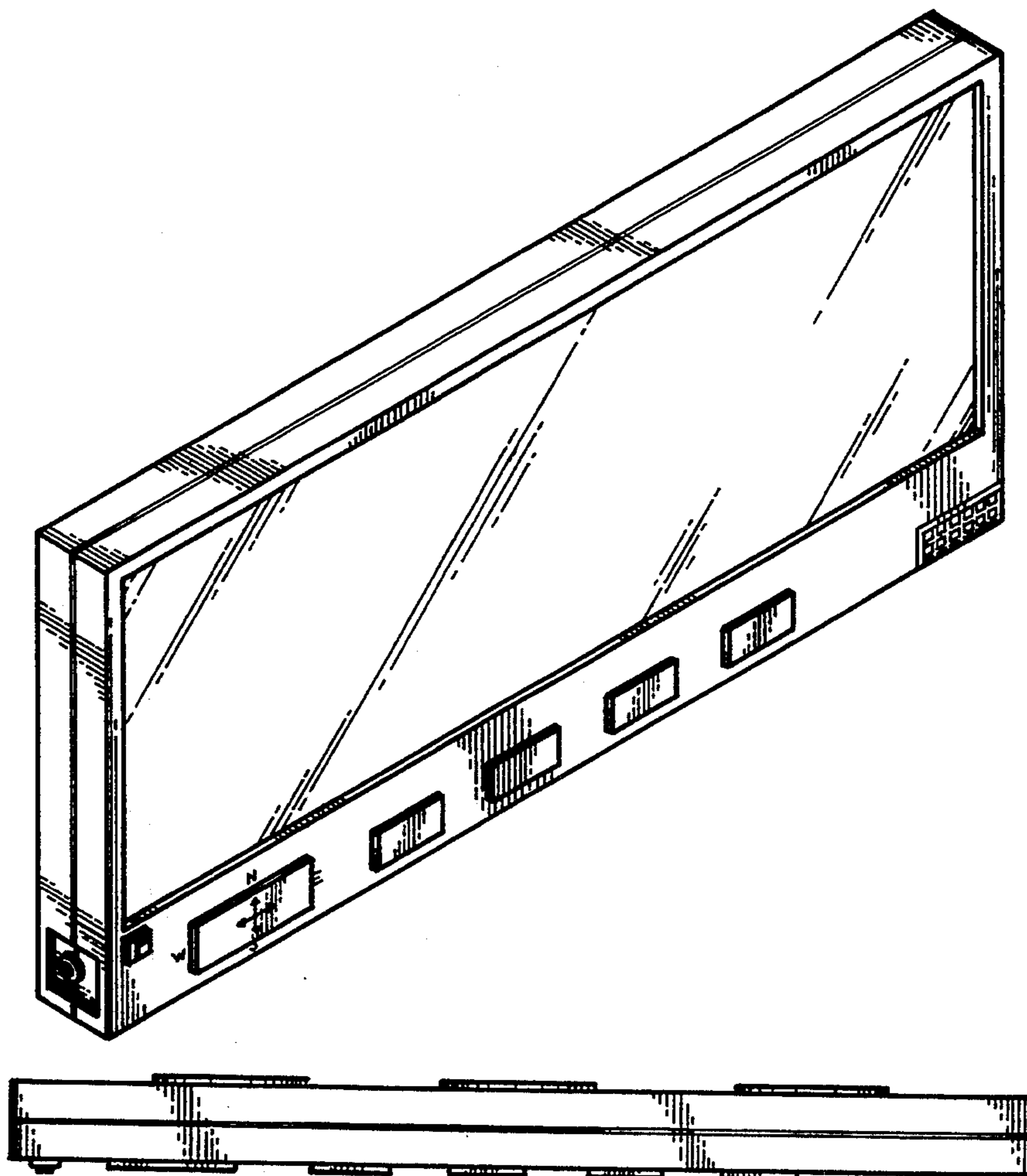
FIG. 3 is a rear elevational view thereof;

FIG. 4 is a side elevational view thereof;

FIG. 5 is an opposite side elevational view thereof;

FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.



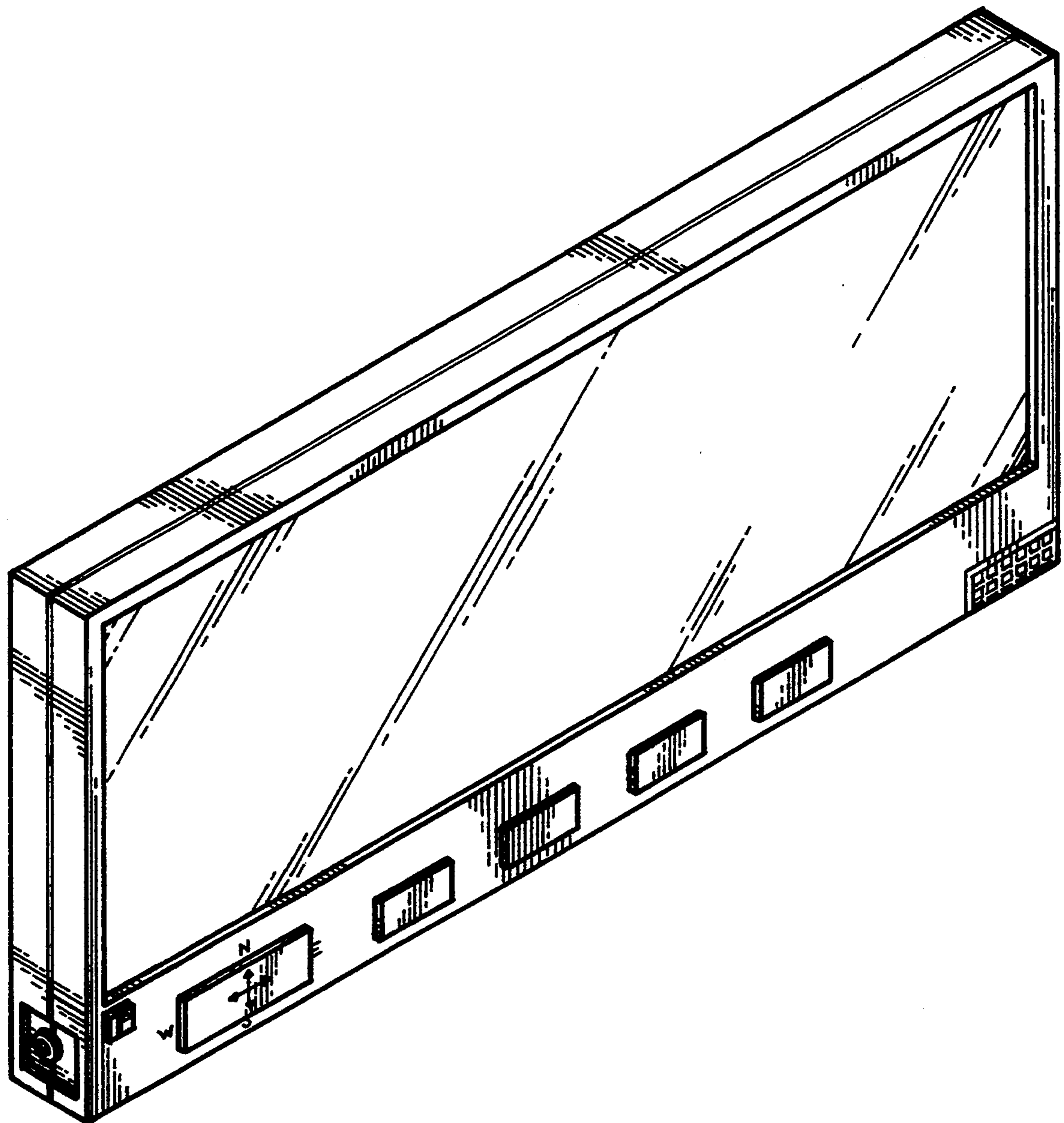


FIG. 1

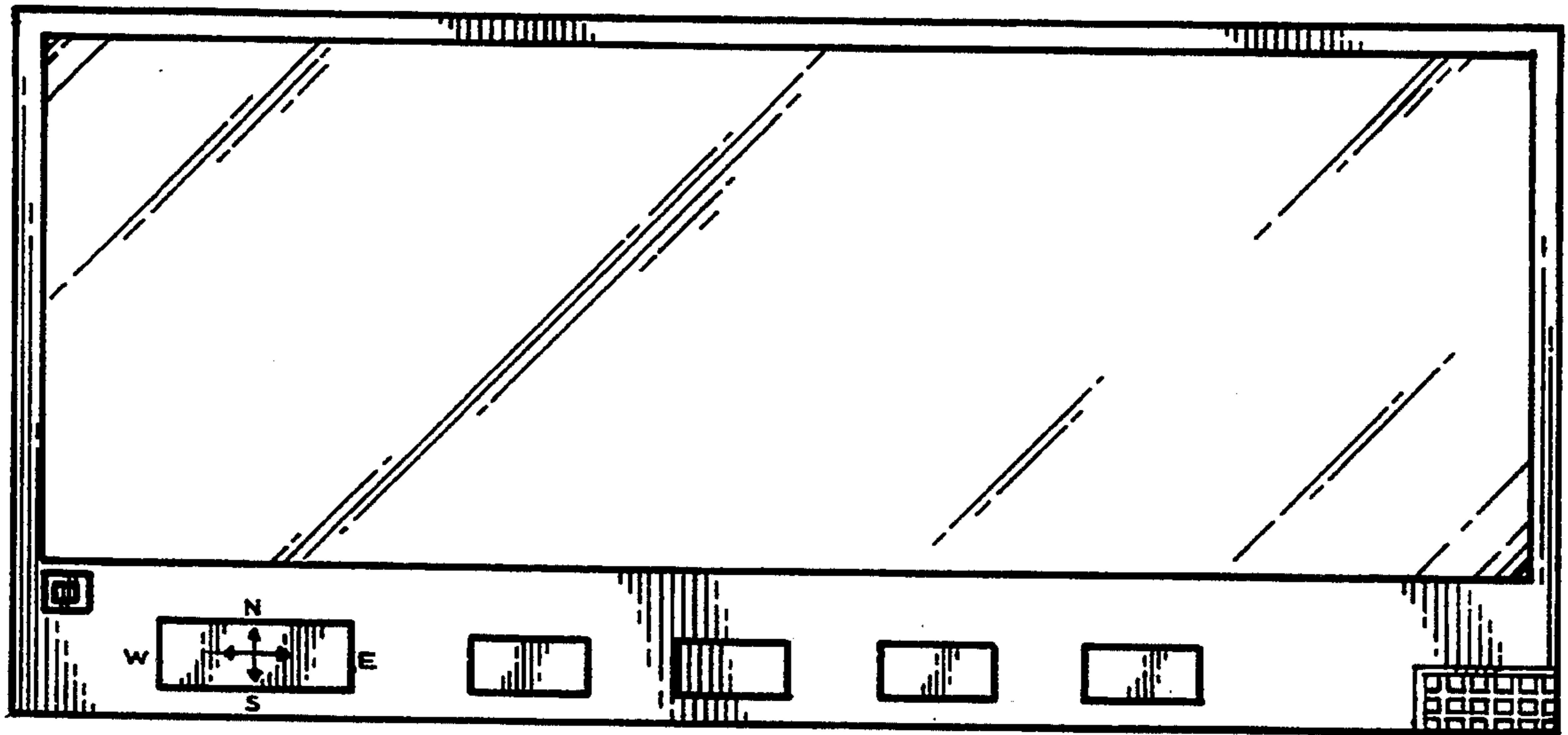


FIG. 2

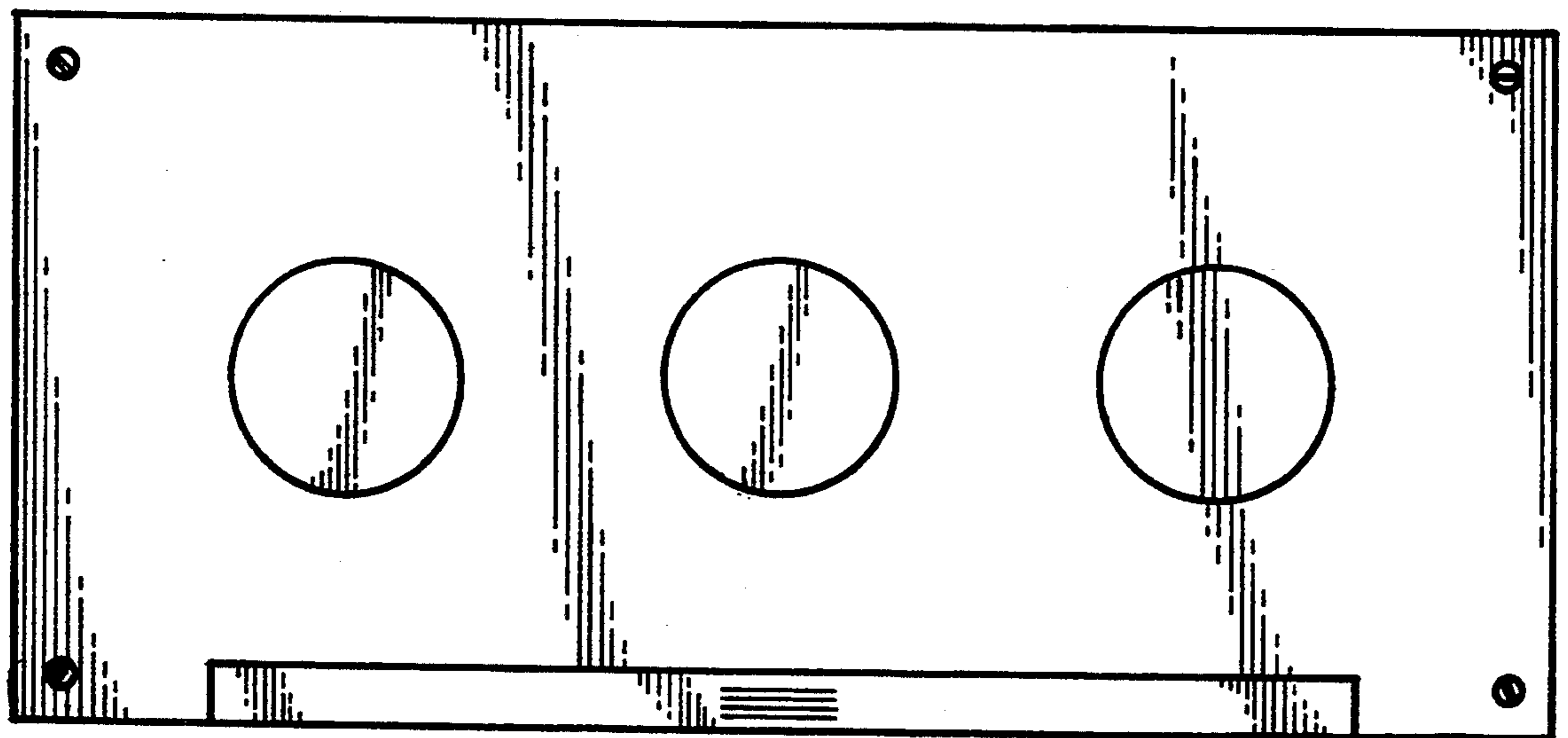


FIG. 3



FIG. 4

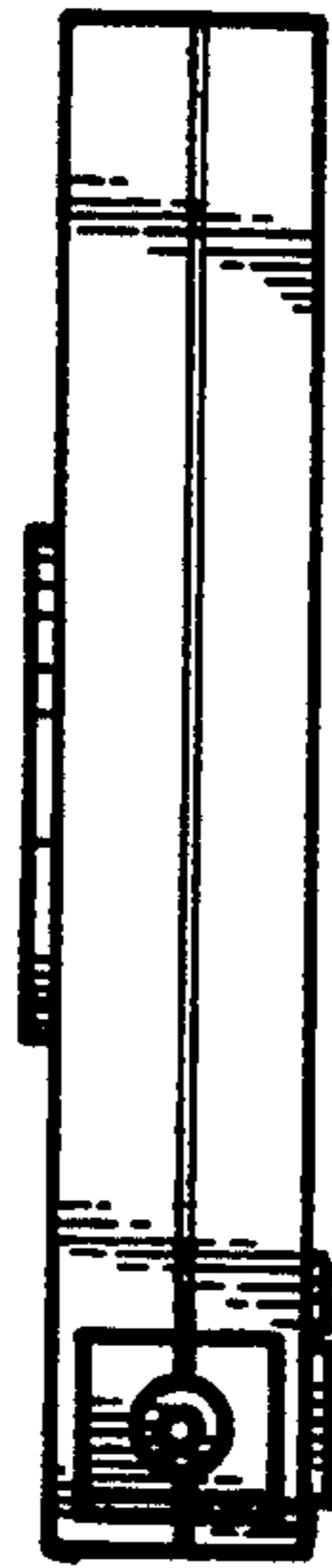


FIG. 5

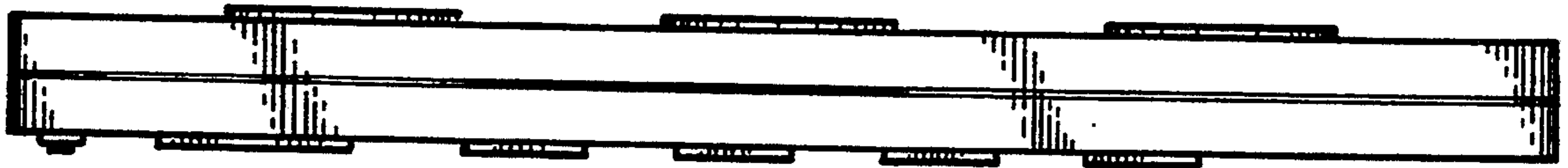


FIG. 6

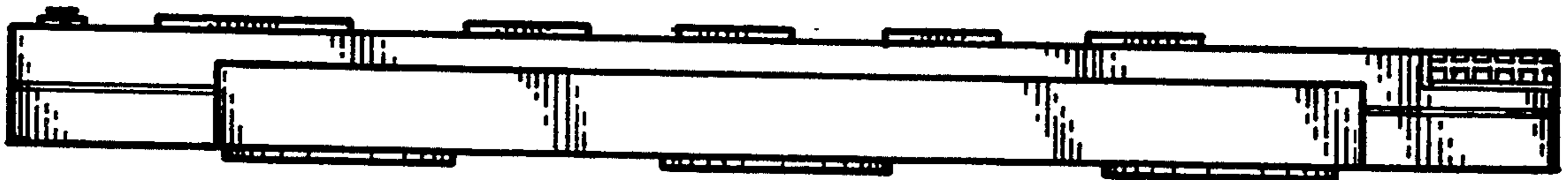


FIG. 7