



US00D353575S

United States Patent [19]

Macomber

[11] Patent Number: **Des. 353,575**

[45] Date of Patent: **** Dec. 20, 1994**

[54] **FLATPACK CAPACITOR**

[75] Inventor: **Laird L. Macomber, Easley, S.C.**

[73] Assignee: **Cornell Dubilier Electronics, Inc., Wayne, N.J.**

[**] Term: **14 Years**

[21] Appl. No.: **6,443**

[22] Filed: **Mar. 29, 1993**

[52] U.S. Cl. **D13/125**

[58] Field of Search 361/306, 307, 309;
174/52.4; D13/123, 125, 182

[56] **References Cited**

U.S. PATENT DOCUMENTS

D. 101,687	10/1936	Peters	D13/125
D. 202,271	9/1965	Wulc	D13/182
D. 340,692	10/1993	Iijima et al.	D13/123 X
3,601,667	8/1971	Desmond	174/52.4 X
3,909,683	9/1975	Kysely	361/307 X
4,250,534	2/1981	Brown et al.	361/309 X
4,527,010	7/1985	Anazawa et al.	174/52.4
4,642,419	2/1987	Meddles	174/52.4

4,684,763	8/1987	Harada	174/52.4
4,725,692	2/1988	Ishii et al.	174/52.4

FOREIGN PATENT DOCUMENTS

37440	3/1965	Germany	361/306.1
2517417	11/1975	Germany	361/306.1
14989	7/1969	Japan	174/52.4

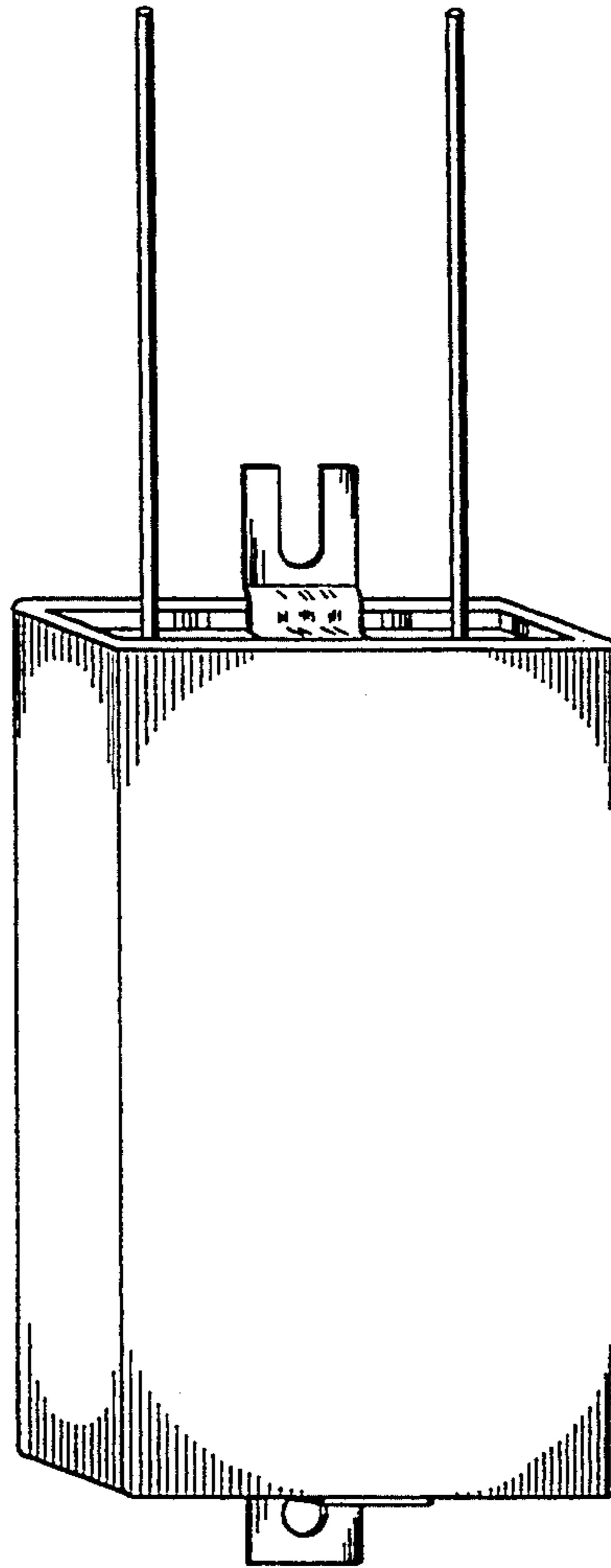
Primary Examiner—Wallace R. Burke
Assistant Examiner—Joel Sincavage
Attorney, Agent, or Firm—Gerald M. Newman

[57] CLAIM

The ornamental design for a flatpack capacitor, as shown and described.

DESCRIPTION

FIG. 1 is a front and upper left perspective view of a flatpack capacitor showing my new design; FIG. 2 is a front elevational view thereof; FIG. 3 is a left side elevational view thereof; FIG. 4 is a right side elevational view thereof; FIG. 5 is a rear elevational view thereof; FIG. 6 is a top plan view thereof; and, FIG. 7 is a bottom plan view thereof.



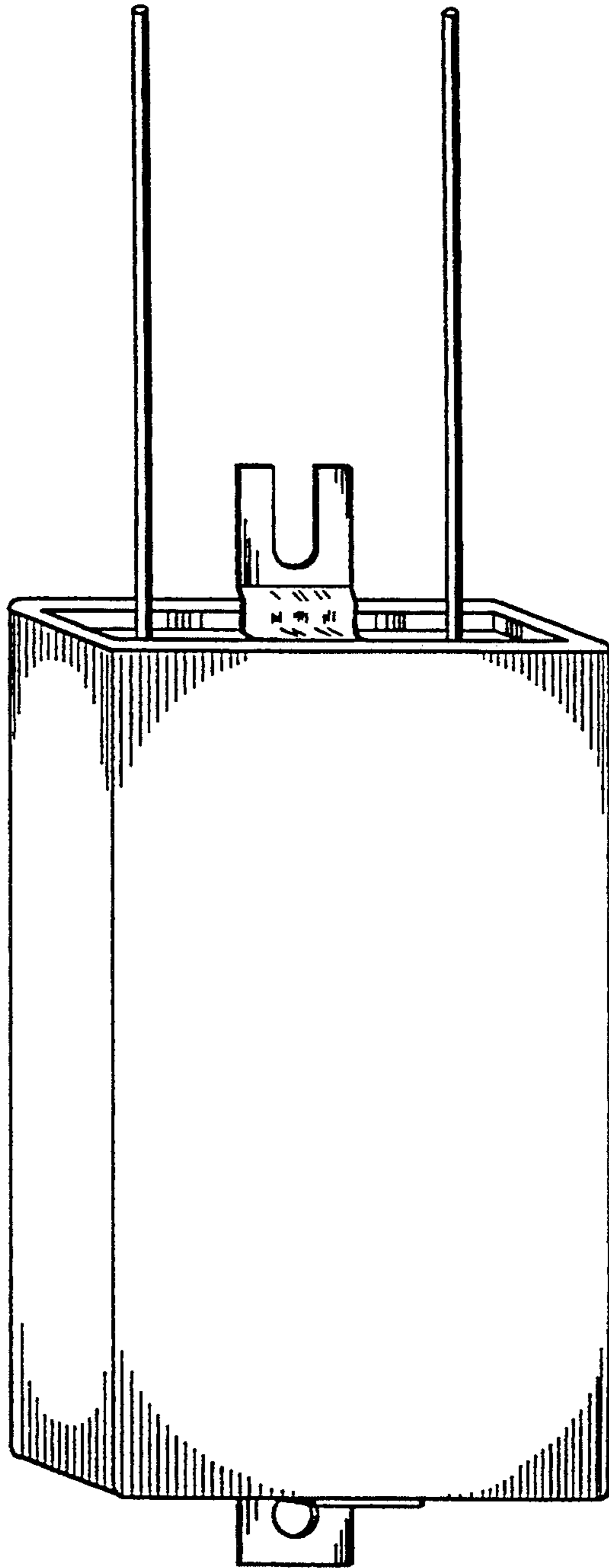


FIG. 1

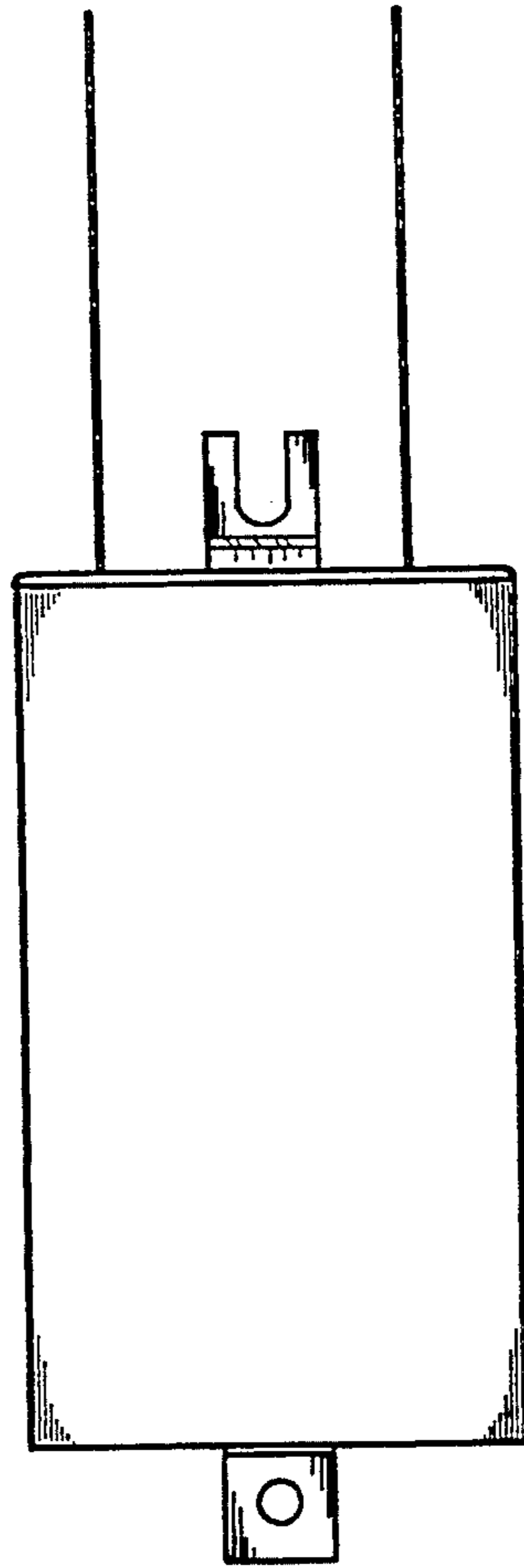


FIG. 2

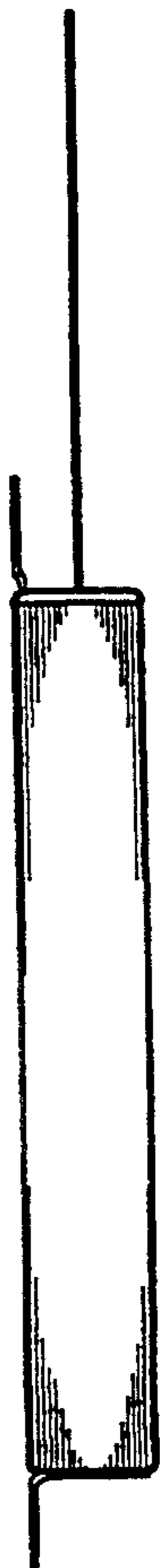


FIG. 3



FIG. 4



FIG. 6

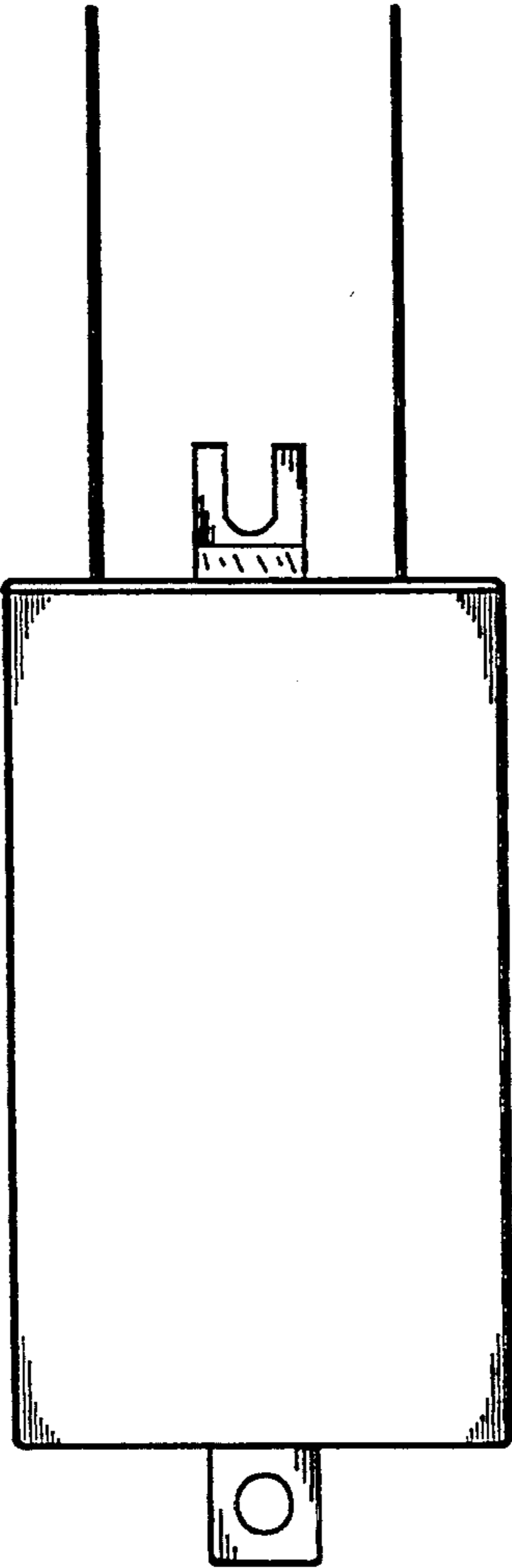


FIG. 5



FIG. 7