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United States Patent [19]

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White

[45] Date of Patent: **** Oct. 25, 1994**

[54] **DIGITAL CLOCK**

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[**] Term: **14 Years**

[21] Appl. No.: **781,292**

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Primary Examiner—Nelson C. Holtje
Attorney, Agent, or Firm—Charles S. McGuire

Related U.S. Application Data

[63] Continuation-in-part of Ser. No. 568,694, Aug. 17, 1990.

[52] U.S. Cl. **D10/15; D10/30; D10/38**

[58] **Field of Search** 368/239-242,
368/82-84, 276-277, 280-282, 285; D10/1-40,
122-132

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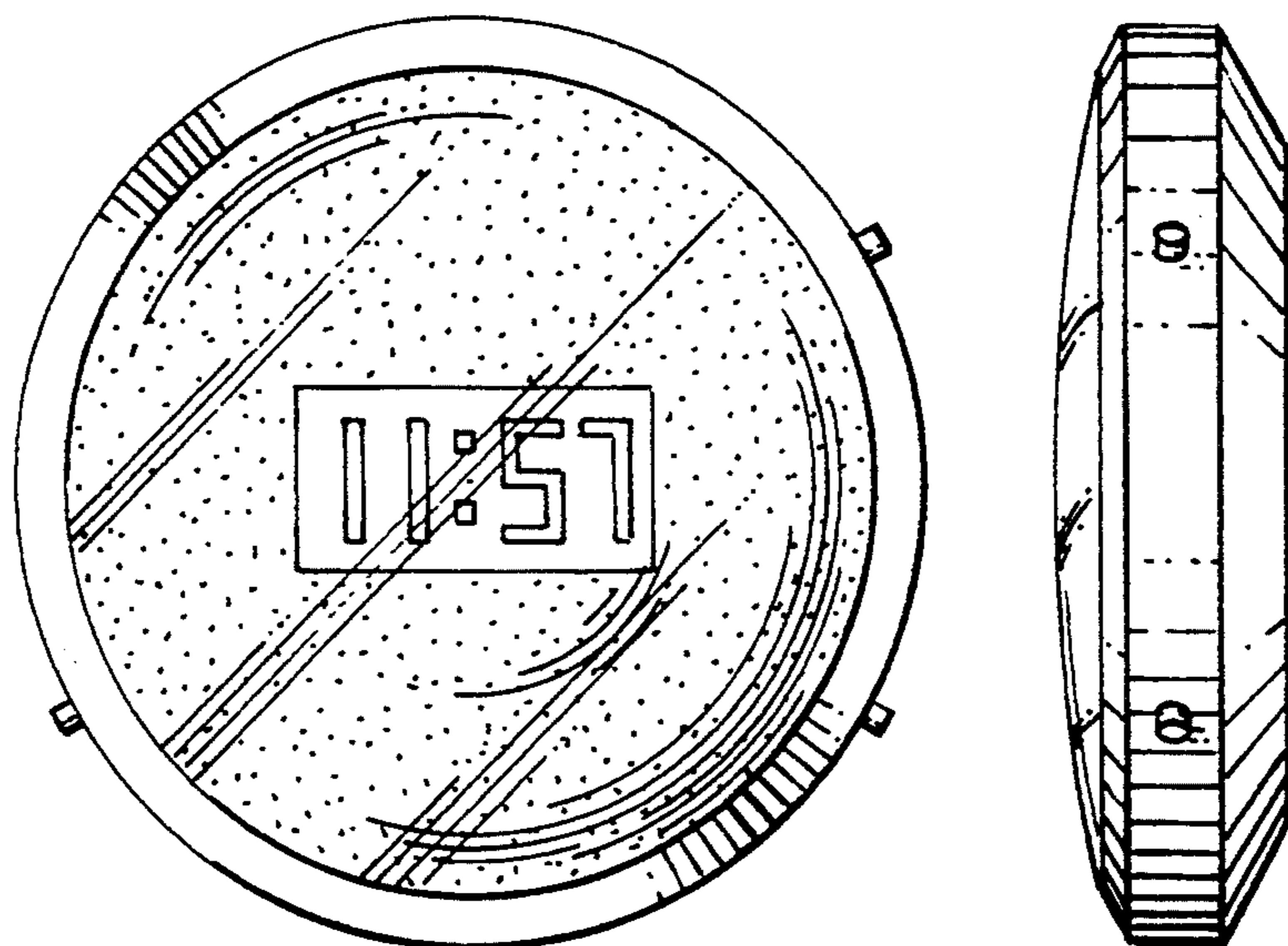
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[57] CLAIM

The ornamental design for digital clock, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of the digital clock showing my new design;
FIG. 2 is a front elevational view;
FIG. 3 is a left side elevational view;
FIG. 4 is a right side elevational view;
FIG. 5 is a bottom plan view;
FIG. 6 is a top plan view; and,
FIG. 7 is a rear elevational view.



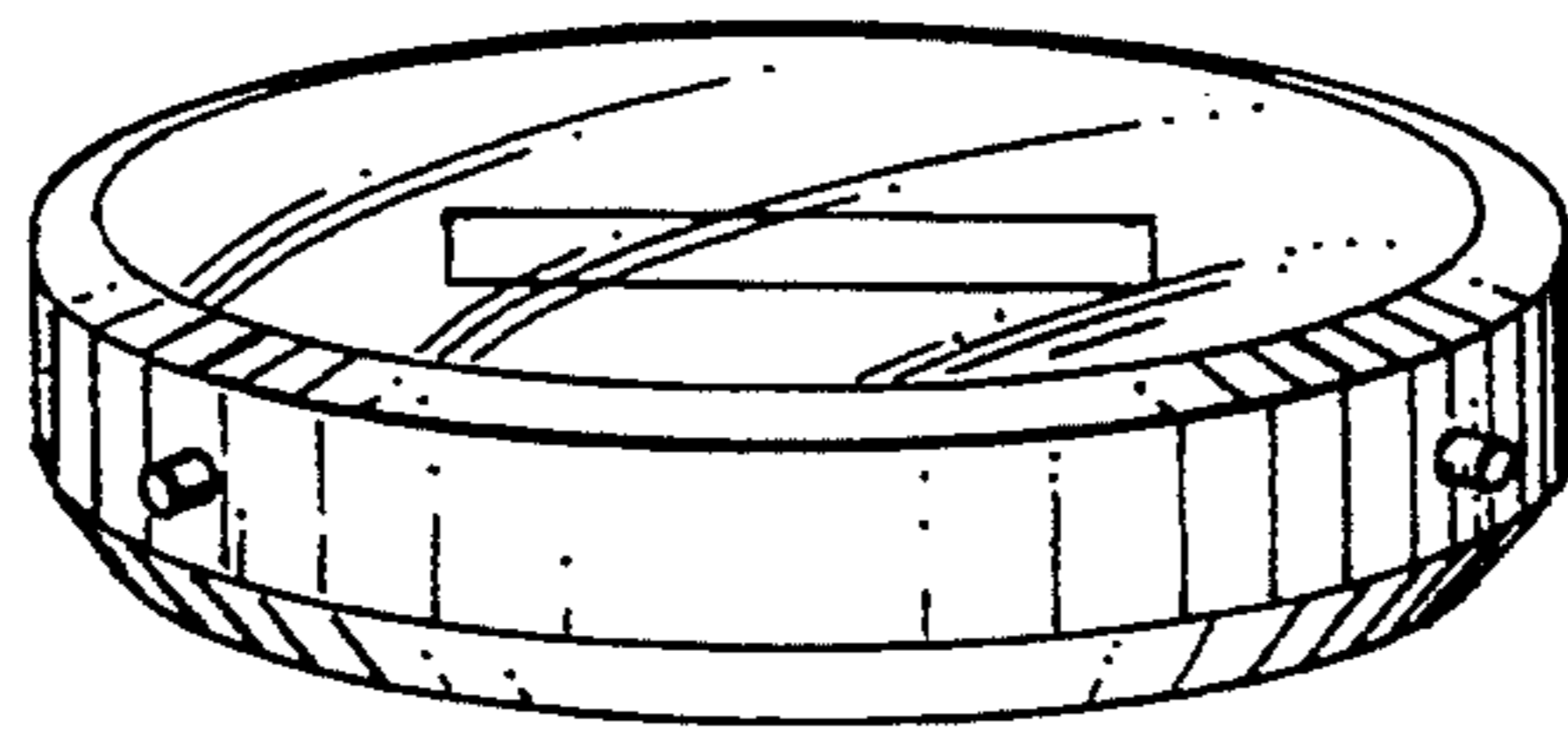


FIG. 1

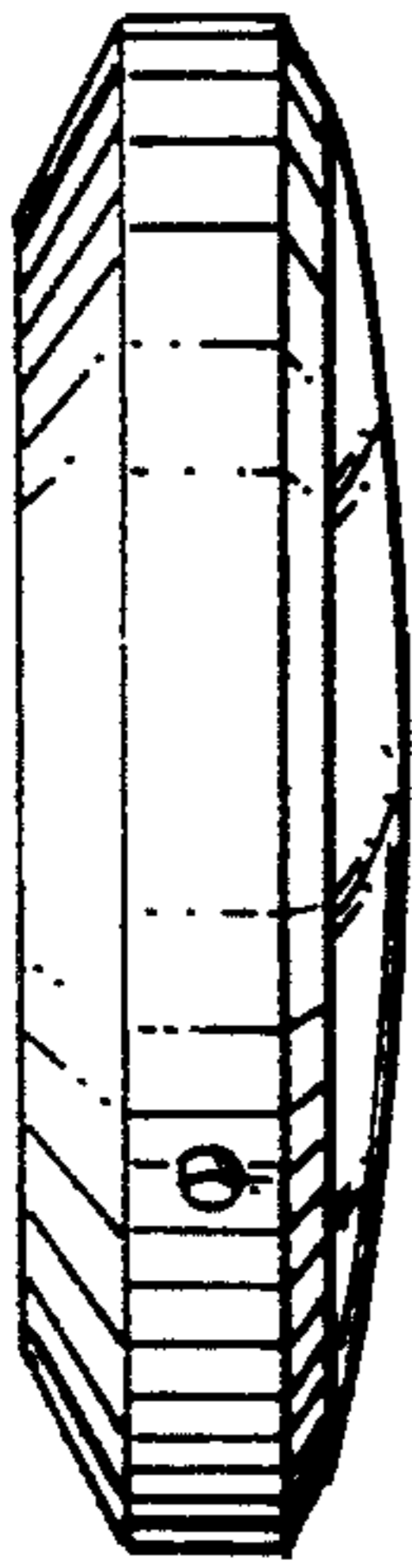


FIG. 3

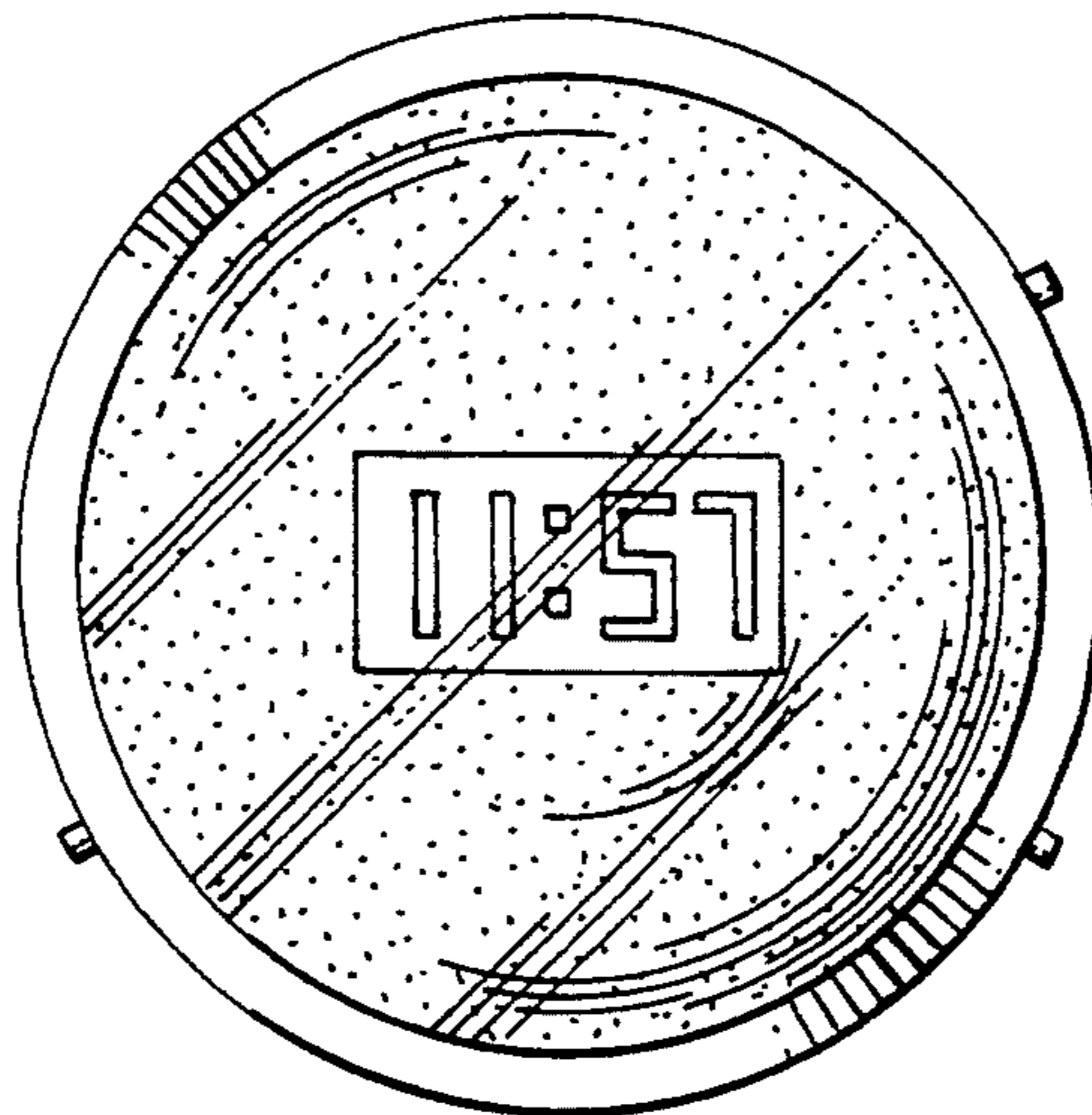


FIG. 2

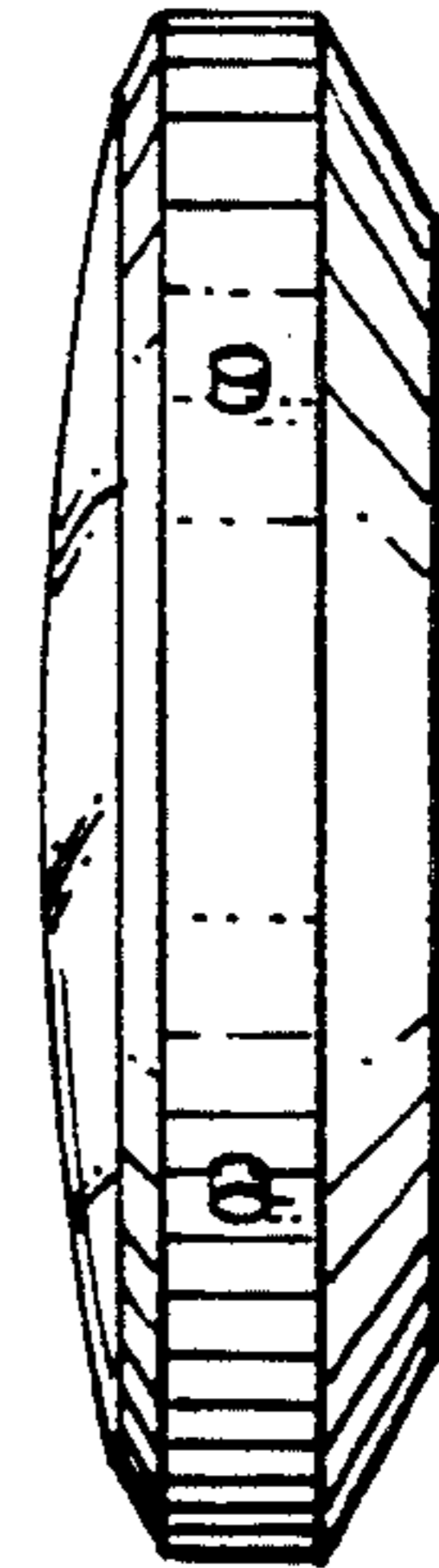


FIG. 4

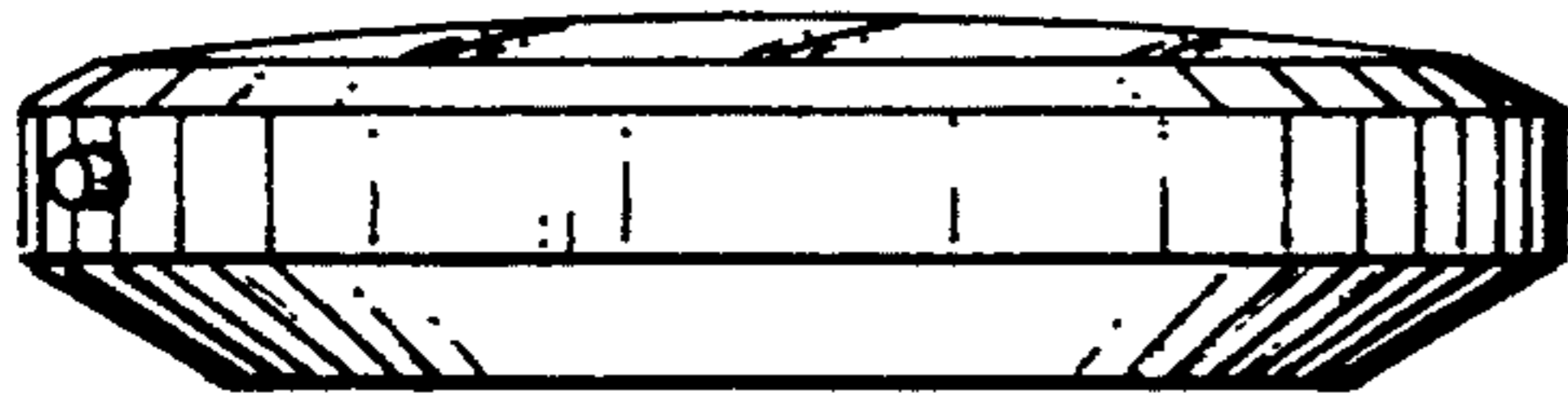


FIG. 5



FIG. 6

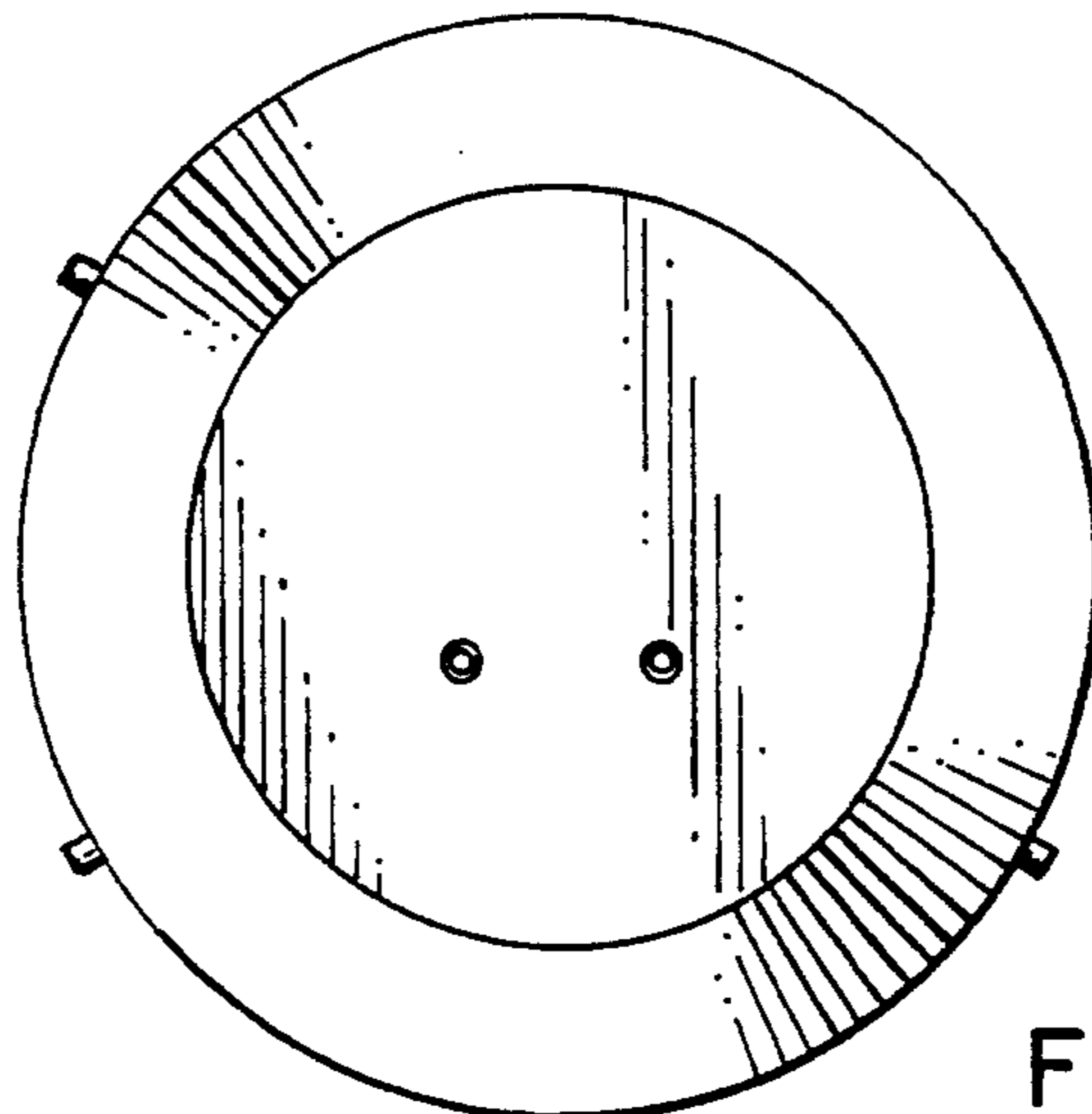


FIG. 7