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United States Patent [19] Cooper

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[45] Date of Patent: **** Jun. 28, 1994**

- [54] **ELECTRONIC CIRCUIT HOUSING**
- [75] Inventor: **David Cooper, Seattle, Wash.**
- [73] Assignee: **Eldec Corporation, Lynnwood, Wash.**
- [**] Term: **14 Years**
- [21] Appl. No.: **9,360**
- [22] Filed: **Jun. 9, 1993**

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[57] **CLAIM**

The ornamental design for an electronic circuit housing, as shown and described.

DESCRIPTION

FIG. 1 is a top front perspective view of an electronic circuit housing showing my new design;
 FIG. 2 is a bottom plan view thereof;
 FIG. 3 is an end elevational view thereof taken from the left of FIG. 2, the opposite end thereof being the mirror image of the end shown in FIG. 3;
 FIG. 4 is a rear elevational view thereof;
 FIG. 5 is a top front perspective of a second embodiment thereof;
 FIG. 6 is a bottom plan view of the second embodiment thereof;
 FIG. 7 is an end elevational view of the second embodiment thereof taken from the left of FIG. 6, the opposite end thereof being the mirror image of the end shown in FIG. 7; and,
 FIG. 8 is a rear elevational view of the second embodiment thereof.

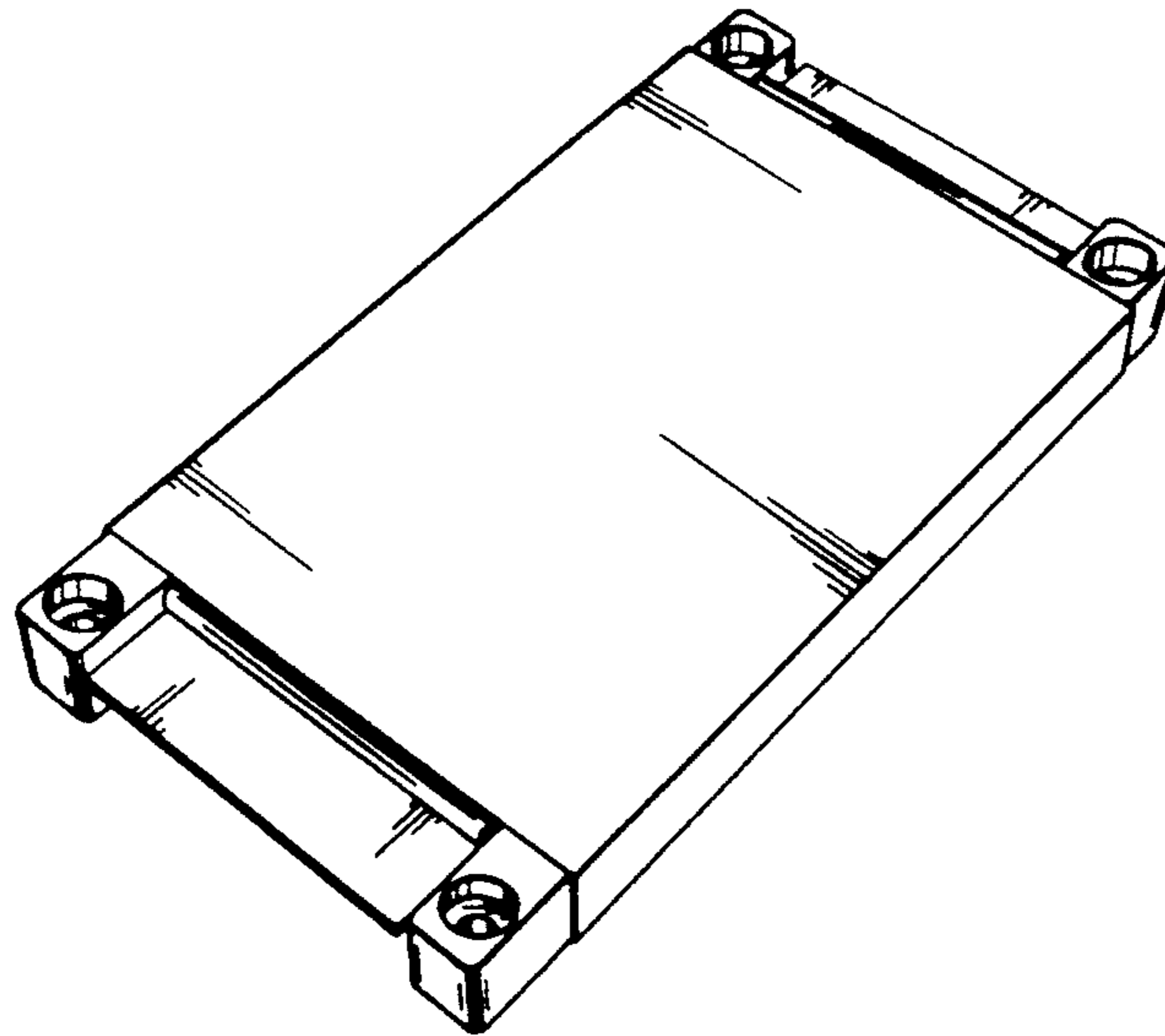
Related U.S. Application Data

- [62] Division of Ser. No. 954,930, Sep. 29, 1992.
- [52] U.S. Cl. **D13/110**
- [58] Field of Search 361/111, 386, 388, 392, 361/395; 336/90, 92, 200; 363/15, 95; 307/150, 151; 174/52.1, 52.2; D13/110, 123, 124, 184

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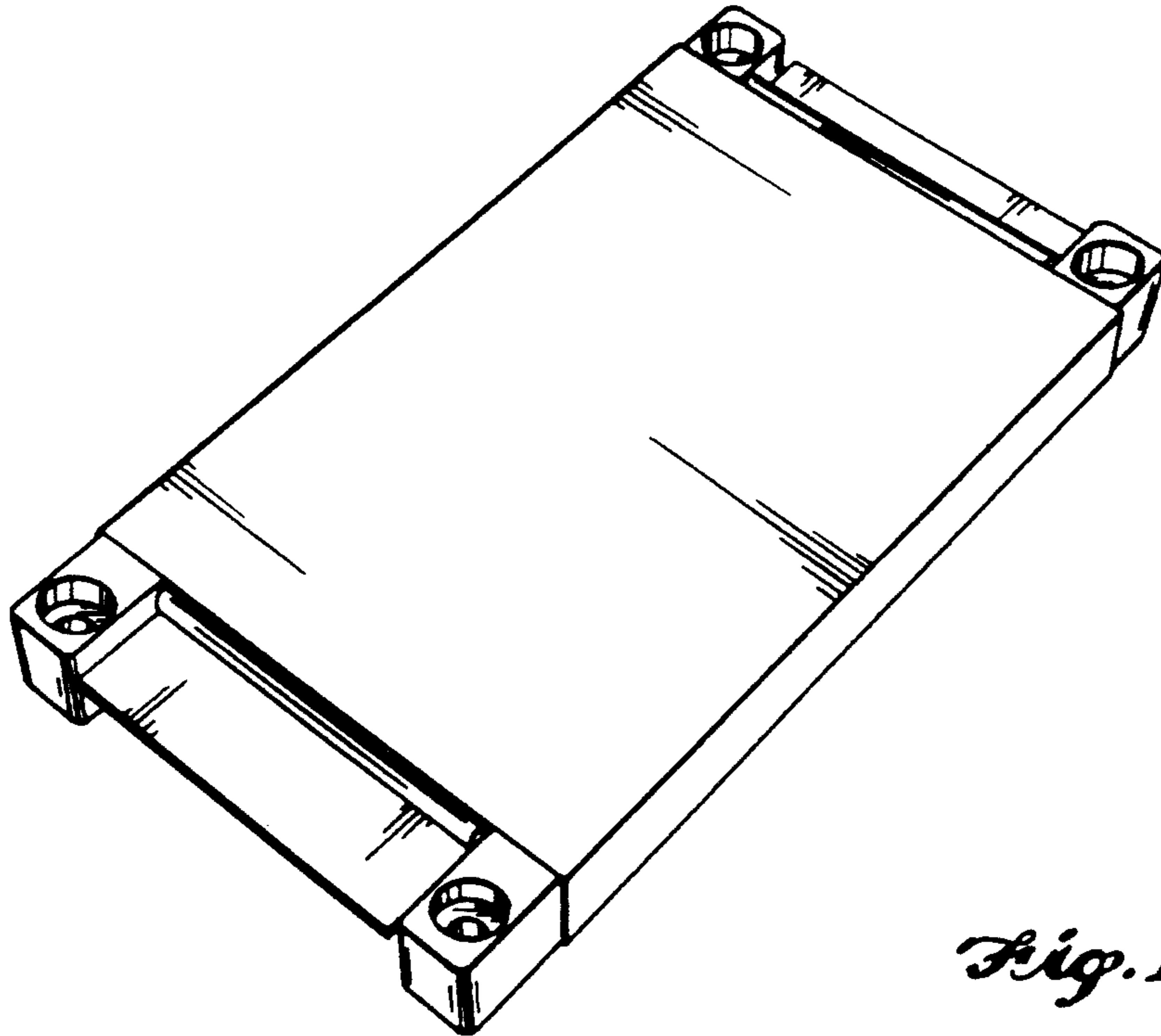


Fig. 1.

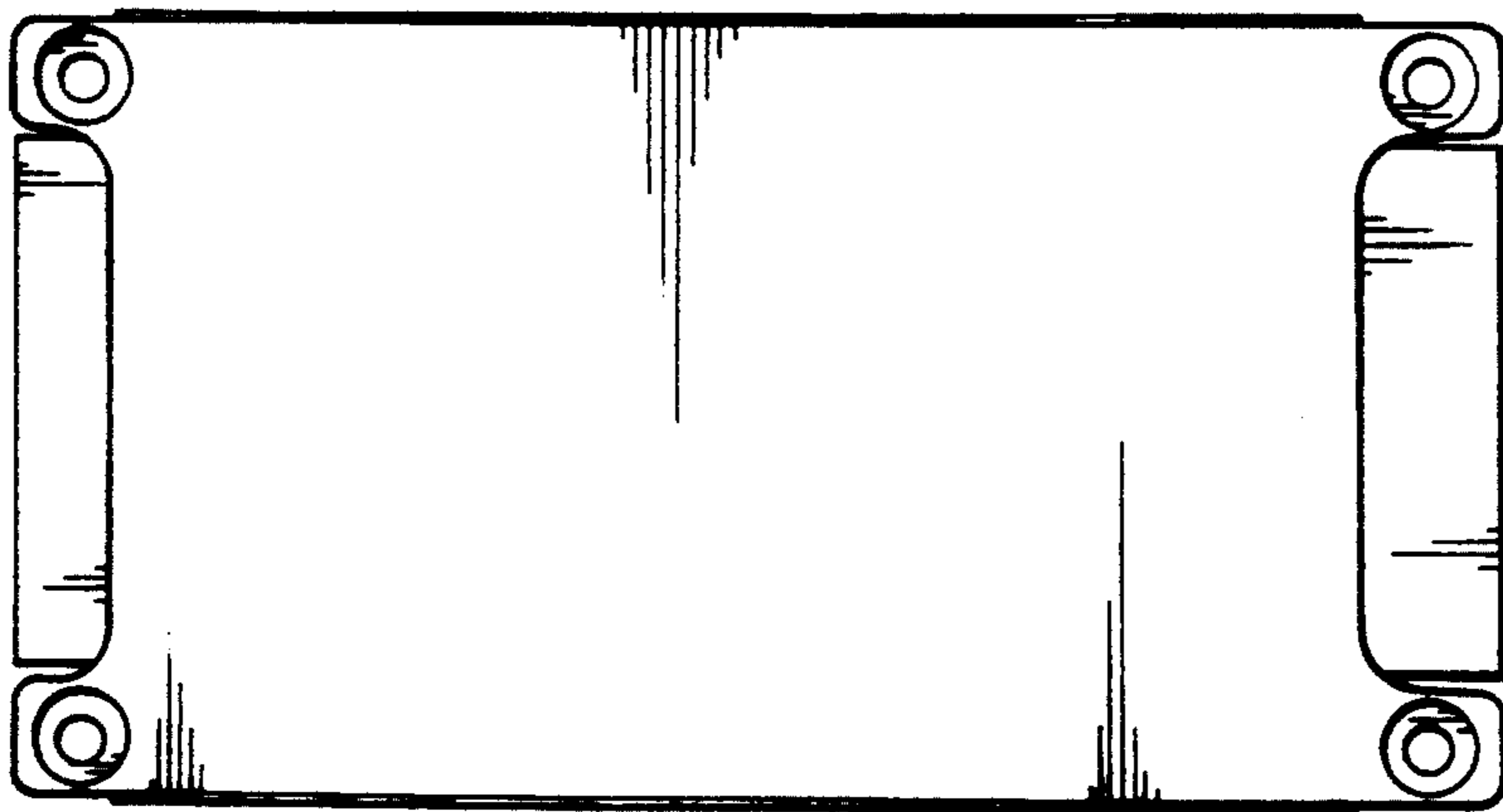


Fig. 2.



Fig. 3.



Fig. 4.

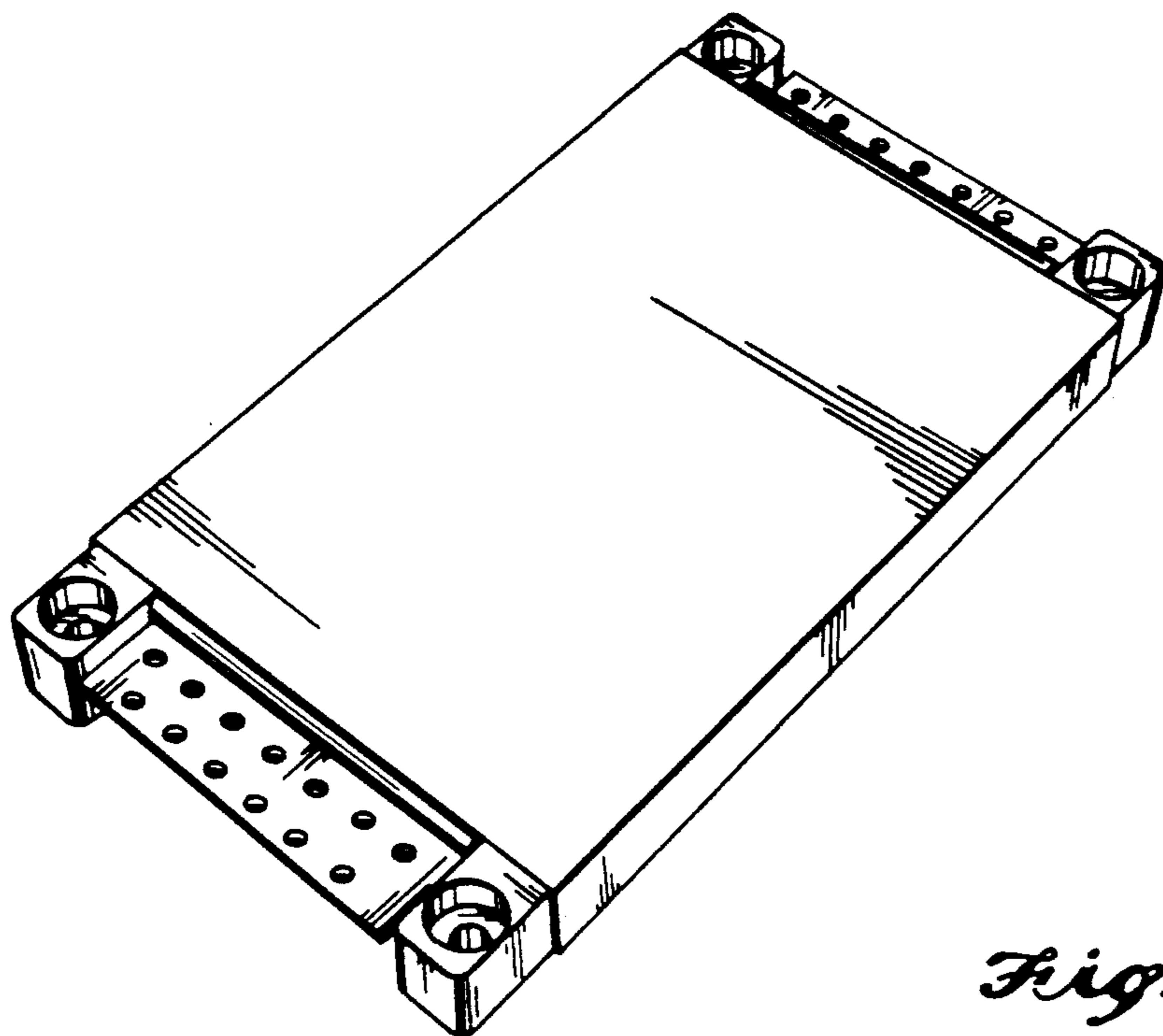


Fig. 5.

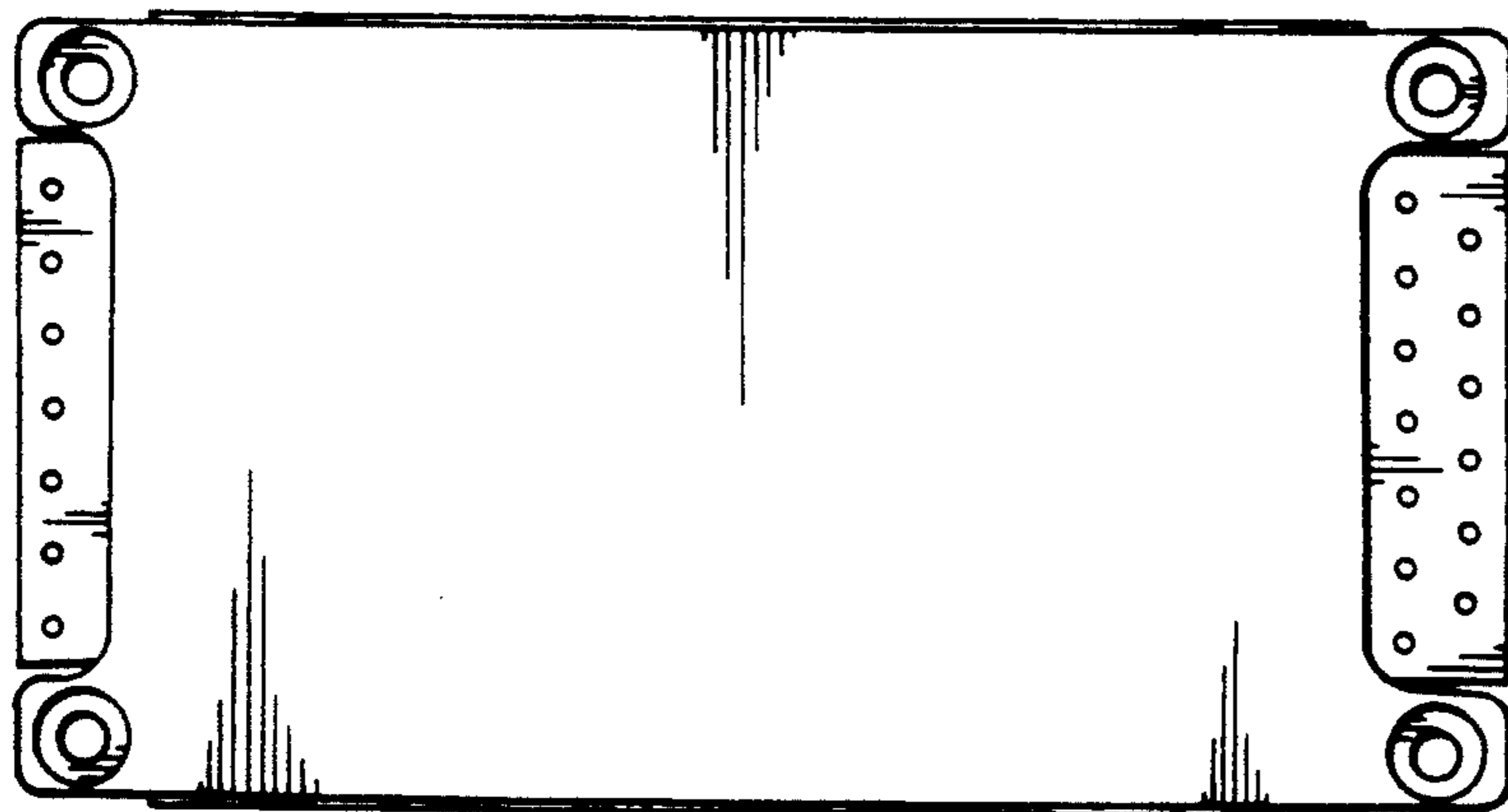


Fig. 6.



Fig. 7.



Fig. 8.