



US00D347182S

# United States Patent [19]

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Wada

[45] Date of Patent: **\*\* May 24, 1994**

[54] **CLOCK**

D. 329,985 10/1992 Takashima ..... D10/28  
D. 329,986 10/1992 Inora ..... D10/28

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*Primary Examiner*—Nelson C. Holtje  
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[73] Assignee: **Seikosha Co., Ltd., Japan**

[\*\*] Term: **14 Years**

[21] Appl. No.: **914,937**

### [57] CLAIM

[22] Filed: **Jul. 15, 1992**

The ornamental design for a clock, as shown and described.

[52] U.S. Cl. .... **D10/28; D10/15**

[58] Field of Search ..... 368/276, 277, 285;  
D10/1-40, 122-132

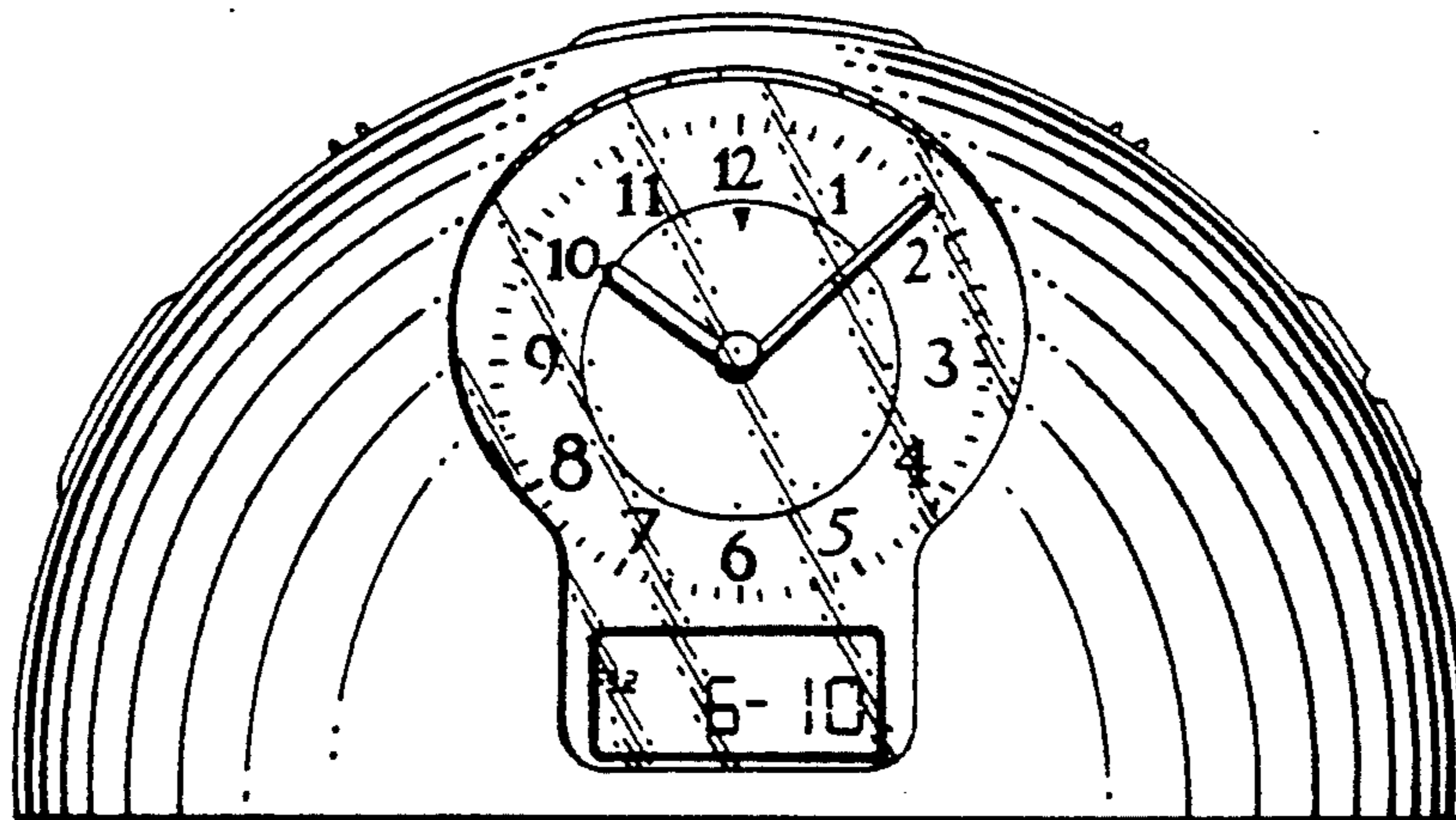
### DESCRIPTION

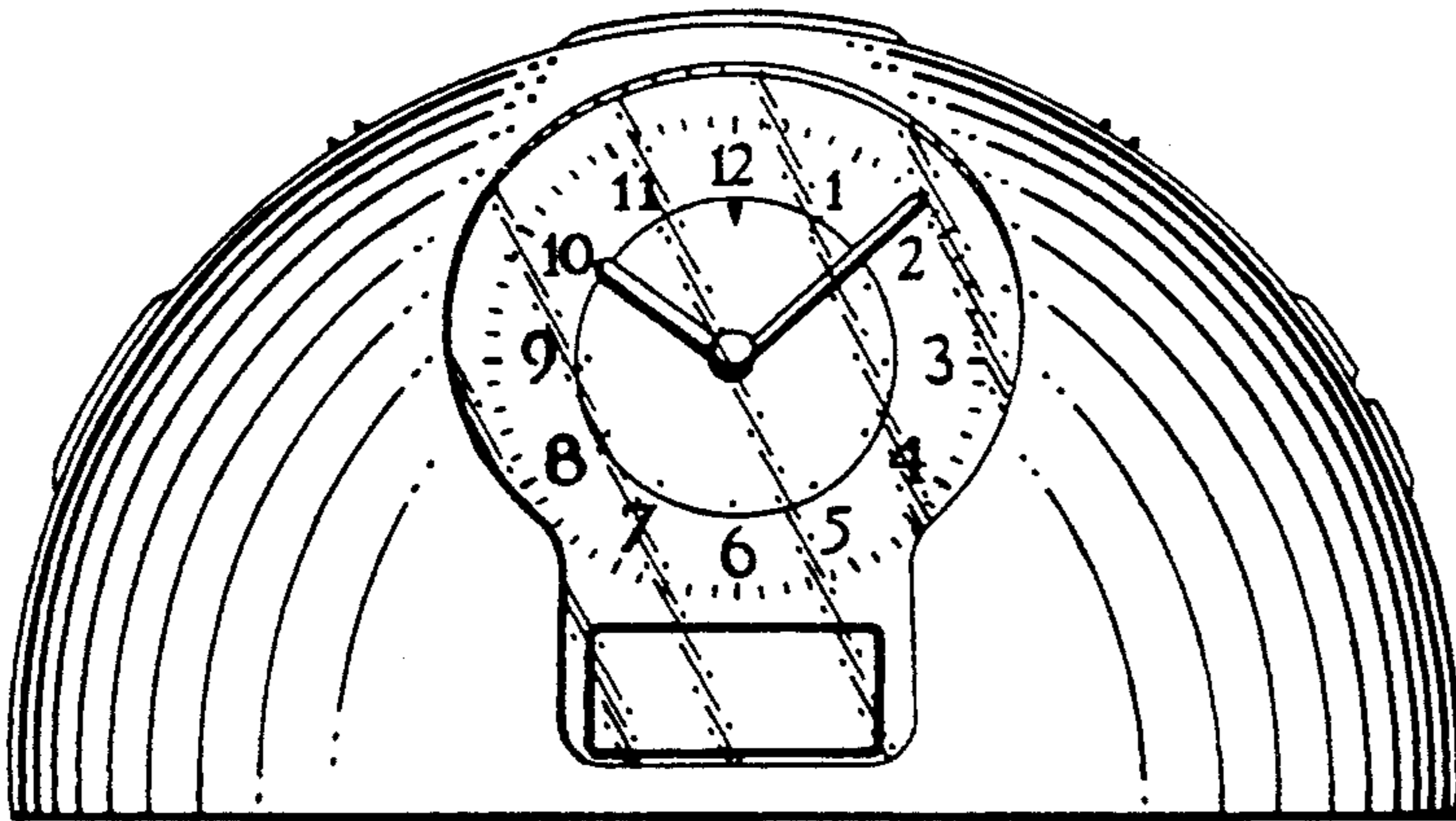
### [56] References Cited

#### U.S. PATENT DOCUMENTS

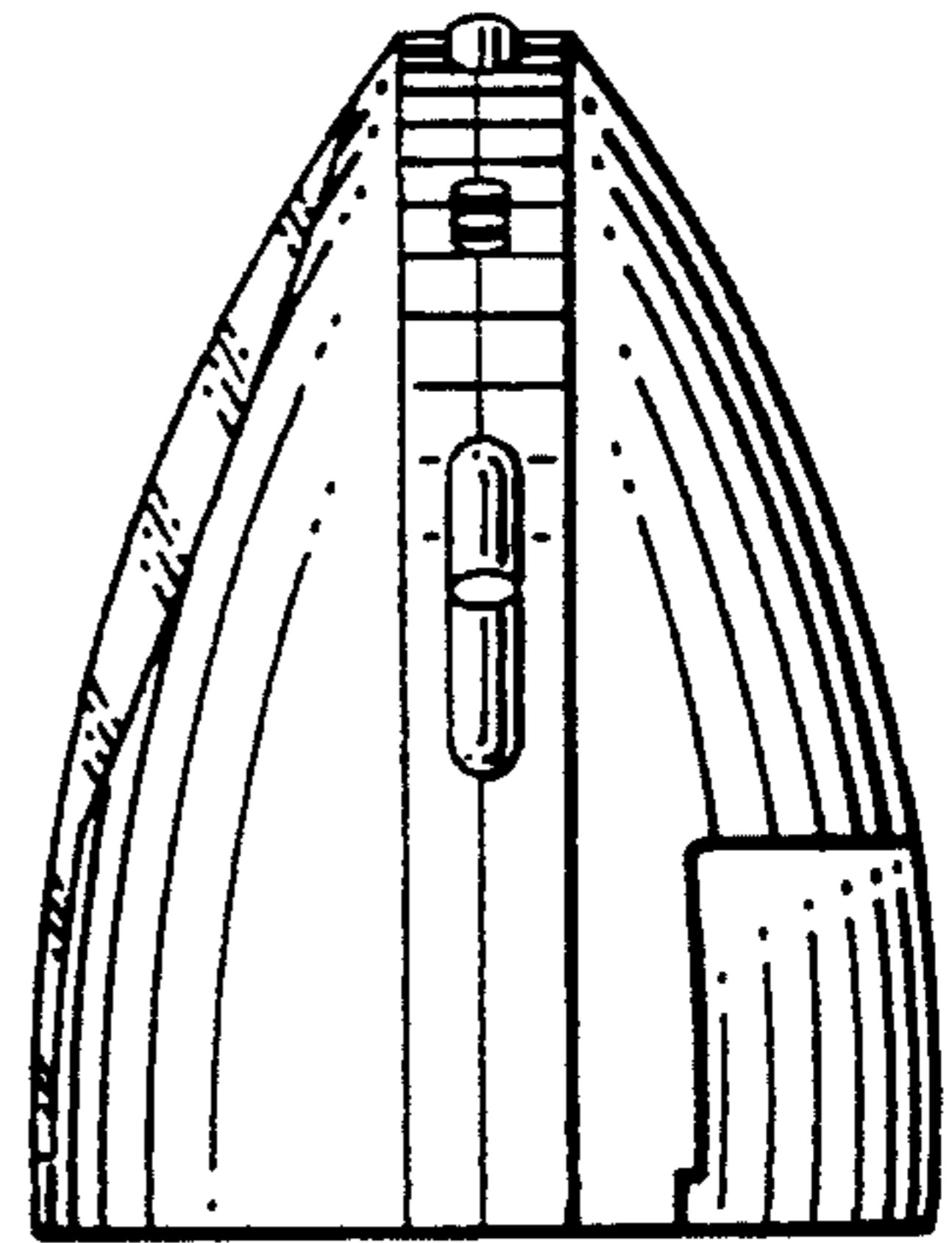
D. 119,889	4/1940	Lux	.....	D10/21 X
D. 314,345	2/1991	Suzuki	.....	D10/28
D. 316,049	4/1991	Kato	.....	D10/28
D. 323,466	1/1992	Albin	.....	D10/28
D. 323,467	1/1992	Takashima	.....	D10/28

FIG. 1 is a front elevational view of a clock showing my new design;  
FIG. 2 is a rear elevational view thereof;  
FIG. 3 is a top plan view thereof;  
FIG. 4 is a bottom plan view thereof;  
FIG. 5 is a right side elevational view thereof;  
FIG. 6 is a left side elevational view thereof; and,  
FIG. 7 is a front elevational view thereof showing a representative energized state of the digital display.

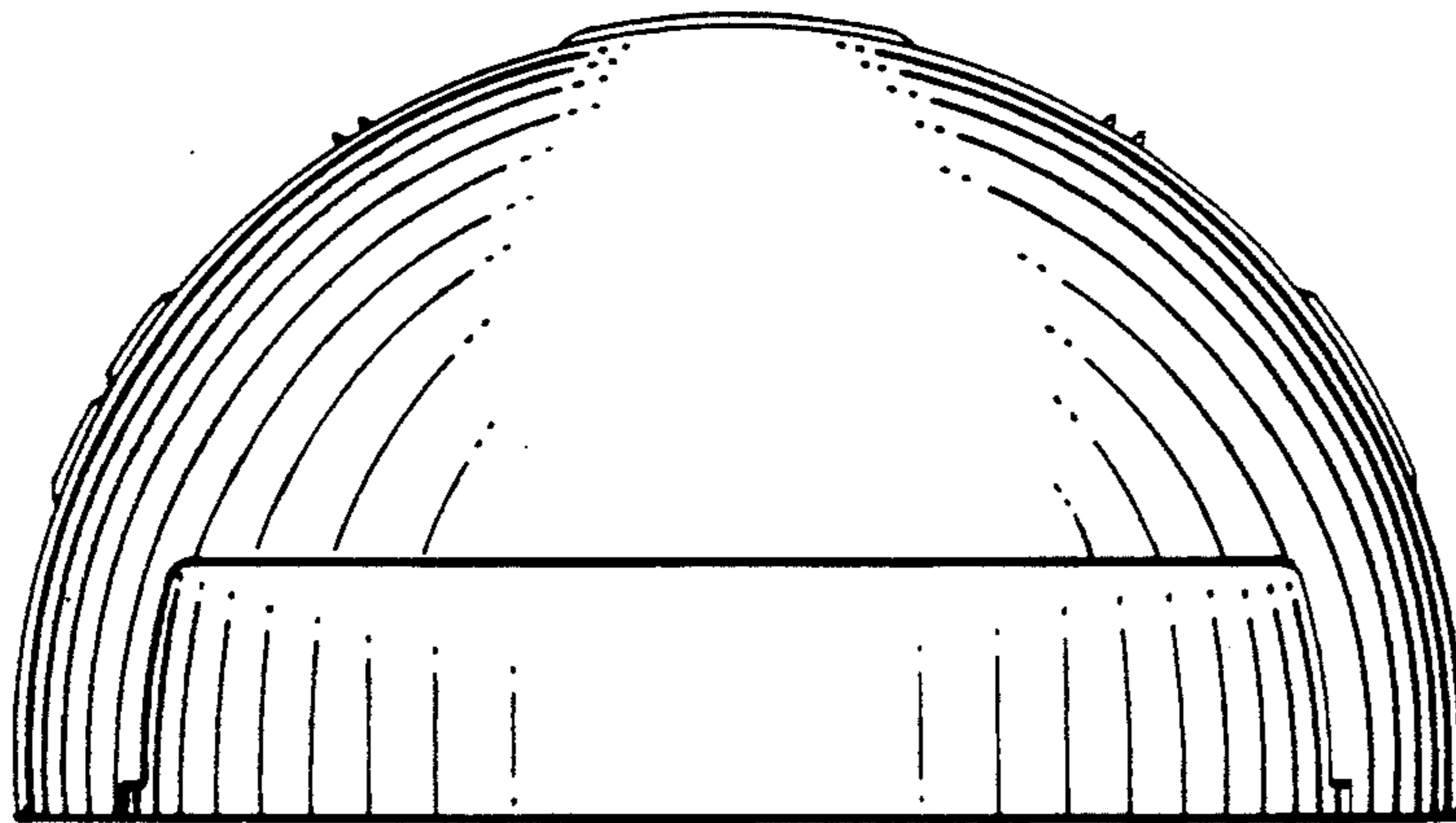




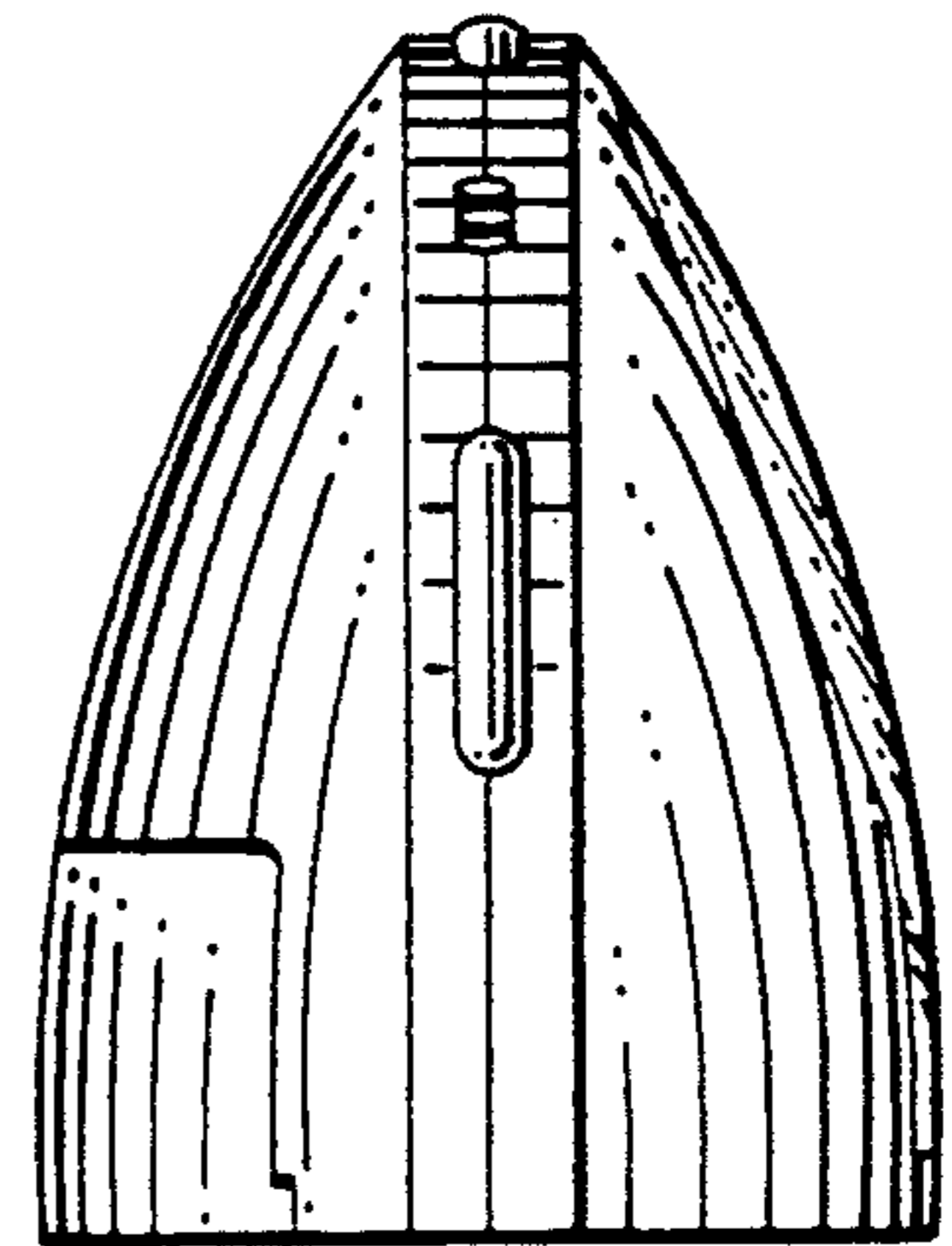
**FIG. 1**



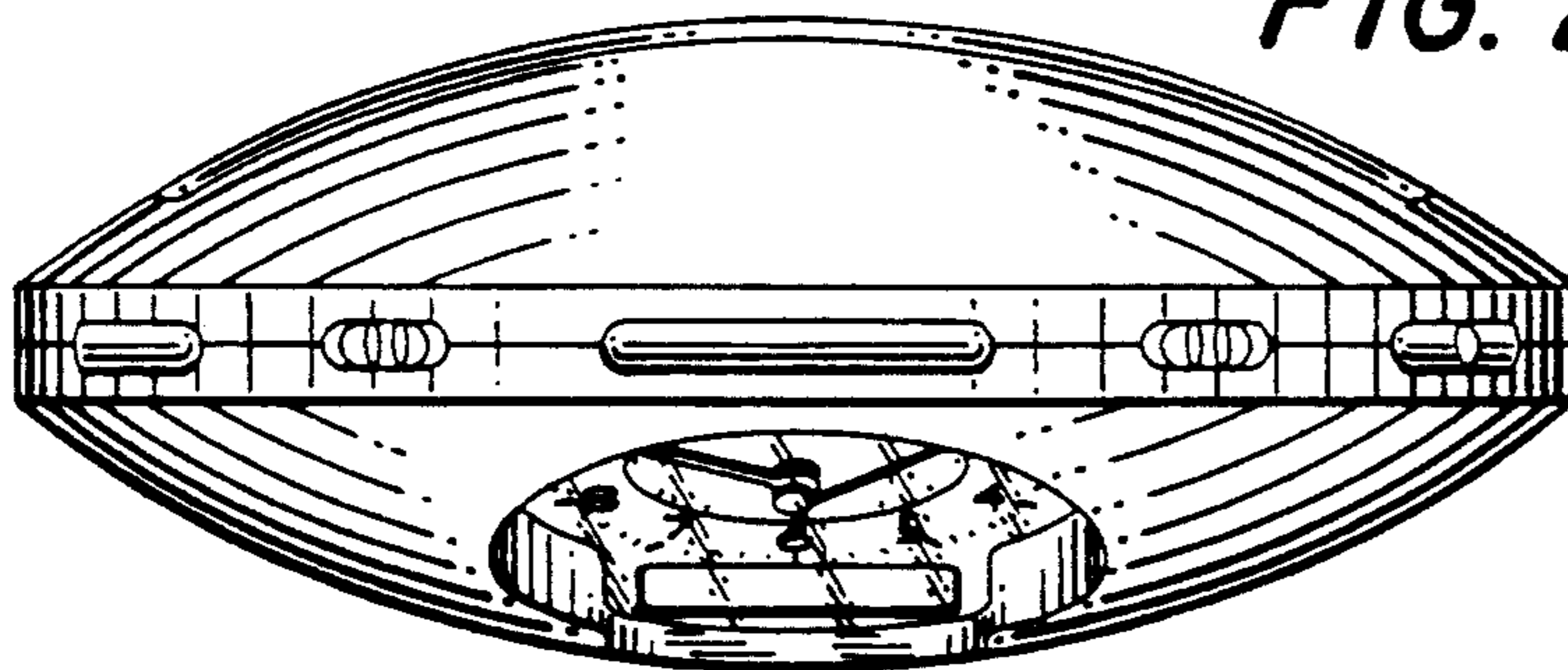
**FIG. 5**



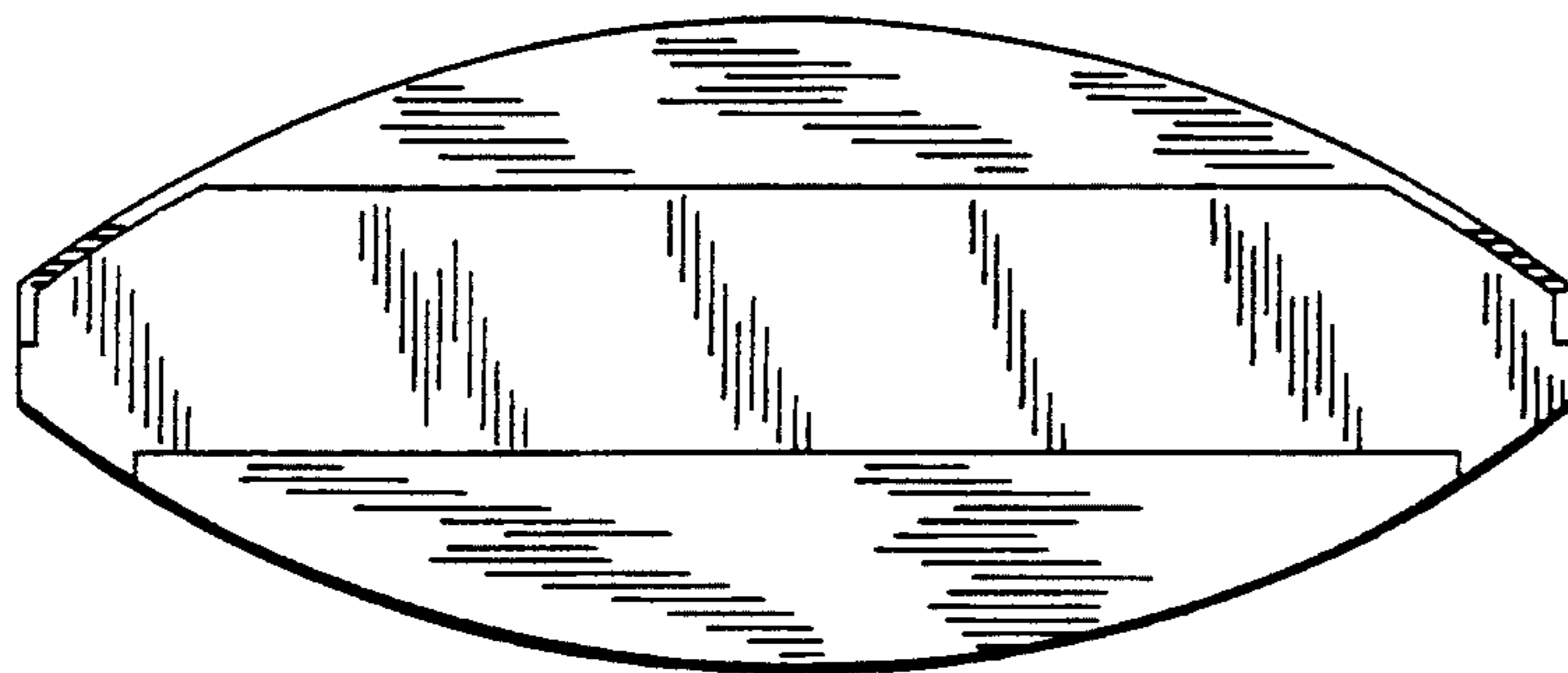
**FIG. 2**



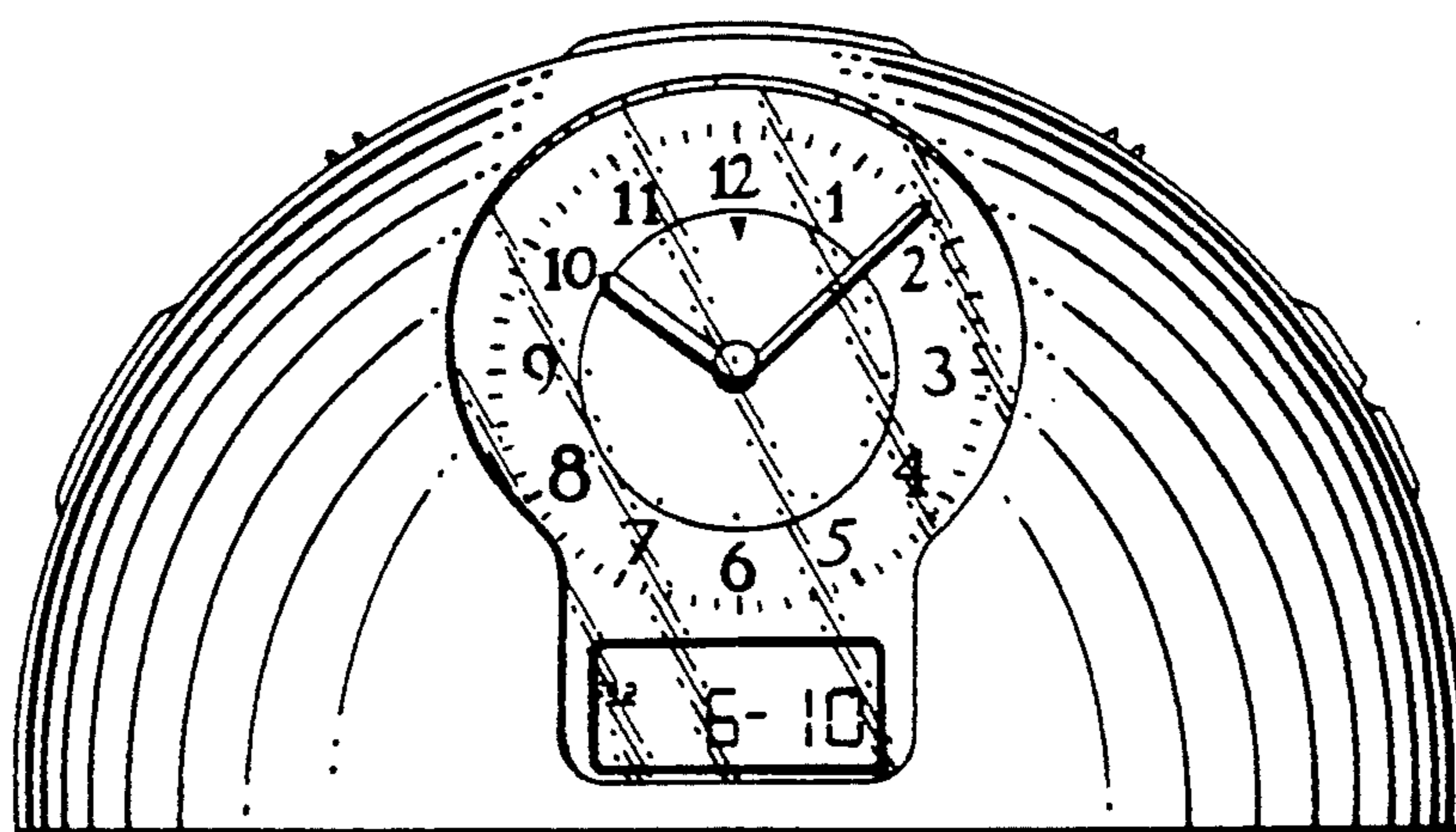
**FIG. 6**



**FIG. 3**



**FIG. 4**



**FIG. 7**