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United States Patent [19]

Alden et al.

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[54] **ORNAMENTAL DESIGN FOR A CIRCUIT PACK FACEPLATE**

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[73] Assignee: **AT&T Bell Laboratories**, Murray Hill, N.J.

[**] Term: **14 Years**

[21] Appl. No.: **807,383**

[22] Filed: **Dec. 16, 1991**

[52] **U.S. Cl.** **D13/177**

[58] **Field of Search** D8/353; 361/331, 380, 361/392, 394, 413, 417; D13/162, 163, 168, 177

[56] **References Cited**

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D. 236,733	9/1975	Clement et al.	D13/168
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D. 303,657	9/1989	Mayo et al.	D13/168

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[57] **CLAIM**

The ornamental design for a circuit pack faceplate, as shown and described.

DESCRIPTION

FIG. **1** is a front view of a circuit pack faceplate in accordance with the embodiment of the design;

FIG. **2** is a side view thereof;

FIG. **3** is a view of the side opposite to that shown in FIG. **2**;

FIG. **4** is a rear view thereof;

FIG. **5** is a top view thereof;

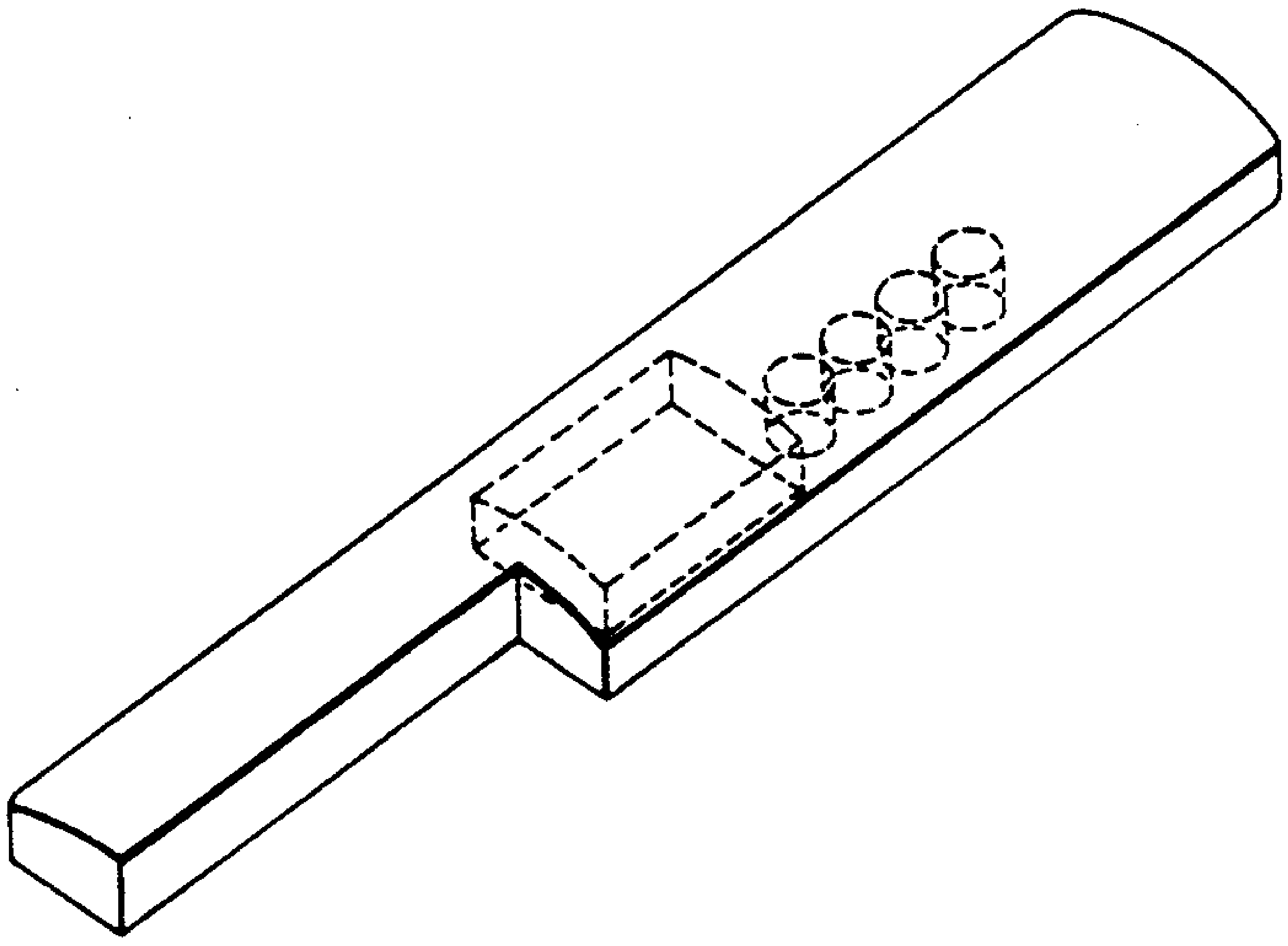
FIG. **6** is a bottom view thereof;

FIG. **7** is a perspective view thereof;

FIG. **8** is a front view of a circuit pack faceplate in accordance with the second embodiment of the design; and,

FIG. **9** is a side view thereof, all other views are the same as the first embodiment except for the presence of the two ribs at the top of the front.

The broken line showing throughout the drawing figures is included for the purpose of illustrating environmental elements only and forms no part of the claimed design.



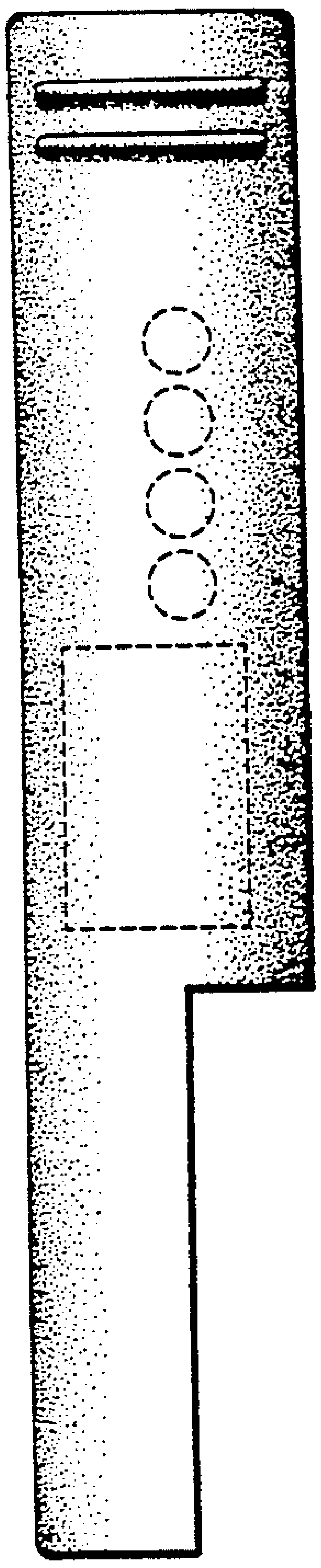


FIG. 1

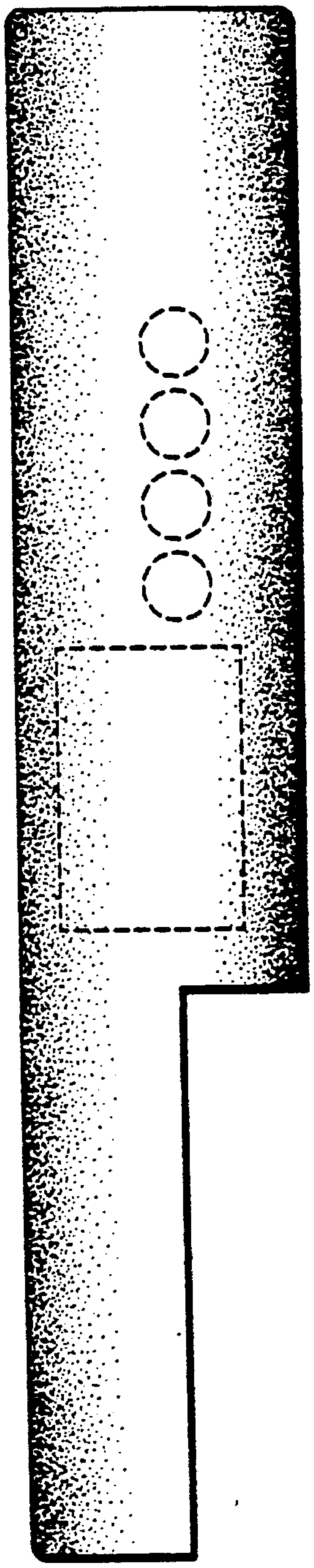


FIG. 2

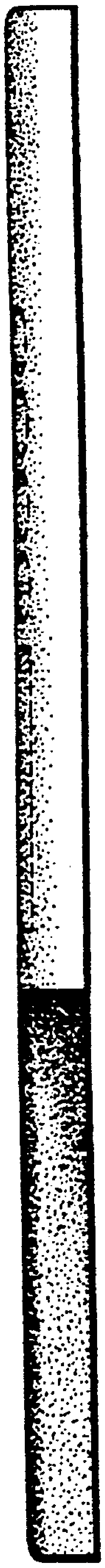


FIG. 3

FIG. 4

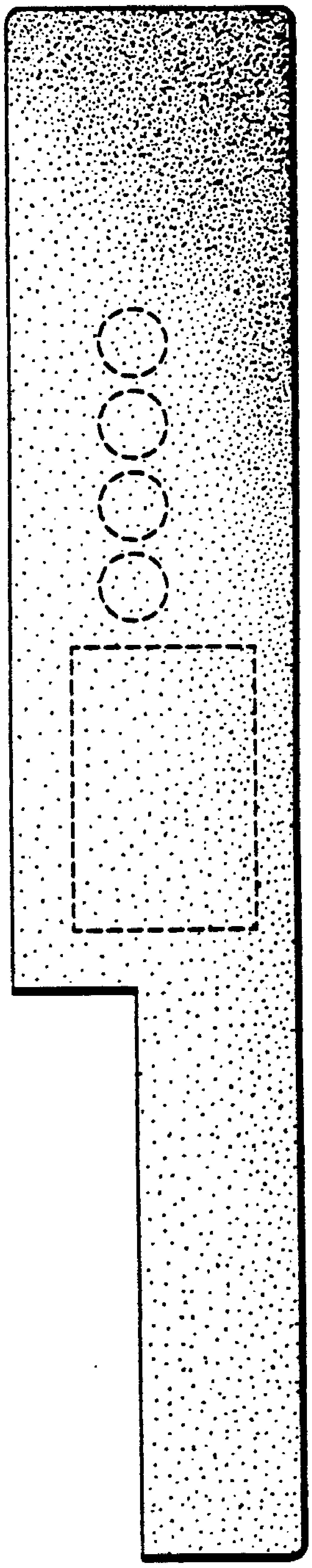


FIG. 5

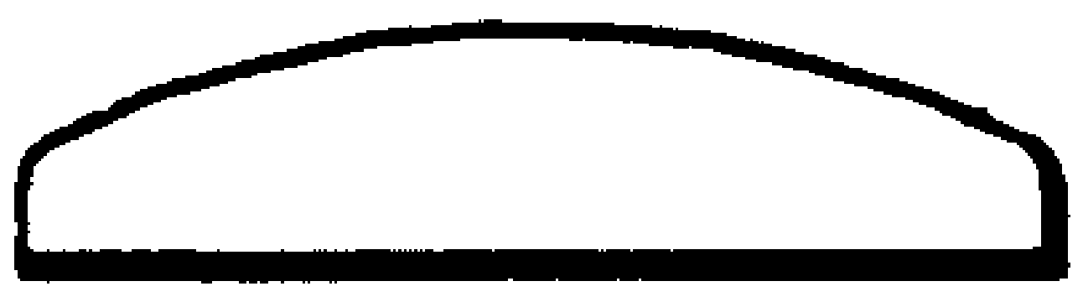


FIG. 6

FIG. 7

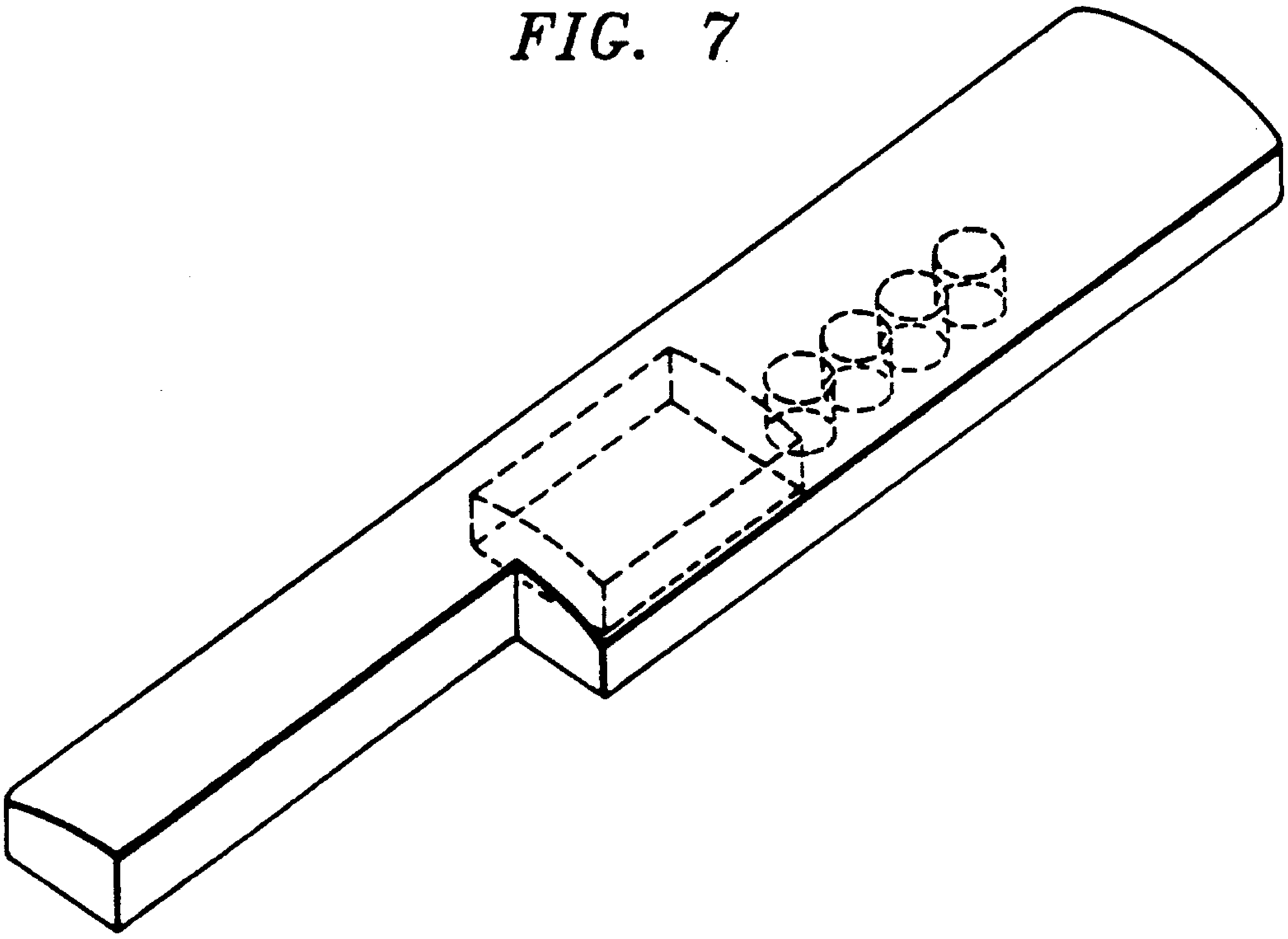


FIG. 8

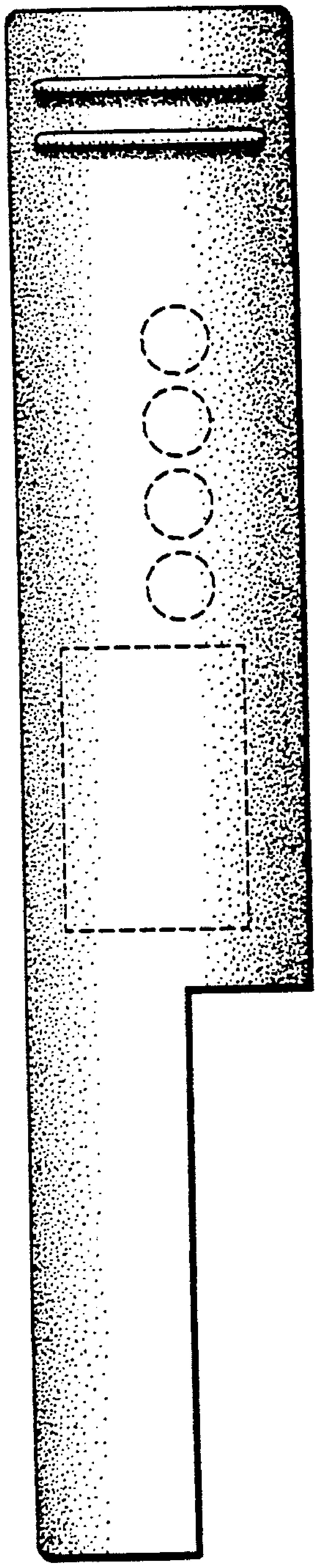


FIG. 9