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United States Patent [19]

Cooper

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[54] **ELECTRONIC CIRCUIT HOUSING**

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[73] Assignee: **ELDEC Corporation,** Lynnwood, Wash.

[**] Term: **14 Years**

[21] Appl. No.: **954,930**

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[52] **U.S. Cl.** **D13/110**

[58] **Field of Search** 361/111, 386, 388, 392; 336/90, 92, 200; 363/15, 95; 307/150, 151; 174/52.1, 52.2; D13/110, 123, 124, 184

[56] **References Cited**

U.S. PATENT DOCUMENTS

D. 224,971	10/1972	Projain et al.	D13/124
D. 297,928	10/1988	Harpley et al.	D13/4
2,876,424	3/1959	Stiller	336/192
4,012,672	3/1977	Douglass et al.	336/2 X
4,487,999	12/1984	Baird et al.	174/52
4,656,450	4/1987	Jarosz et al.	336/82 X
5,043,211	8/1991	Yoshizumi et al.	174/52.2 X
5,119,266	6/1992	Petry	361/111

OTHER PUBLICATIONS

Keltec Florida, Inc., "Keltec Military Superiority, DC-DC Power Converters," publication date unknown.

Vicor Corporation, "Vicor Express Component Power Solutions," publication date unknown.

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[57] **CLAIM**

The ornamental design for an electronic circuit housing, as shown and described.

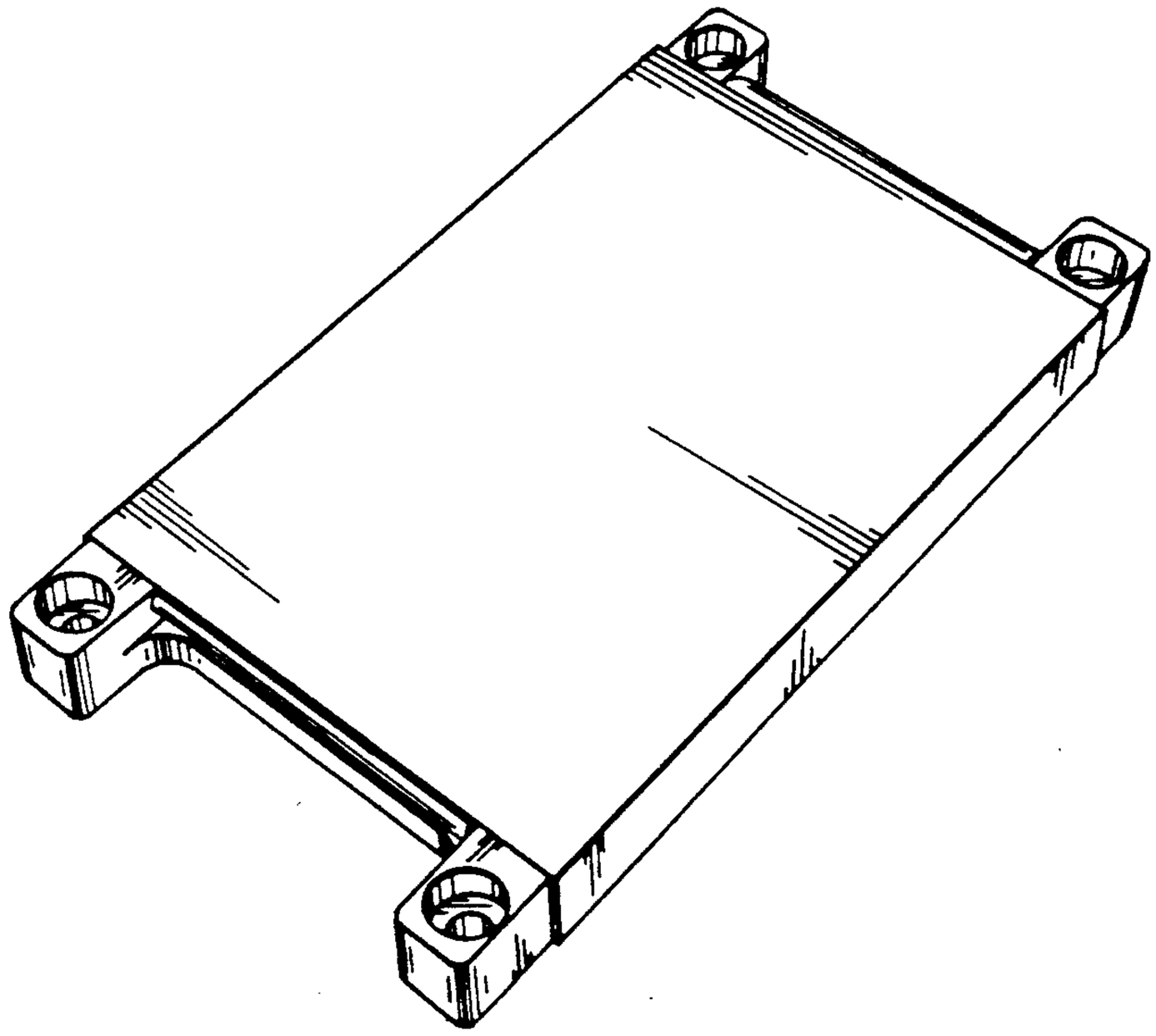
DESCRIPTION

FIG. **1** is a top front perspective view of an electronic circuit housing showing my new design;

FIG. **2** is a bottom plan view thereof;

FIG. **3** is an end elevational view thereof taken from the left of FIG. **2,** the opposite end thereof being the mirror image of the end shown in FIG. **3;** and,

FIG. **4** is a rear elevational view thereof.



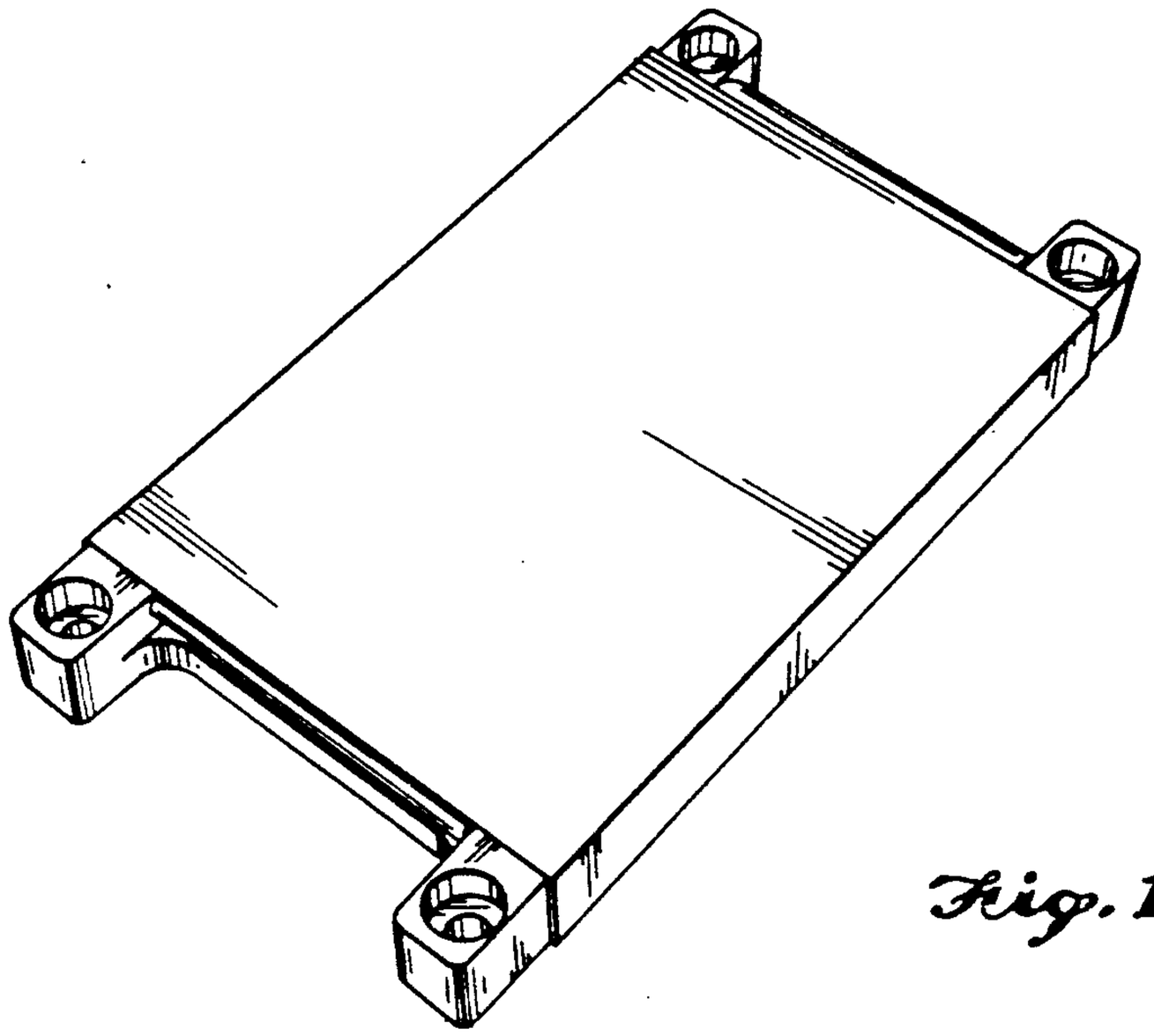


Fig. 1.

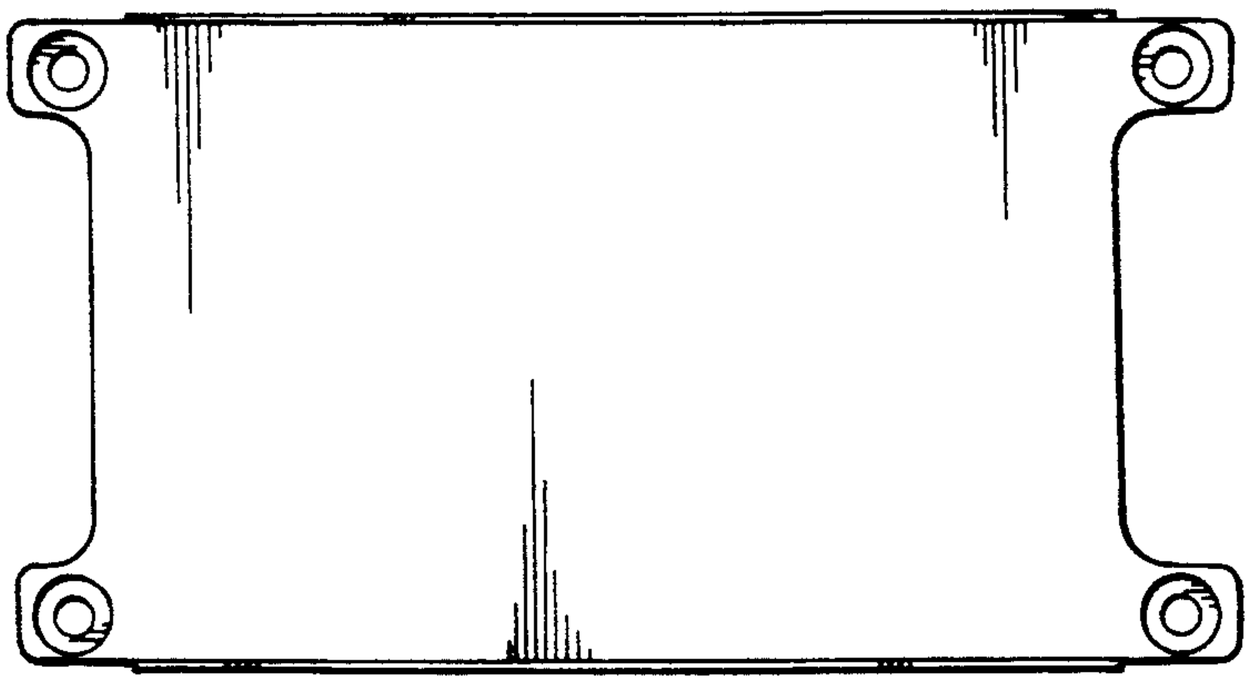


Fig. 2.

Fig. 3.

Fig. 4.