



US00D344710S

# United States Patent [19]

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Morgan et al.

[45] Date of Patent: **\*\* Mar. 1, 1994**

[54] **ELECTRONIC DEVICE MODULE**

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[73] Assignee: **Digital Equipment Corporation**,  
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[\*] Notice: The portion of the term of this patent  
subsequent to Jun. 1, 2010 has been  
disclaimed.

[\*\*] Term: **14 Years**

[21] Appl. No.: **683,367**

[22] Filed: **Apr. 8, 1991**

[52] U.S. Cl. .... **D14/114; D14/107**

[58] Field of Search ..... **361/392, 393; D14/100,**  
**D14/107, 114; D13/147, 162, 199**

[56] **References Cited**

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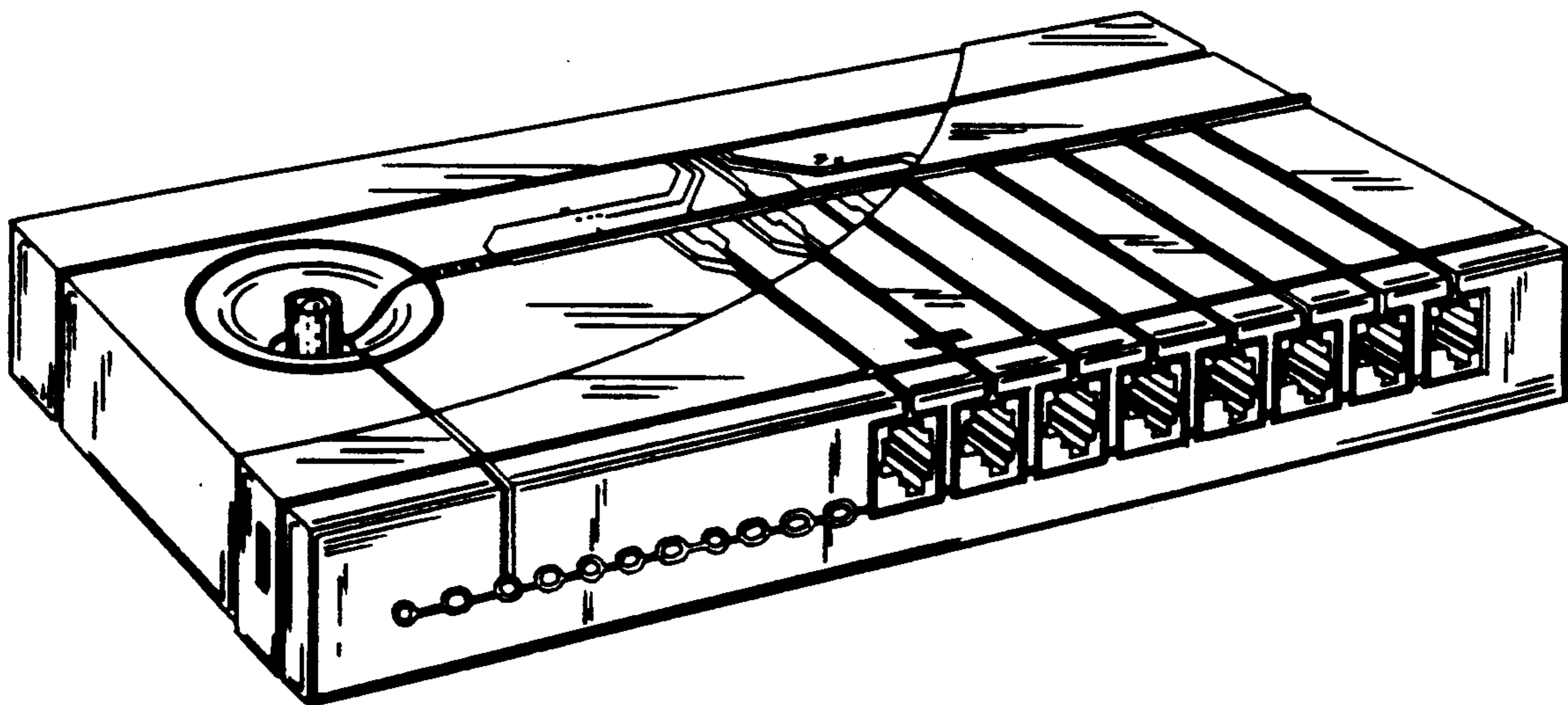
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Hudgens; Richard Paciulan

[57] **CLAIM**

The ornamental design for an electronic device module,  
as shown and described.

**DESCRIPTION**

FIG. 1 is a front left side perspective view of the elec-  
tronic device module showing my new design;  
FIG. 2 is a left side elevational view thereof;  
FIG. 3 is a right side elevational view thereof;  
FIG. 4 is a front elevational view thereof;  
FIG. 5 is a top plan view thereof;  
FIG. 6 is a rear elevational view thereof; and,  
FIG. 7 is a bottom plan view thereof.  
FIGS. 2-7 are drawn on an enlarged scale.



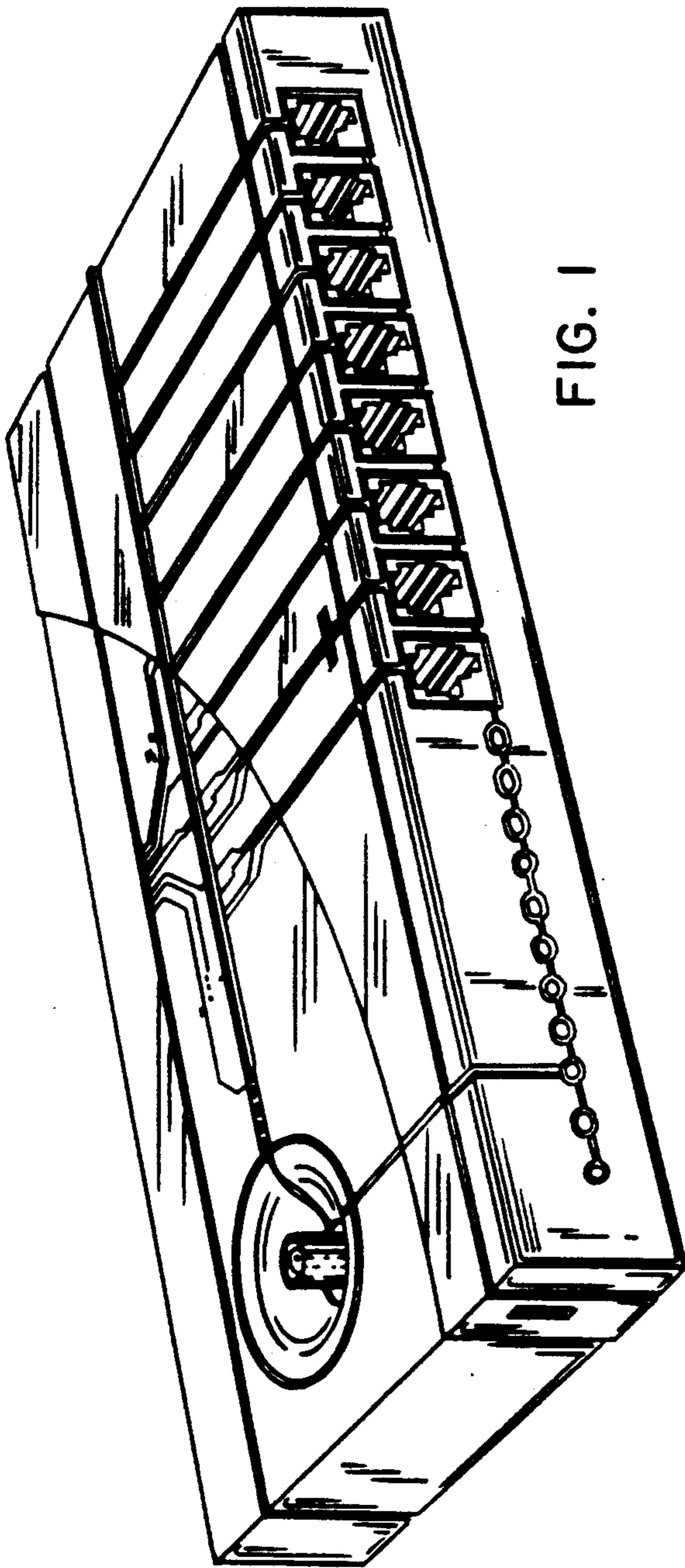


FIG. 1

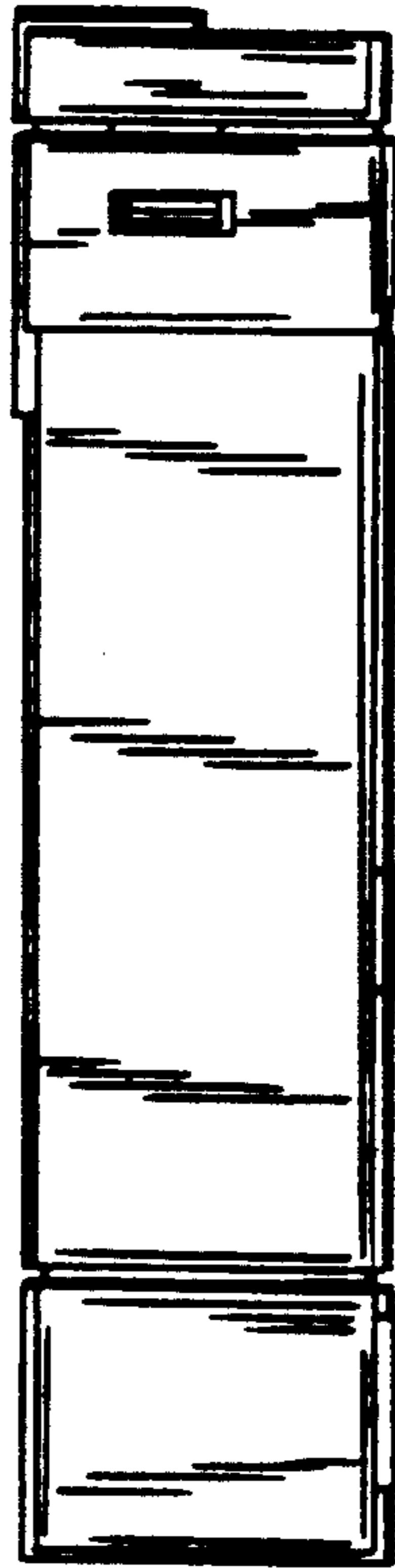


FIG. 2

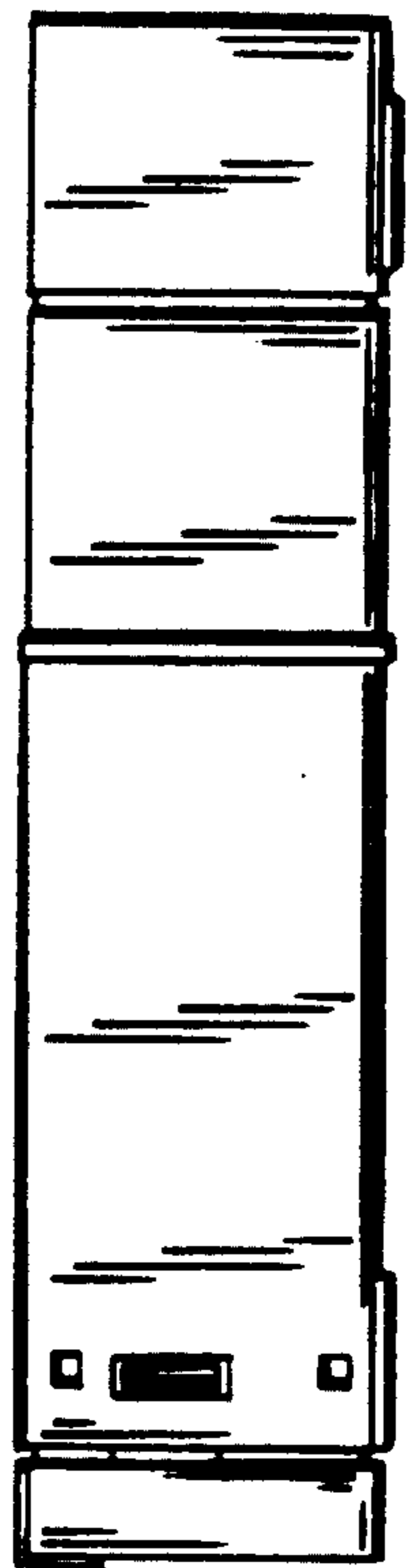


FIG. 3

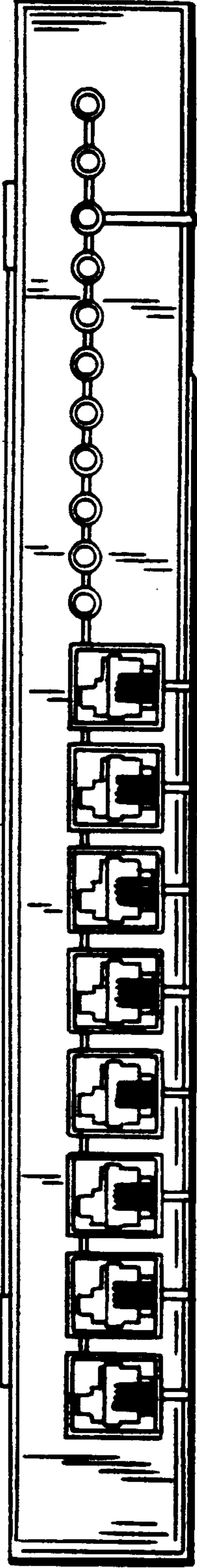


FIG. 4

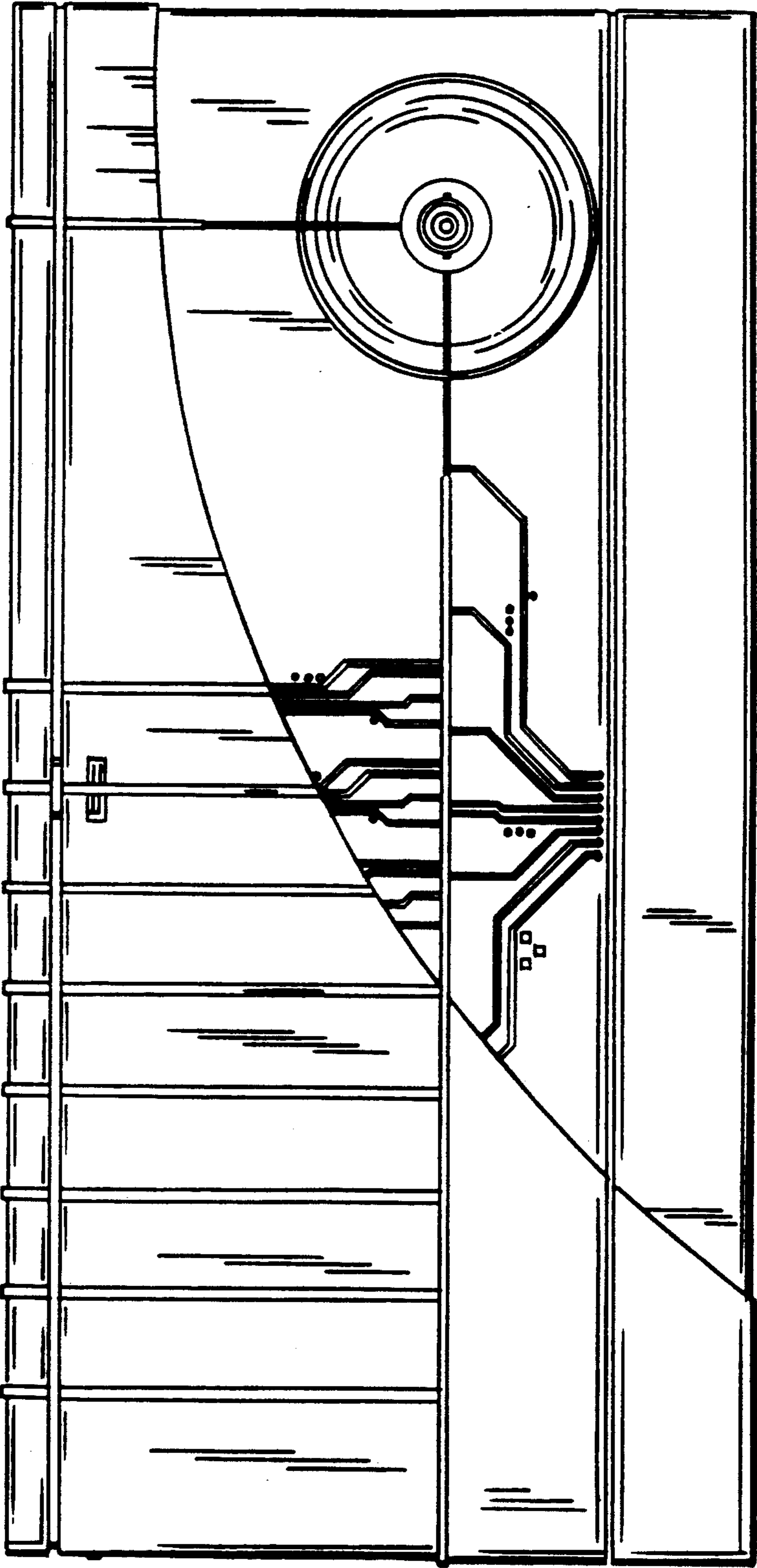


FIG. 5

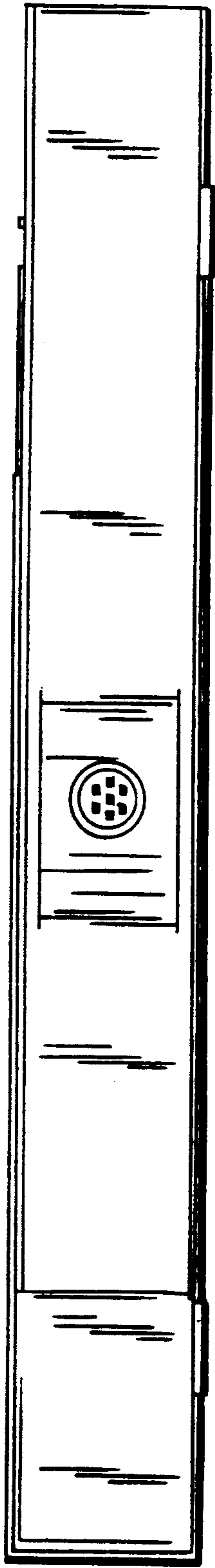


FIG. 6

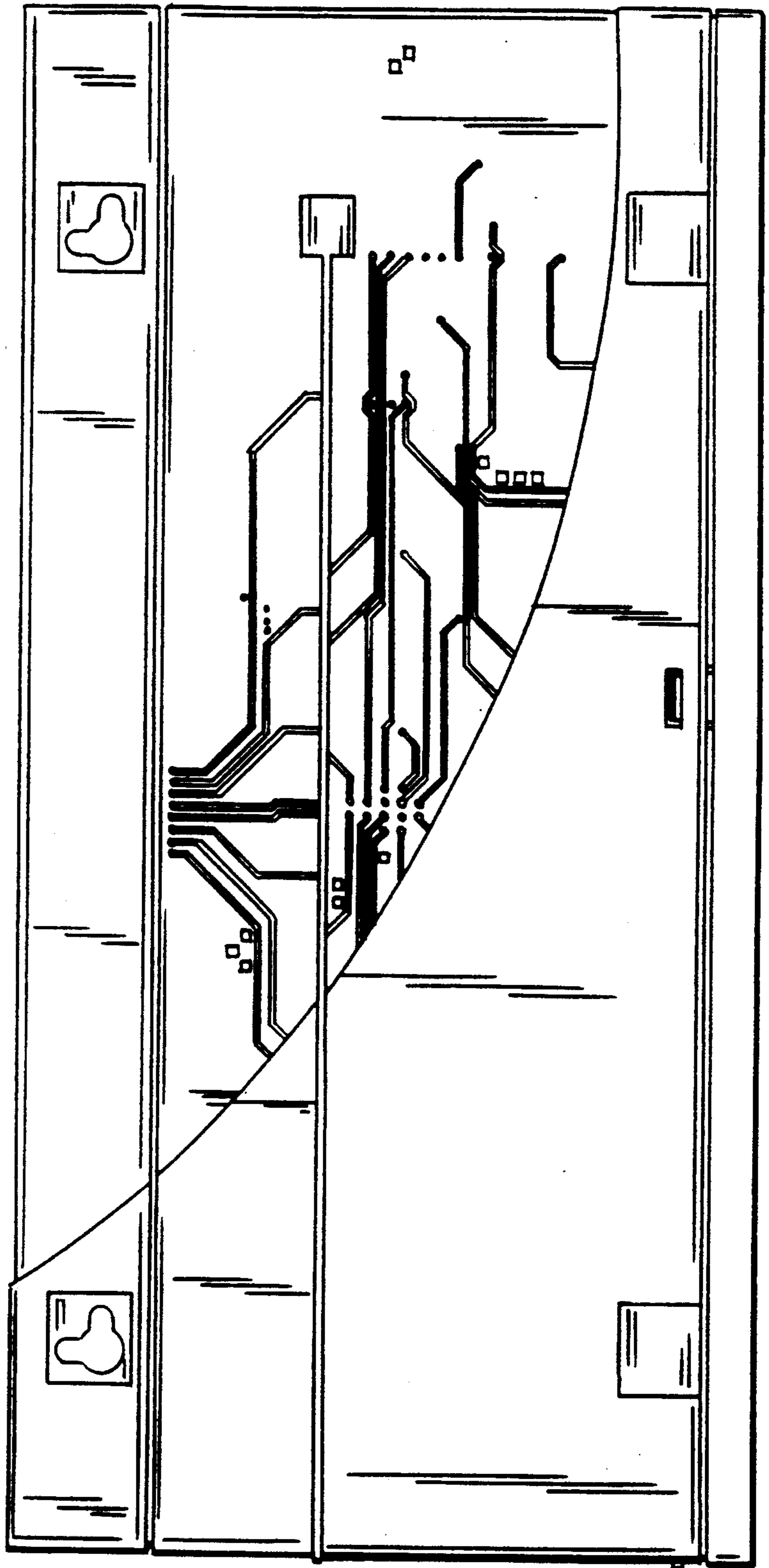


FIG. 7