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United States Patent [19]

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Ueda et al.

[45] Date of Patent: **** Feb. 15, 1994**

[54] **ARITHMETIC CONTROL UNIT FOR AN ELECTRONIC COMPUTER**

[56] **References Cited**

U.S. PATENT DOCUMENTS

[75] Inventors: **Hiroshi Ueda; Taneaki Chiba**, both of Tokyo; **Akiyo Iizuka**, Niigata; **Kimio Nobeashi**, Tokyo, all of Japan

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[73] Assignee: **NEC Corporation**, Tokyo, Japan

Primary Examiner—Wallace R. Burke
Assistant Examiner—Freda S. Nunn
Attorney, Agent, or Firm—Sughrue, Mion, Zinn, Macpeak & Seas

[*] Notice: The portion of the term of this patent subsequent to Dec. 17, 2005 has been disclaimed.

[57] CLAIM

[**] Term: **14 Years**

The ornamental design for an arithmetic control unit for an electronic computer, as shown and described.

[21] Appl. No.: **721,862**

DESCRIPTION

[22] Filed: **Jun. 25, 1991**

FIG. 1 is a front elevational view of an arithmetic control unit for an electronic computer showing our new design;

[30] Foreign Application Priority Data

Dec. 25, 1990 [JP] Japan 2-43177

[52] U.S. Cl. **D14/100**

FIG. 2 is a rear elevational view thereof;

FIG. 3 is a top plan view thereof;

FIG. 4 is a bottom plan view thereof;

FIG. 5 is a left side elevational view thereof;

FIG. 6 is a right side elevational view thereof; and,

FIG. 7 is an enlarged front, top and right side perspective view thereof.

[58] Field of Search 360/99.12; 361/331, 361/332, 390-395; D14/100, 102, 108, 109; D13/162, 184, 199

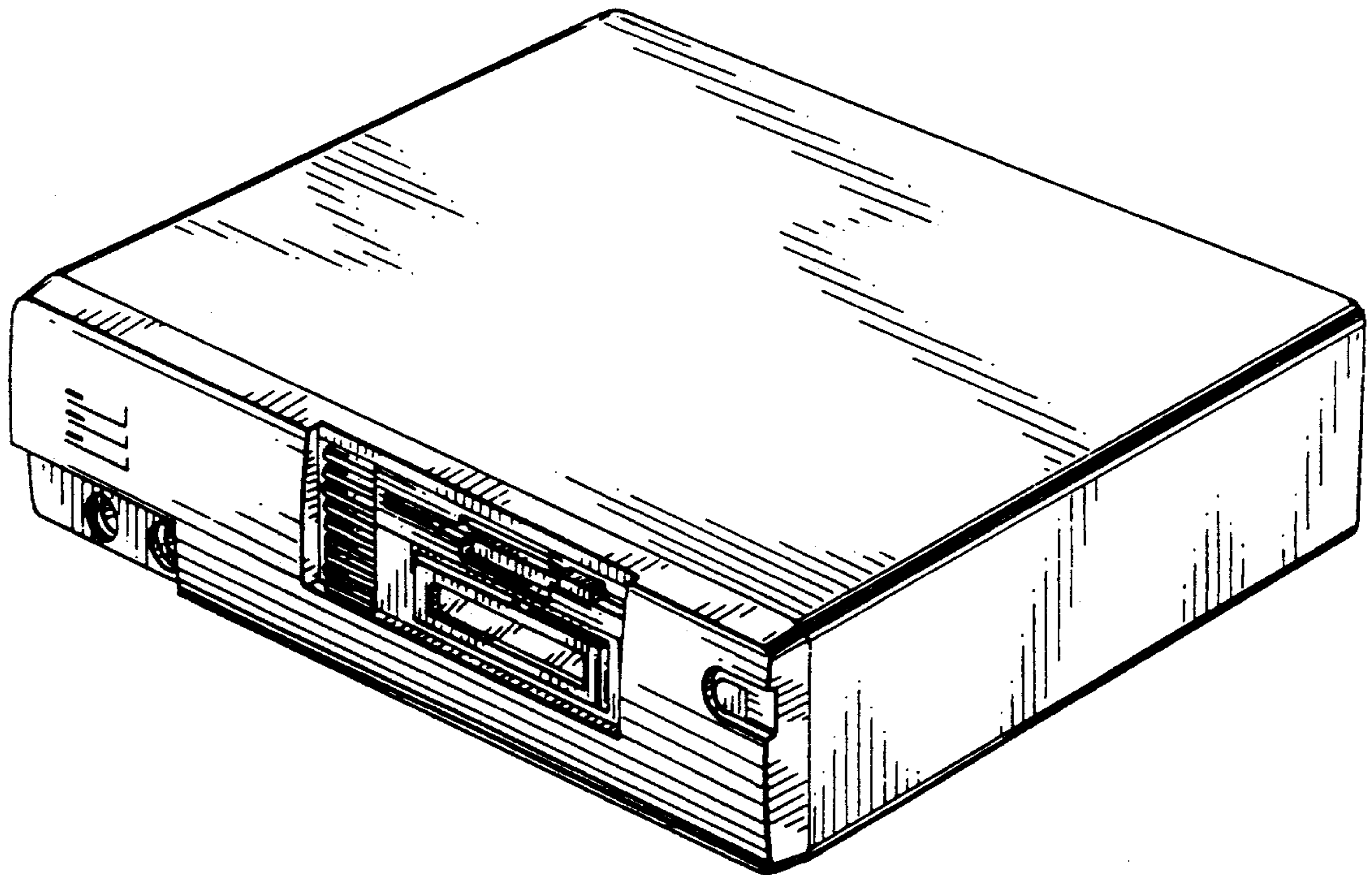


FIG. 1

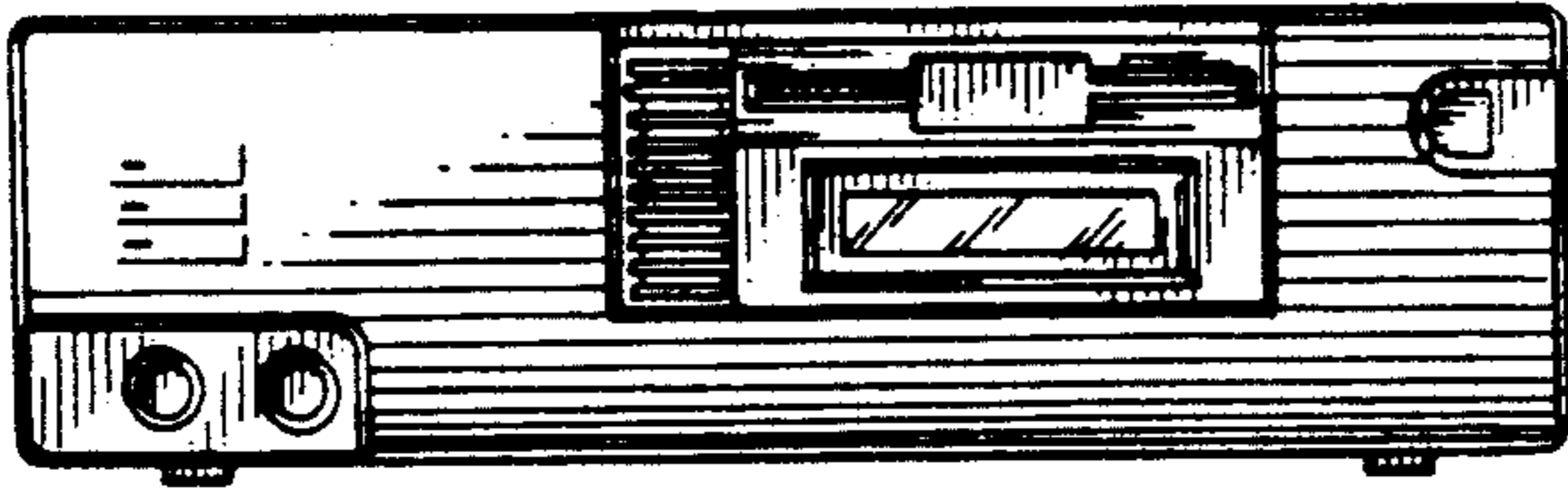


FIG. 2

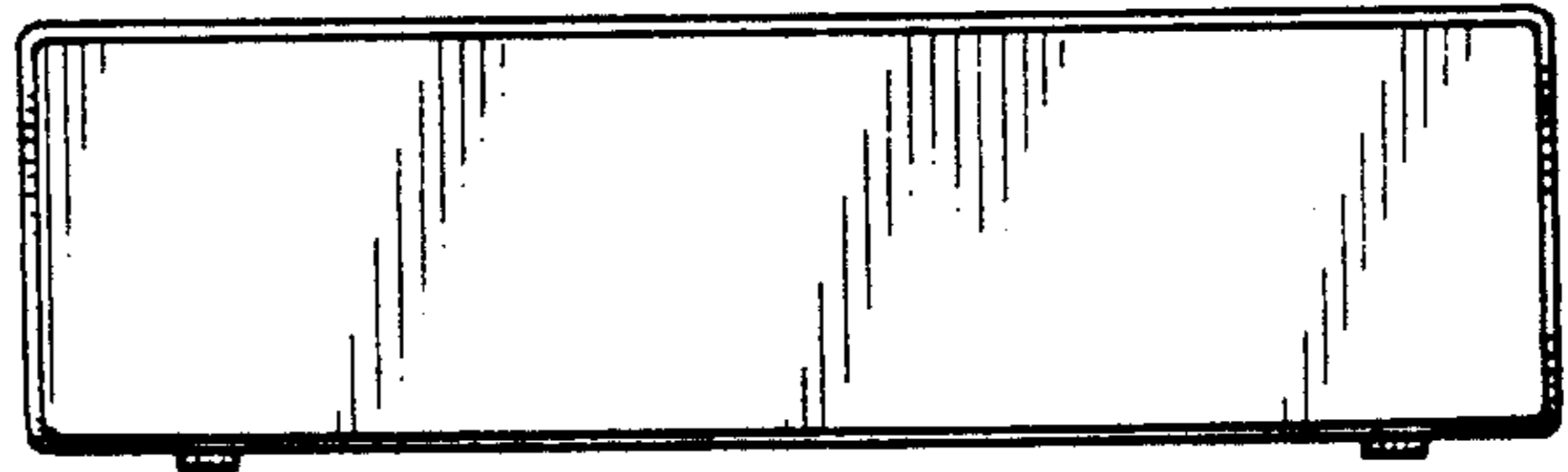


FIG. 3

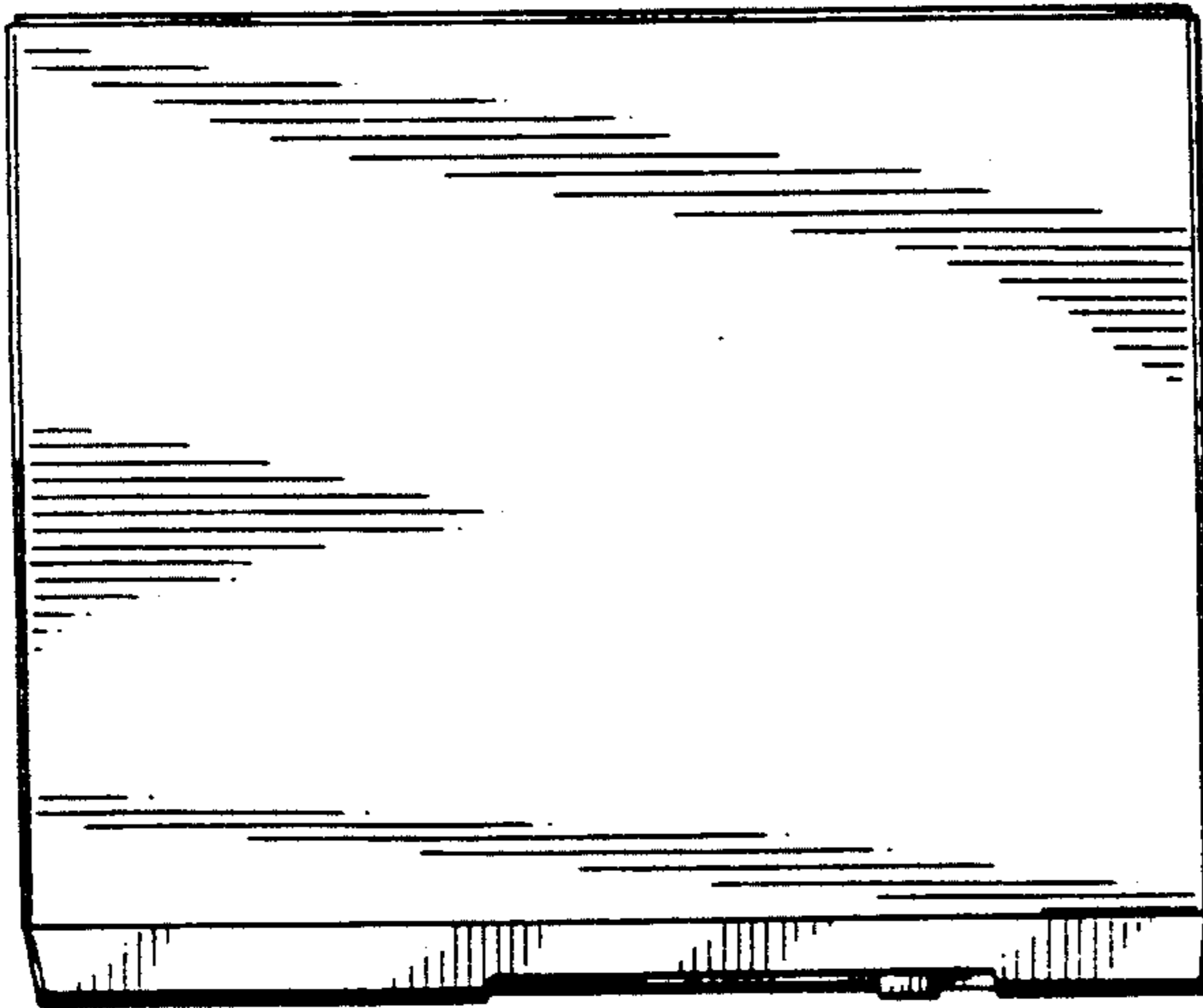


FIG. 4

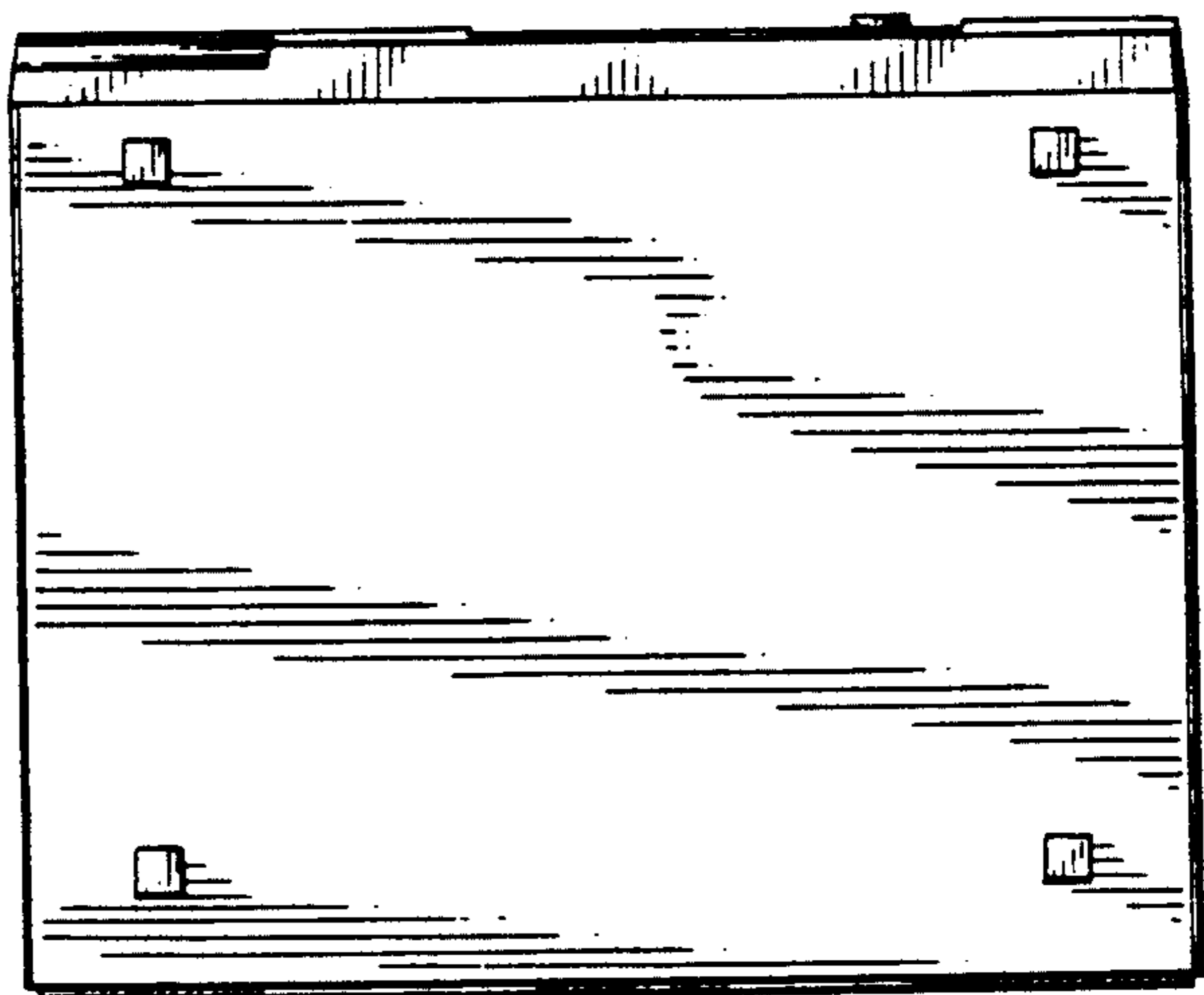


FIG. 5

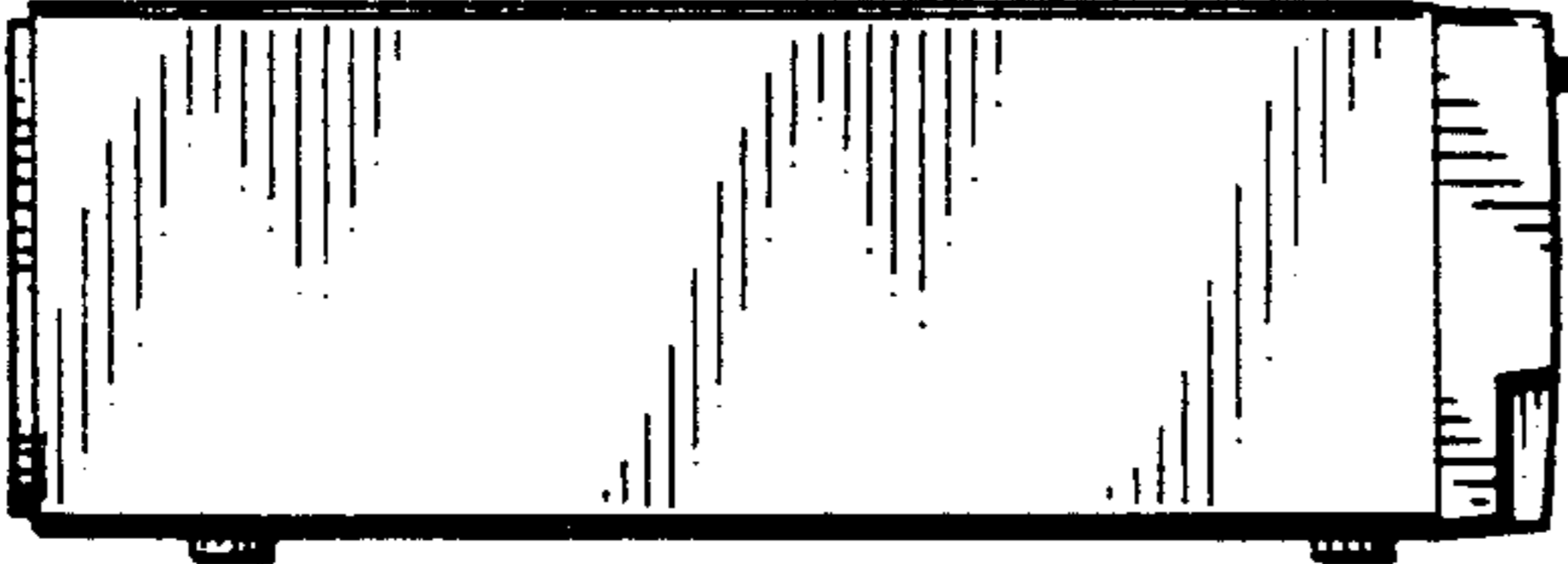


FIG. 6



FIG. 7

