



US00D340917S

United States Patent [19]

Sakaguchi et al.

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[54] MEMORANDUM INPUT AND OUTPUT DEVICE

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[73] Assignee: Sharp Kabushiki Kaisha, Osaka, Japan

[**] Term: 14 Years

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[30] Foreign Application Priority Data

May 29, 1991 [JP] Japan 3-15786

[52] U.S. Cl. D14/100; D18/7

[58] Field of Search D14/100, 106, 115; D18/7; 340/700, 706; 341/22, 23; 235/145 A, 145 R; 364/706-709.09

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[57] CLAIM

The ornamental design for a memorandum input and output device, as shown and described.

DESCRIPTION

FIG. 1 is a top plan view of a memorandum input and output device showing our new design in the closed position;
 FIG. 2 is a bottom plan view thereof;
 FIG. 3 is a front elevational view thereof;
 FIG. 4 is a rear elevational view thereof;
 FIGS. 5 and 6 are right and left side elevational views thereof;
 FIG. 7 is a left side elevational view thereof with the cover in an open position;
 FIG. 8 is a right side elevational view thereof;
 FIG. 9 is a front elevational view thereof;
 FIG. 10 is a top plan view thereof;
 FIG. 11 is a rear elevational view thereof with the cover in the open position; and,
 FIG. 12 is a bottom plan view thereof with the cover in an open position.

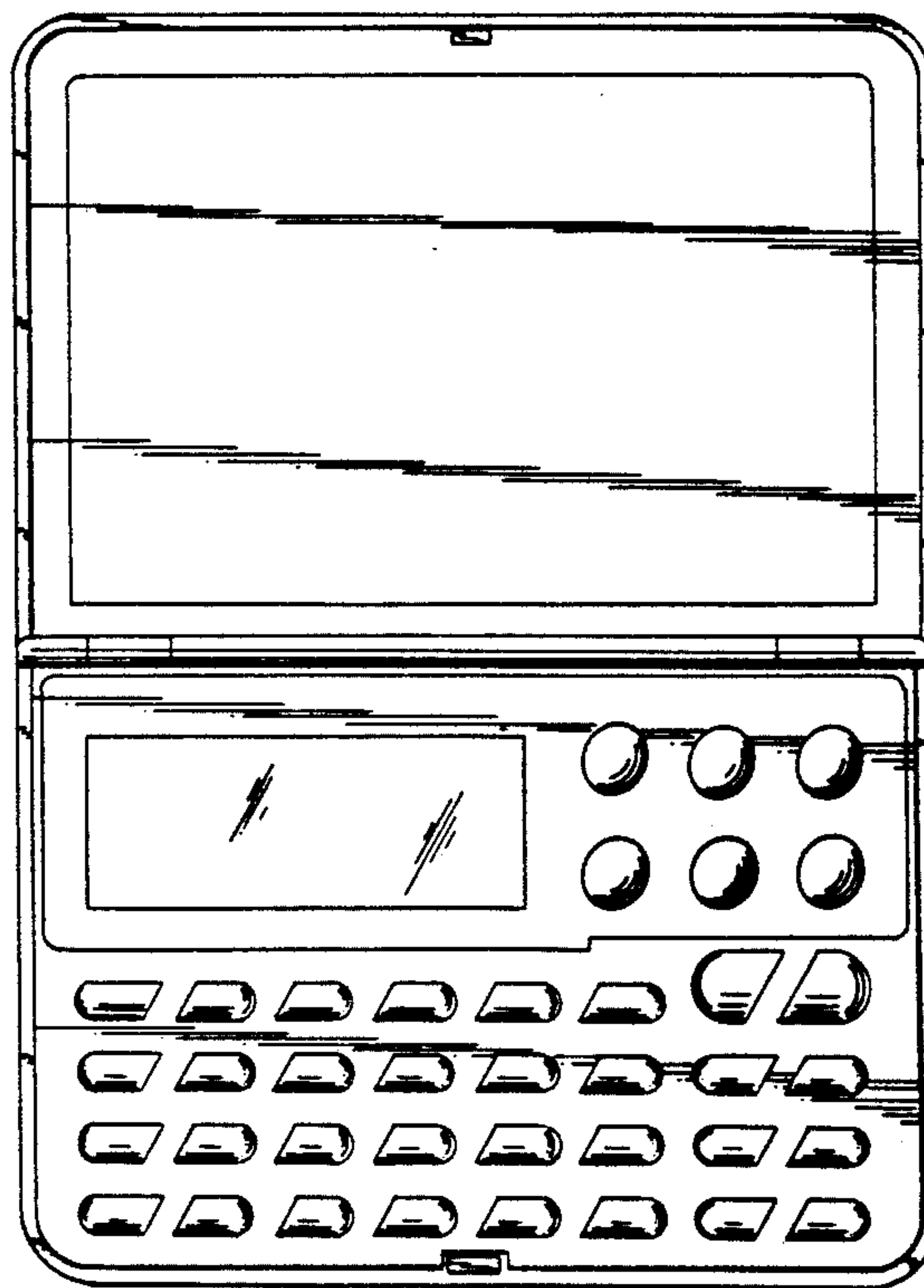
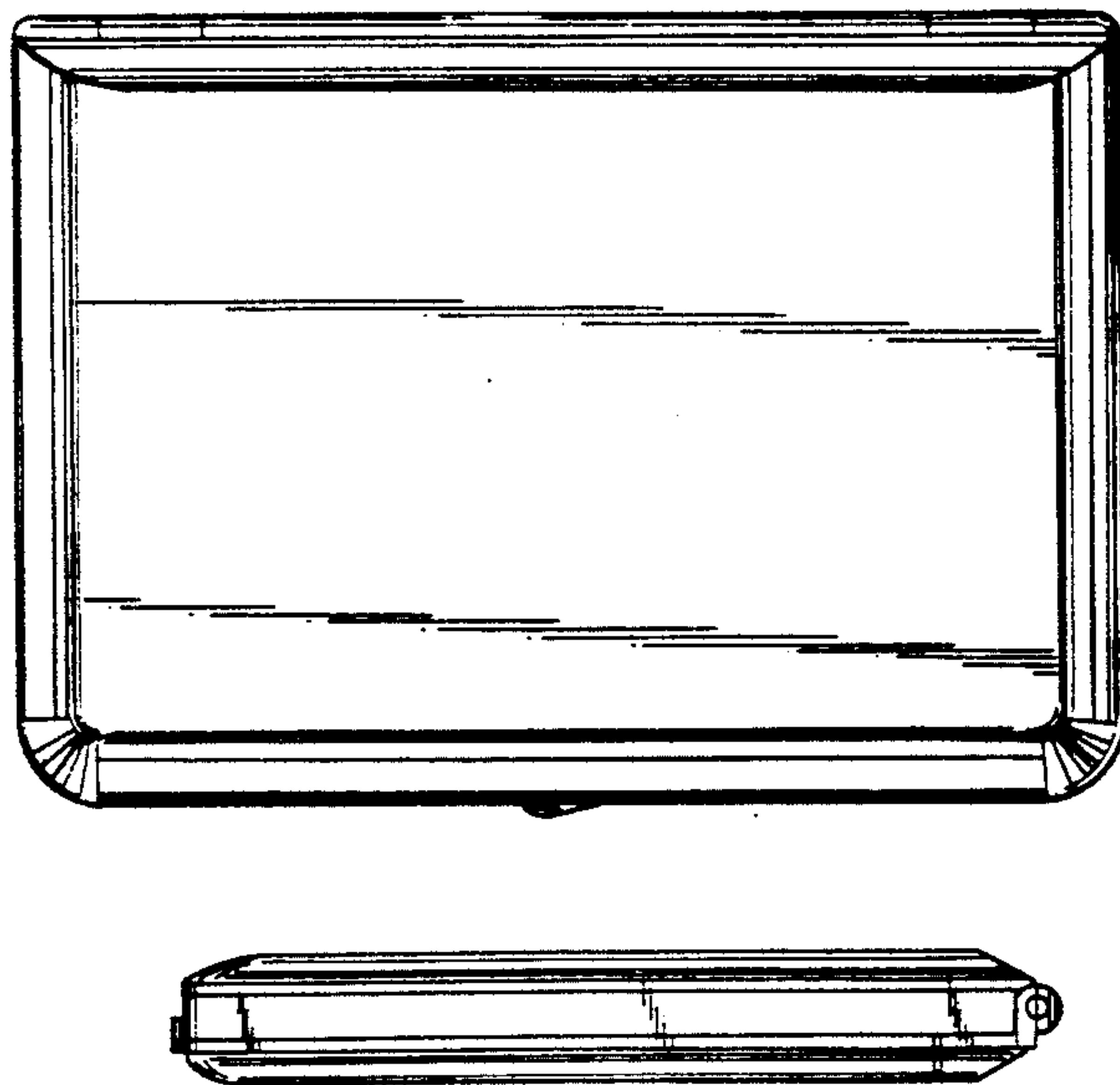


FIG. 1

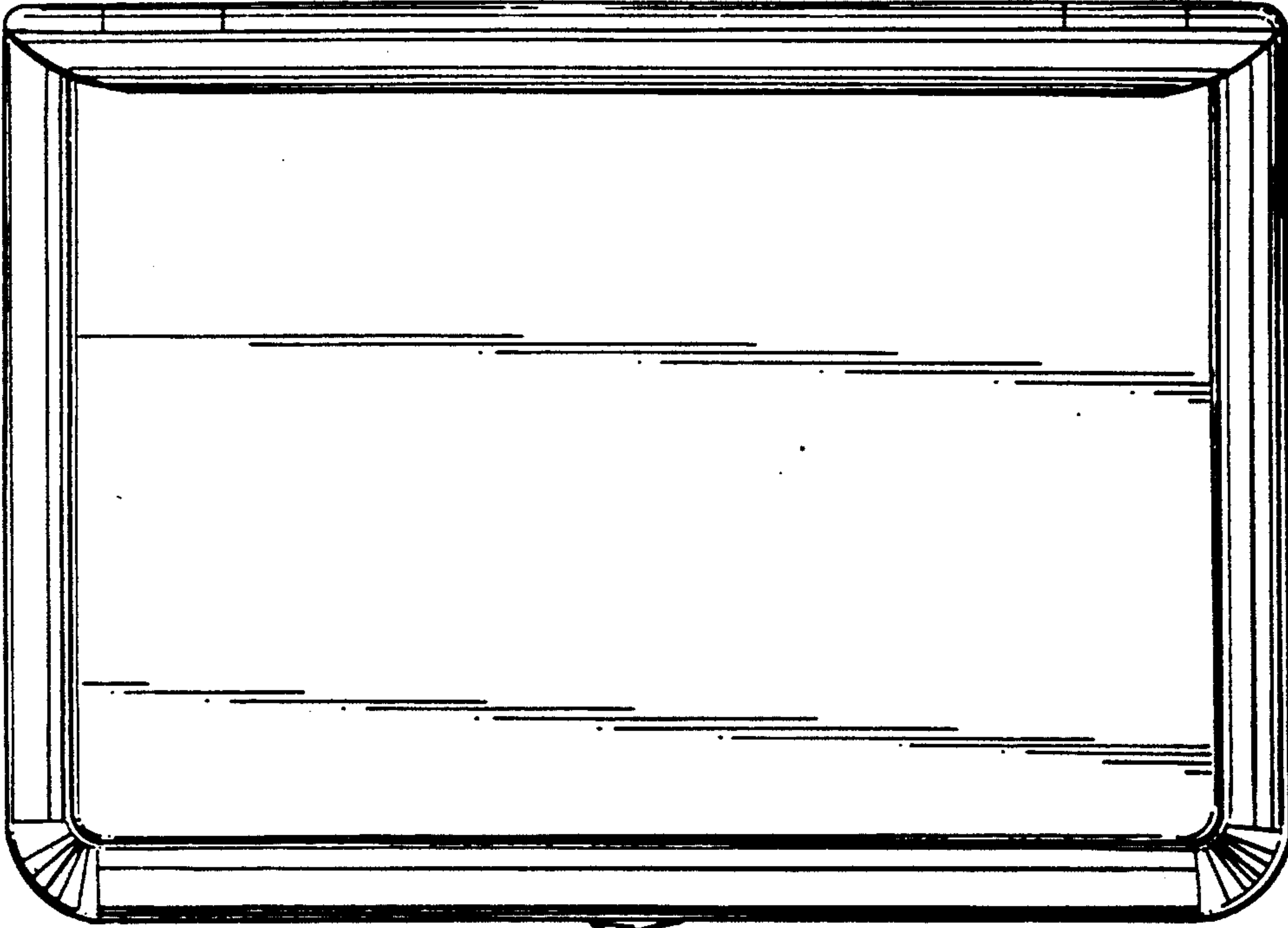


FIG. 2

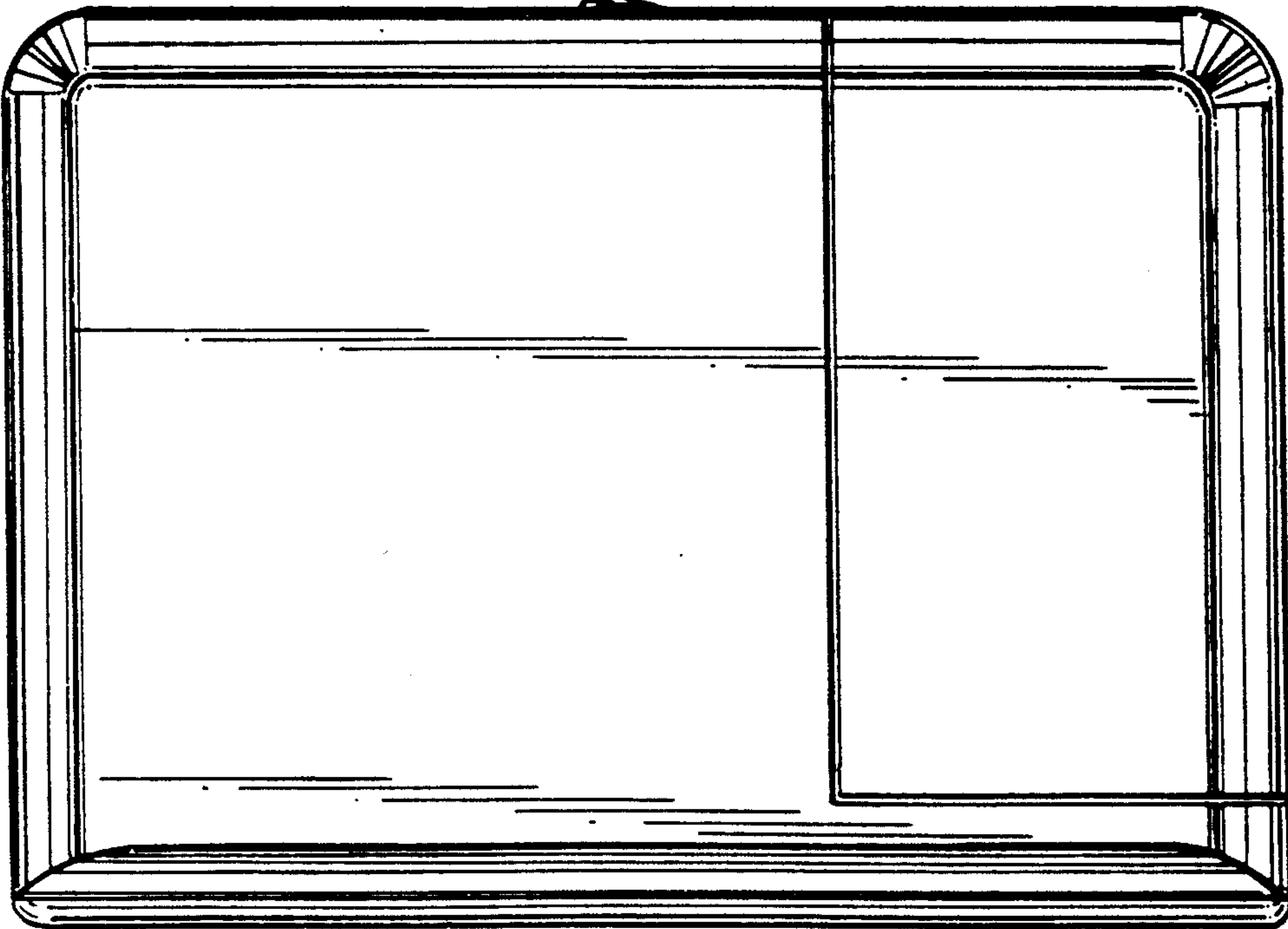


FIG. 3

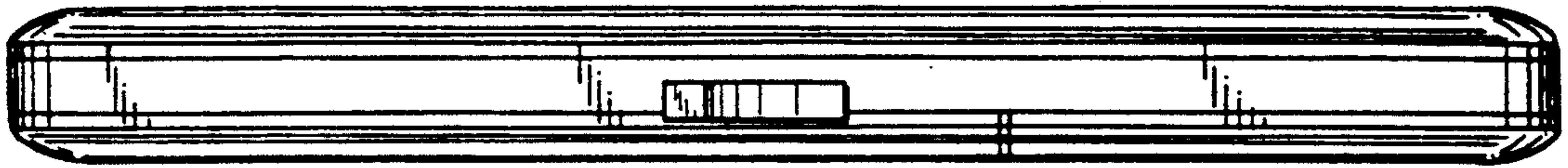


FIG. 4



FIG. 5

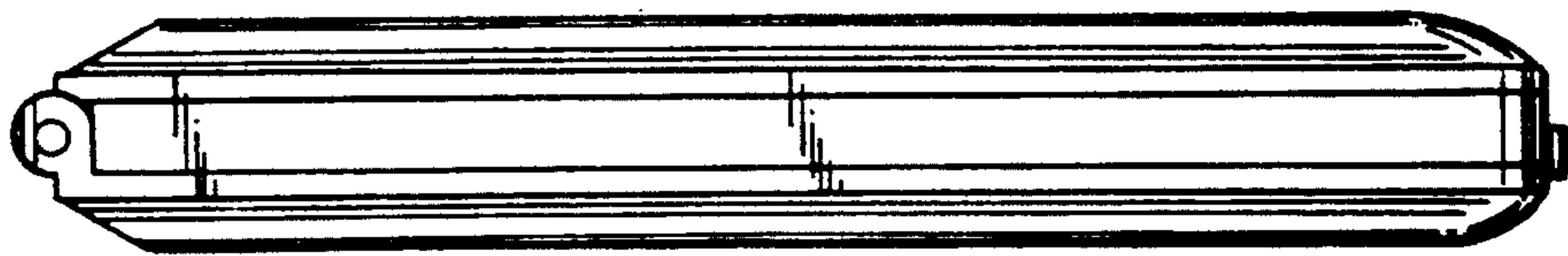
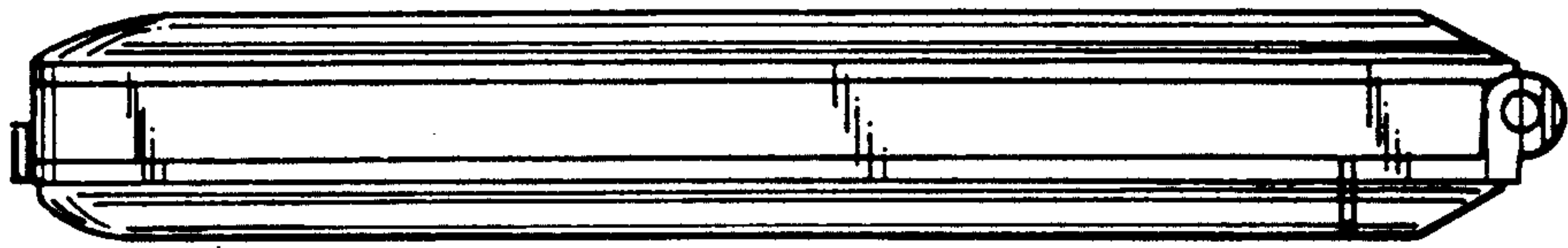


FIG. 6

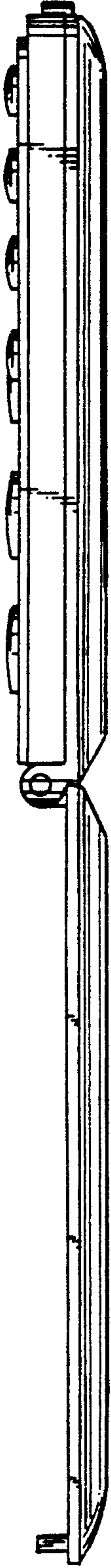


FIG. 7



FIG. 8

FIG. 9



FIG. 10

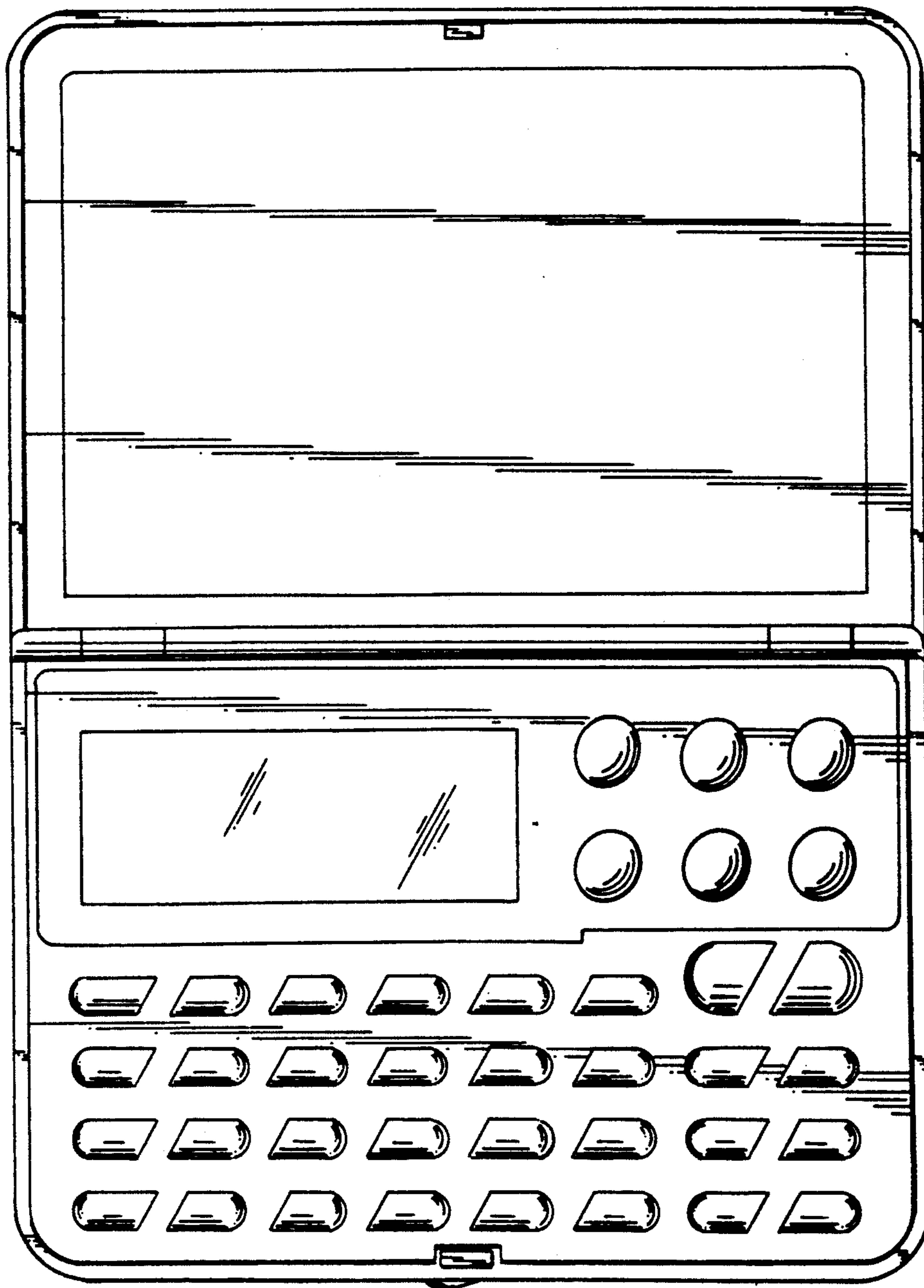


FIG. 11



FIG. 12

