



US00D340231S

# United States Patent [19]

[11] Patent Number: **Des. 340,231**

Jones et al.

[45] Date of Patent: **\*\* Oct. 12, 1993**

[54] **MAINFRAME MULTIPROCESSOR**

|            |        |                |       |         |
|------------|--------|----------------|-------|---------|
| D. 236,357 | 8/1975 | Clayton        | ..... | D14/102 |
| D. 267,490 | 1/1983 | LaDue et al.   | ..... | D14/102 |
| D. 309,130 | 7/1990 | Yoshida et al. | ..... | D14/102 |
| D. 334,564 | 4/1993 | Basara et al.  | ..... | D14/100 |

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[57] **CLAIM**

[\*\*] Term: **14 Years**

The ornamental design for a mainframe multiprocessor, as shown.

[21] Appl. No.: **792,056**

**DESCRIPTION**

[22] Filed: **Nov. 12, 1991**

[52] U.S. Cl. .... **D14/102**

**FIG. 1** is a perspective view of the front right side of the mainframe multiprocessor;  
**FIG. 2** is a front elevation view;  
**FIG. 3** is a rear elevation view;  
**FIG. 4** is a left side elevation view;  
**FIG. 5** is a right side elevation view; and,  
**FIG. 6** is a top elevation view.

[58] Field of Search ..... **D14/100, 102, 108, 109;**  
**D13/162, 184, 199; 360/99.12; 361/390-395**

[56] **References Cited**

**U.S. PATENT DOCUMENTS**

D. 231,935 6/1974 Davis et al. .... **D14/102**

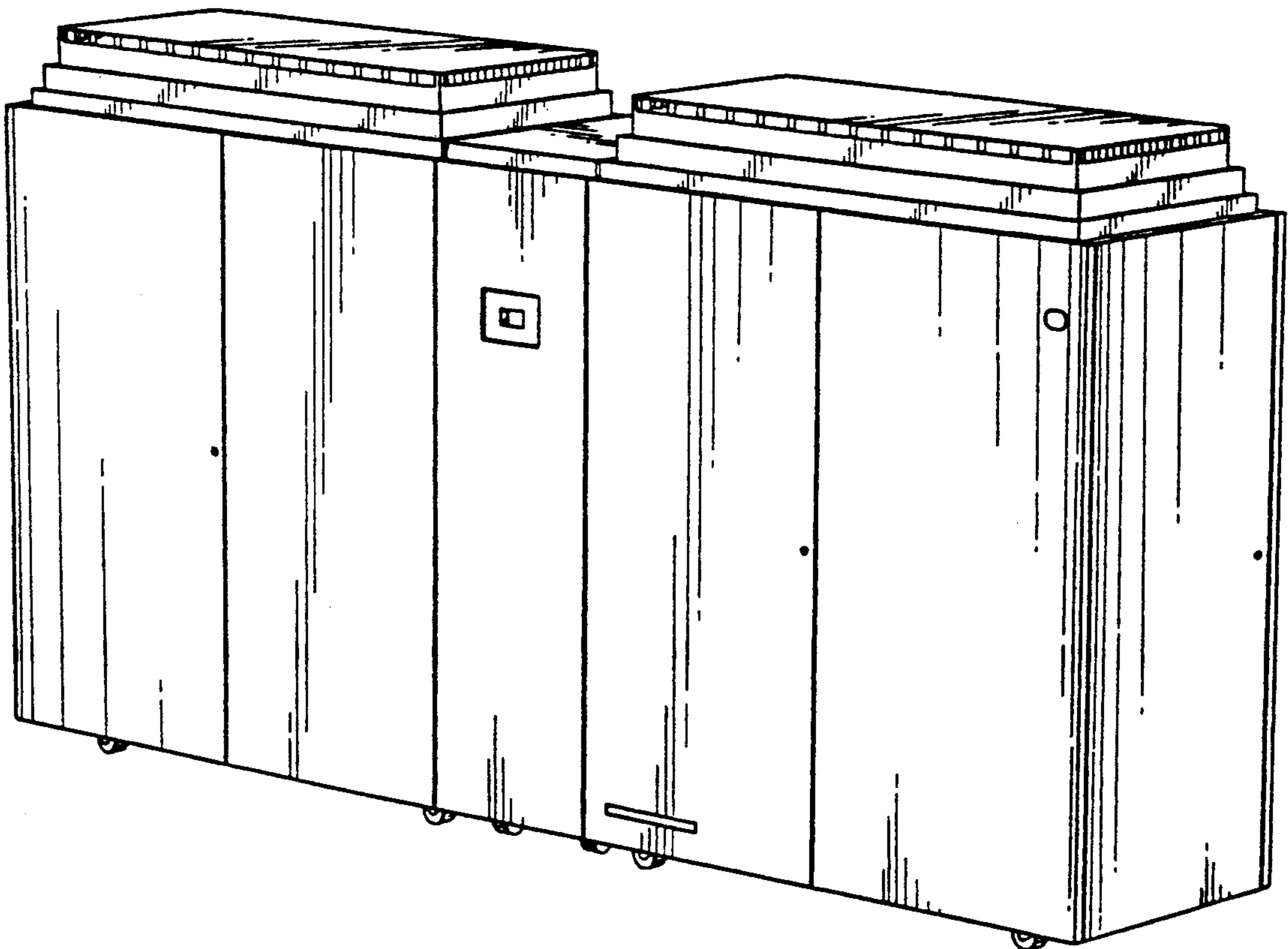


FIG. 1

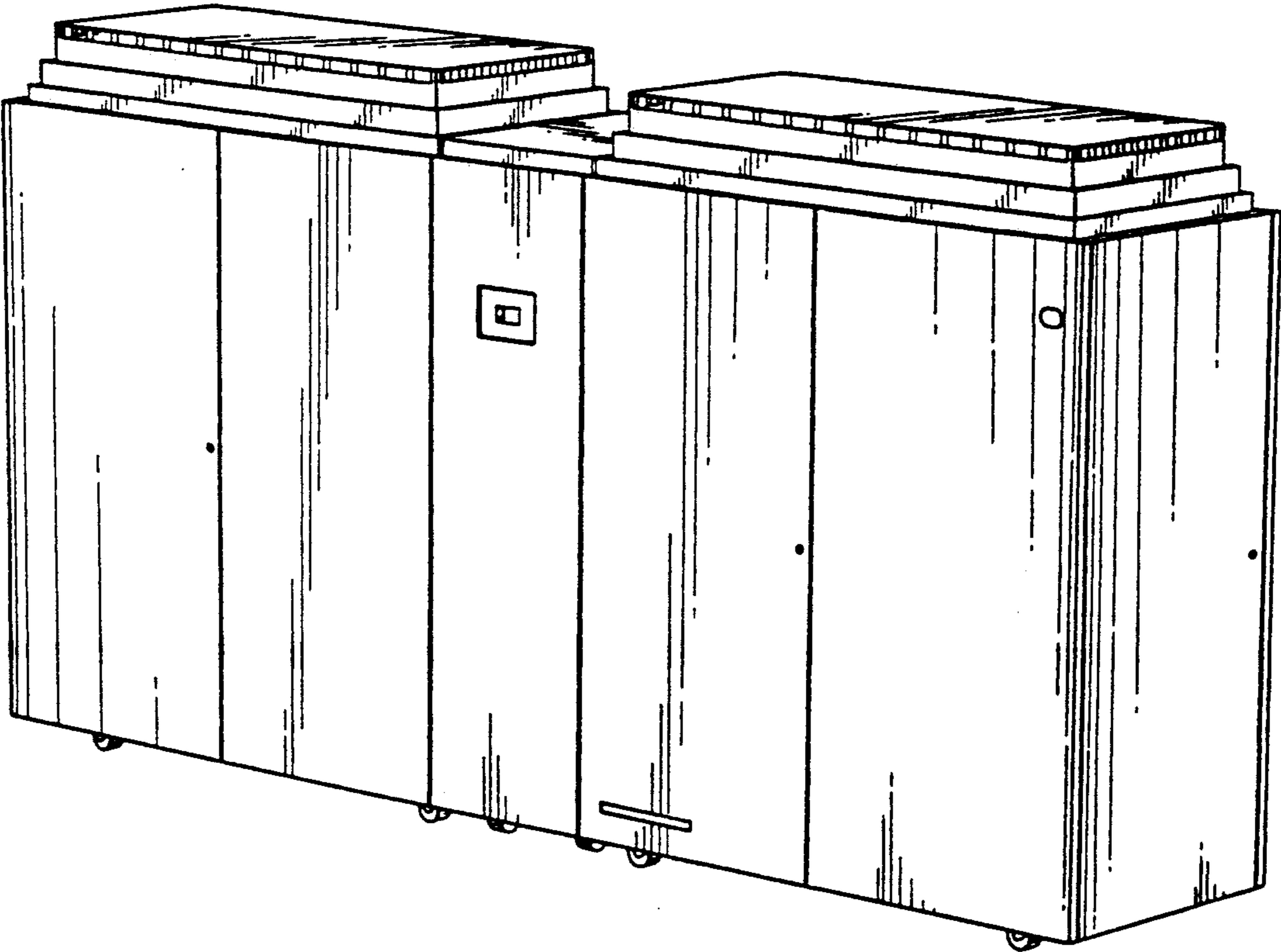


FIG. 2

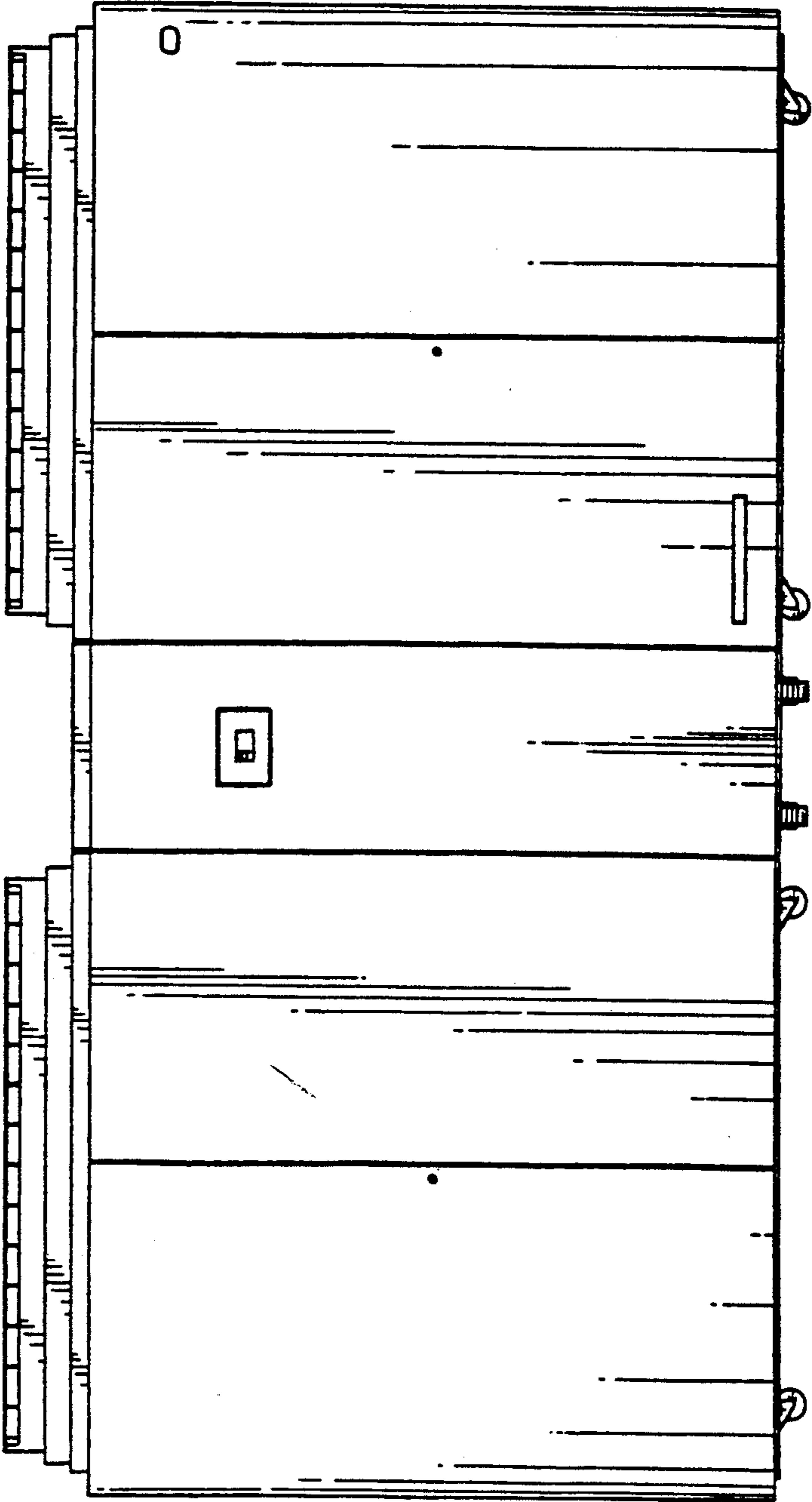


FIG. 3

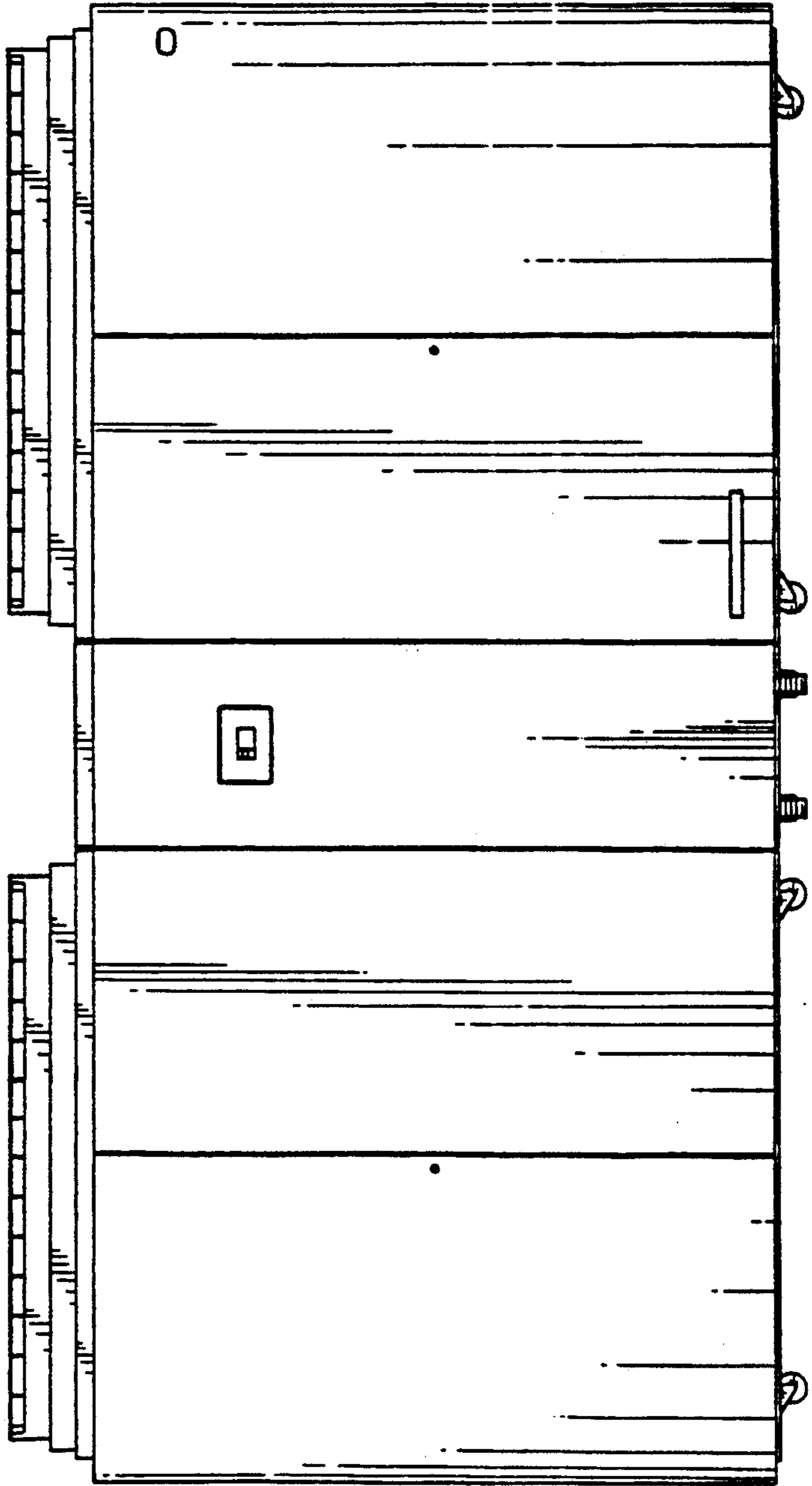


FIG. 4

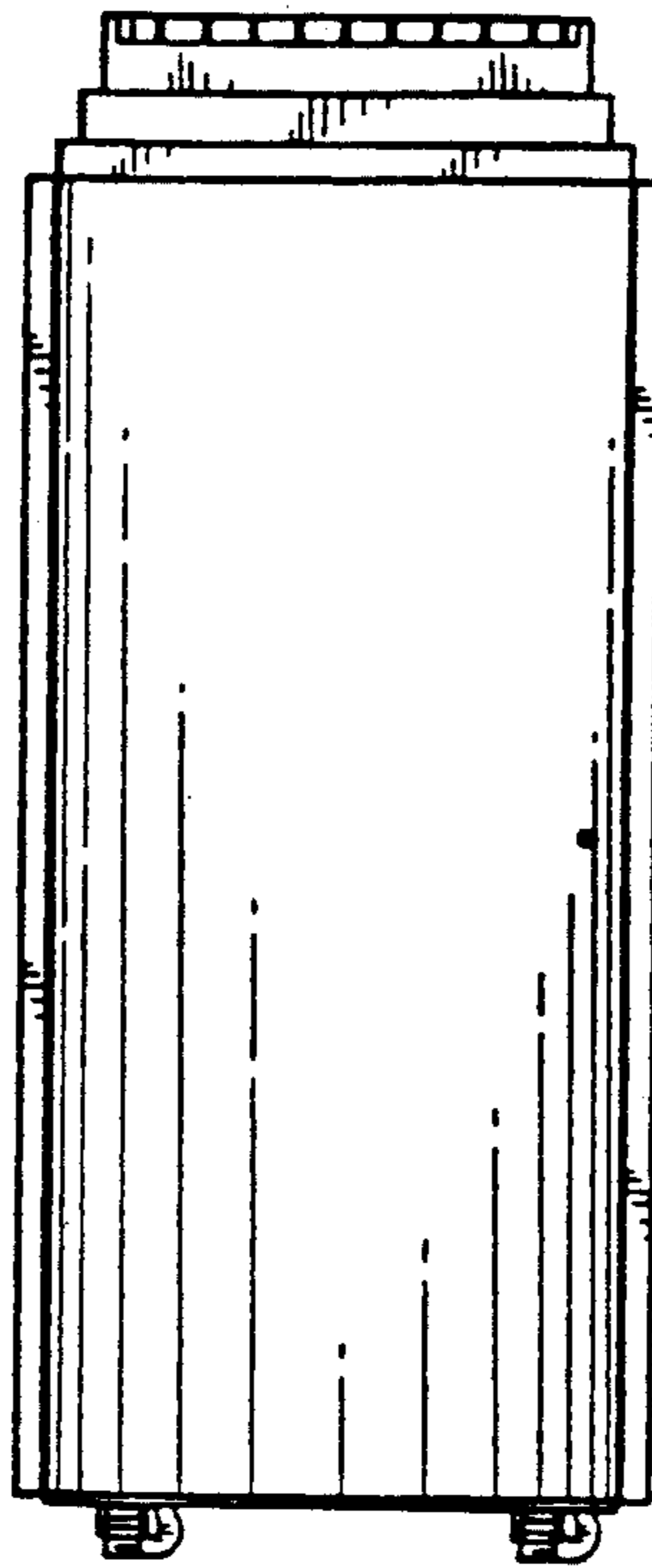


FIG. 5

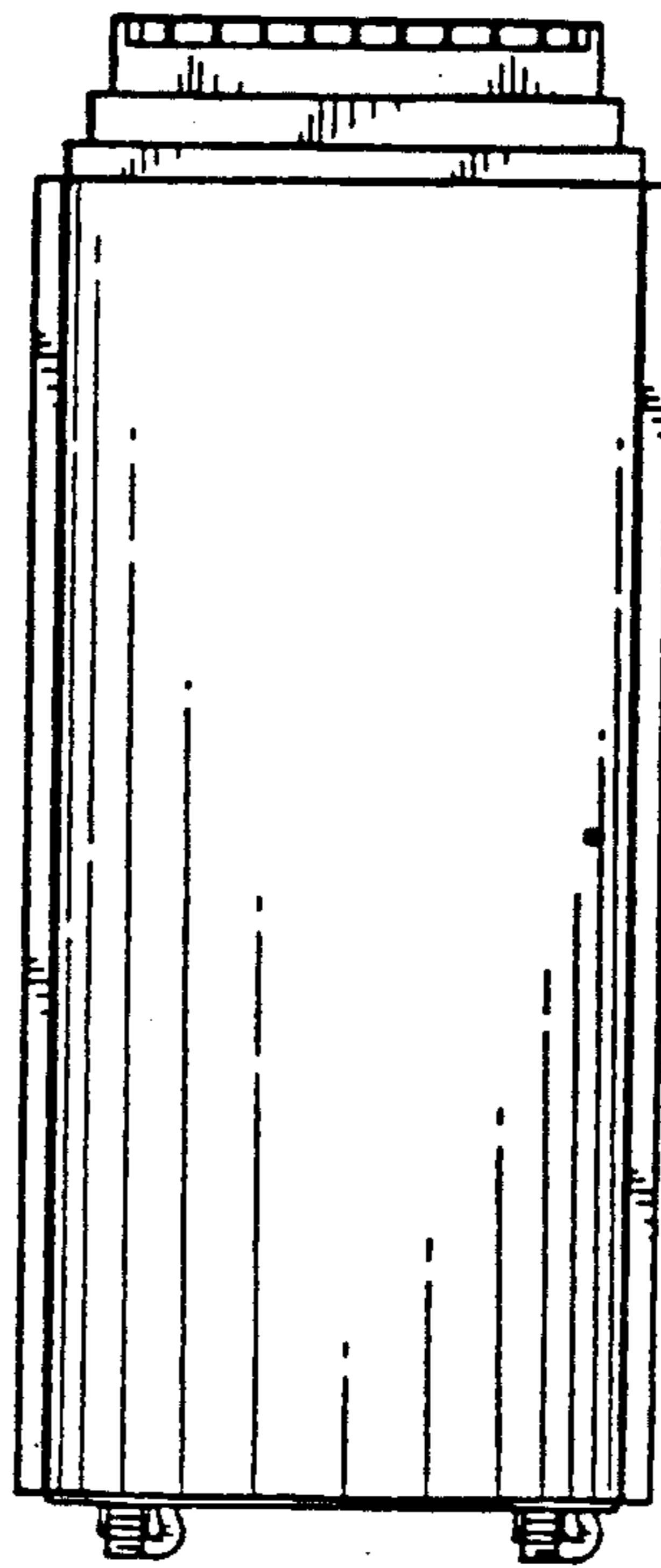


FIG. 6

