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United States Patent [19]

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Feng

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[54] **INTEGRATED CIRCUIT EXTRACTION TOOL**

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[**] Term: **14 Years**

[21] Appl. No.: **827,960**

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[52] U.S. Cl. **D8/52; D28/55**

[58] Field of Search **D8/14, 51, 52; D28/55; D24/143; 29/758; 81/3.8; 294/99.2**

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Assistant Examiner—Philip Hyder
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[57] CLAIM

The ornamental design for a integrated circuit extraction tool, as shown and described.

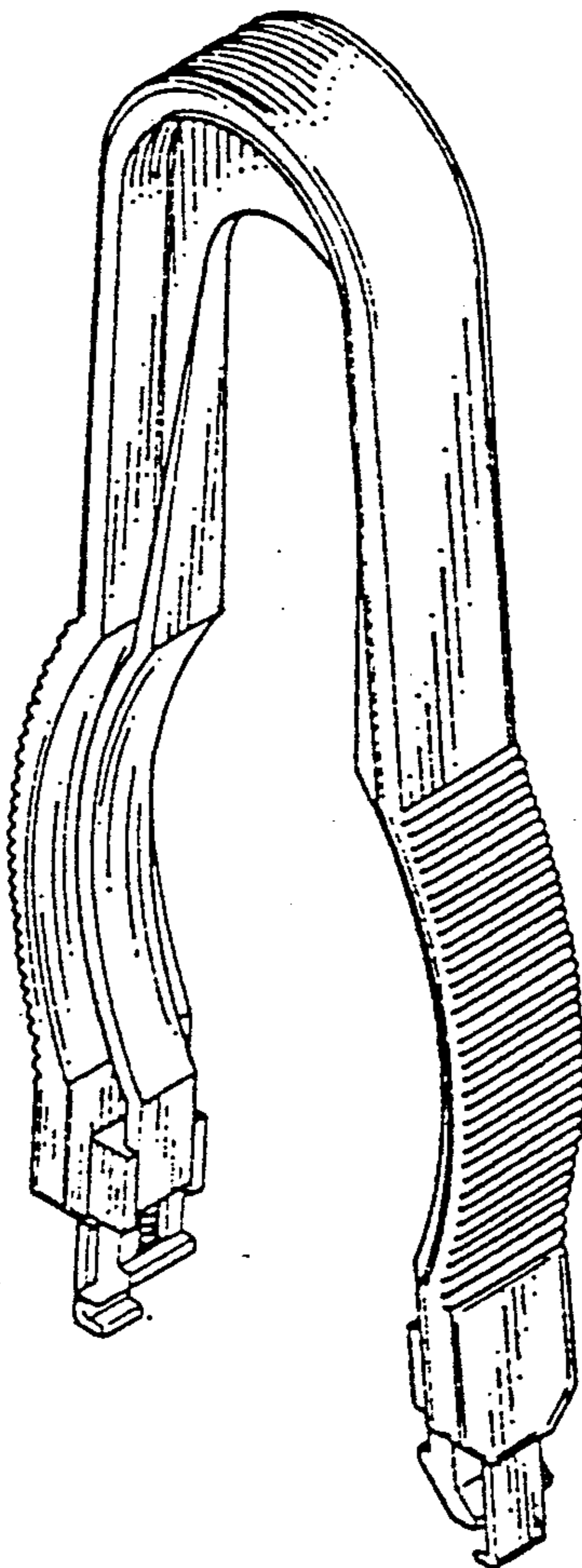
DESCRIPTION

FIG. 1 is a perspective view of a integrated circuit extraction tool showing my new design;
 FIG. 2 is a front elevational view thereof;
 FIG. 3 is a rear elevational view thereof;
 FIG. 4 is a left side elevational view thereof;
 FIG. 5 is a right side elevational view thereof;
 FIG. 6 is a top plan view thereof; and,
 FIG. 7 is a bottom plan view thereof.

[56] **References Cited**

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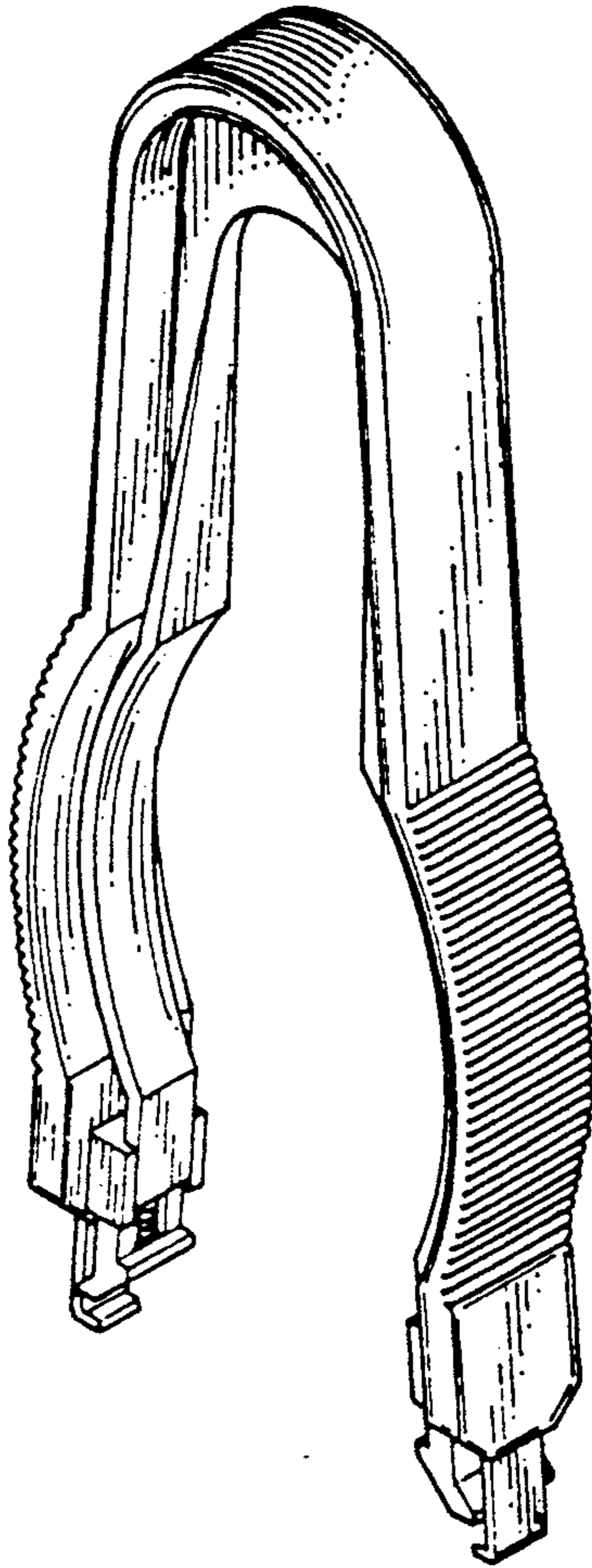


FIG. 1

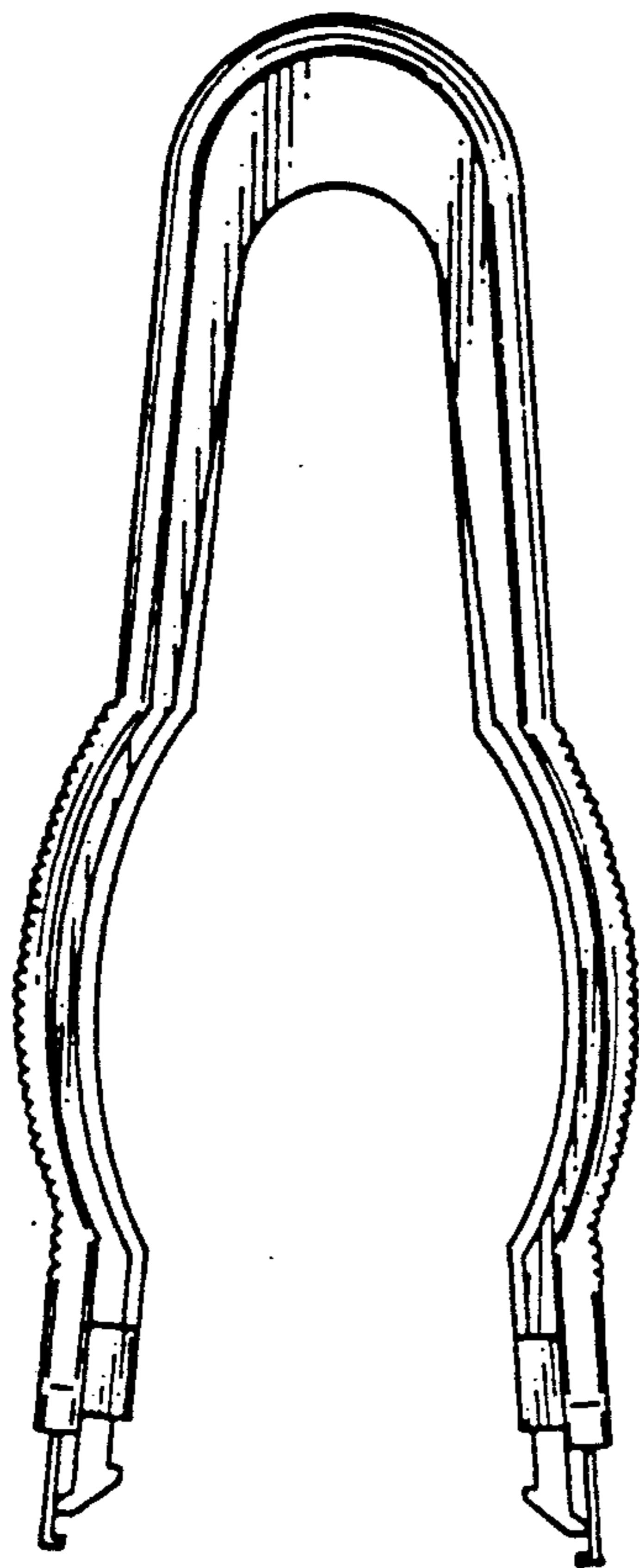


FIG. 2

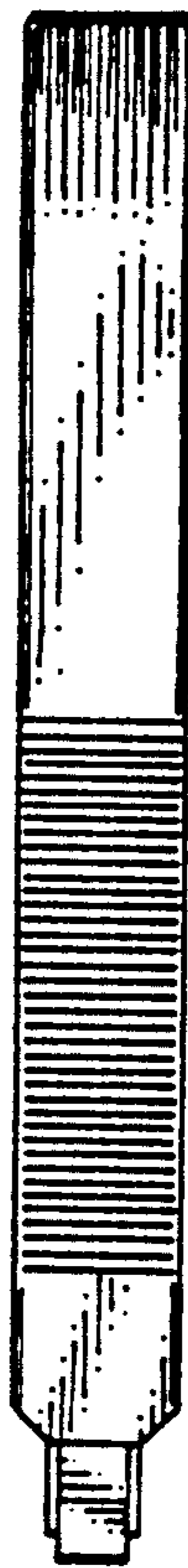


FIG. 5

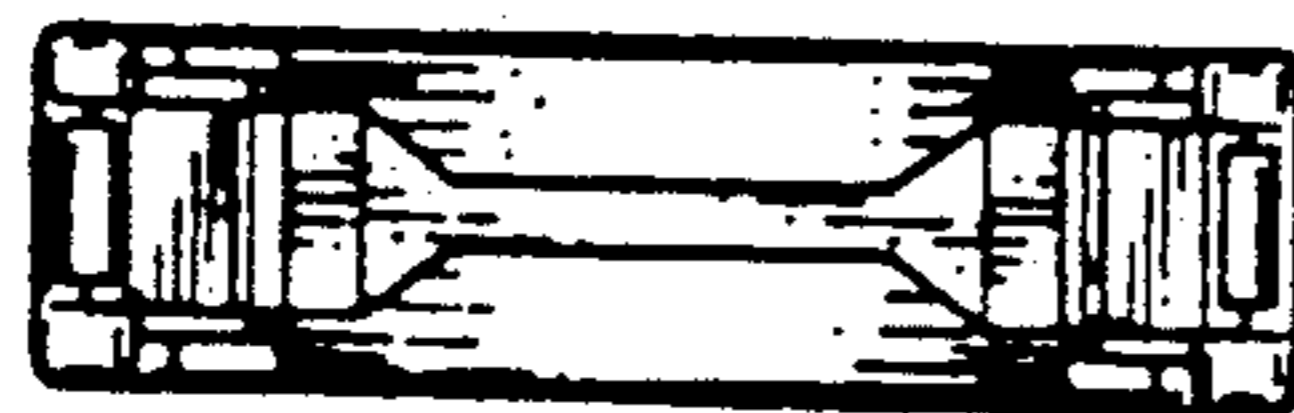


FIG. 7

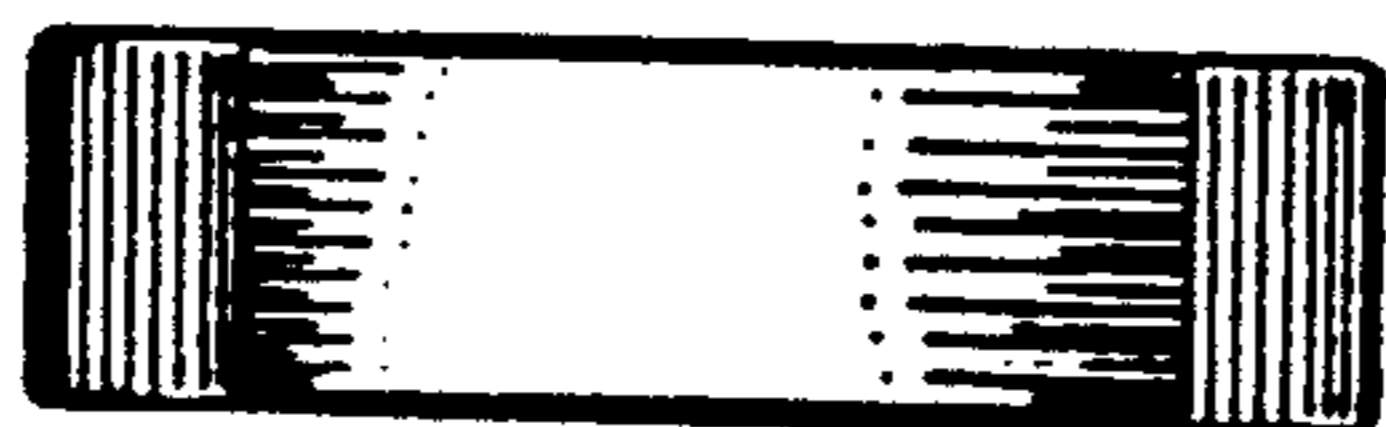


FIG. 6

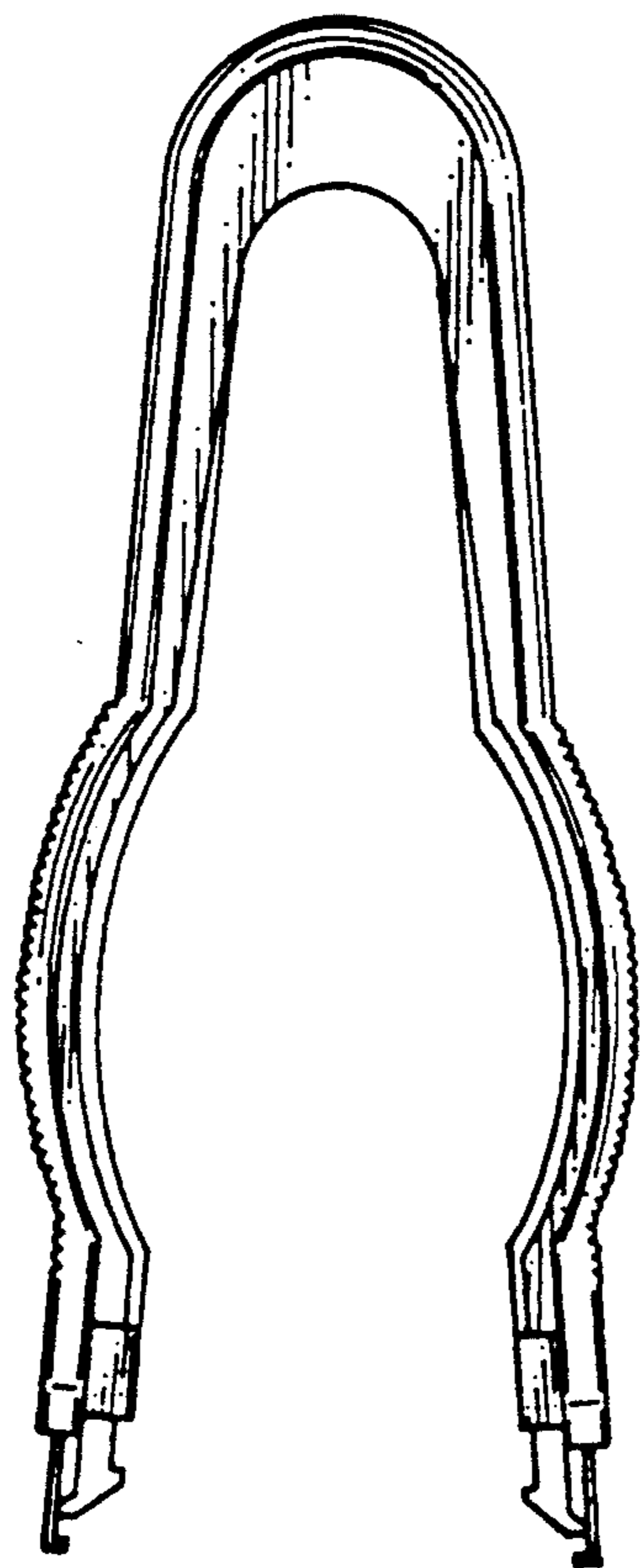


FIG. 3



FIG. 4