



US00D340173S

# United States Patent [19]

[11] Patent Number: **Des. 340,173**

Feng

[45] Date of Patent: **\*\* Oct. 12, 1993**

[54] **INTEGRATED CIRCUIT EXTRACTION TOOL**

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4,666,199	5/1987	Cheh .....	294/99.2
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4,964,663	10/1990	Jermyn .....	294/99.2

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*Attorney, Agent, or Firm*—Bacon & Thomas

[\*\*] Term: **14 Years**

[21] Appl. No.: **827,960**

[57] **CLAIM**

[22] Filed: **Jan. 29, 1992**

The ornamental design for a integrated circuit extraction tool, as shown and described.

[52] U.S. Cl. .... **D8/52; D28/55**

[58] Field of Search ..... **D8/14, 51, 52; D28/55; D24/143; 29/758; 81/3.8; 294/99.2**

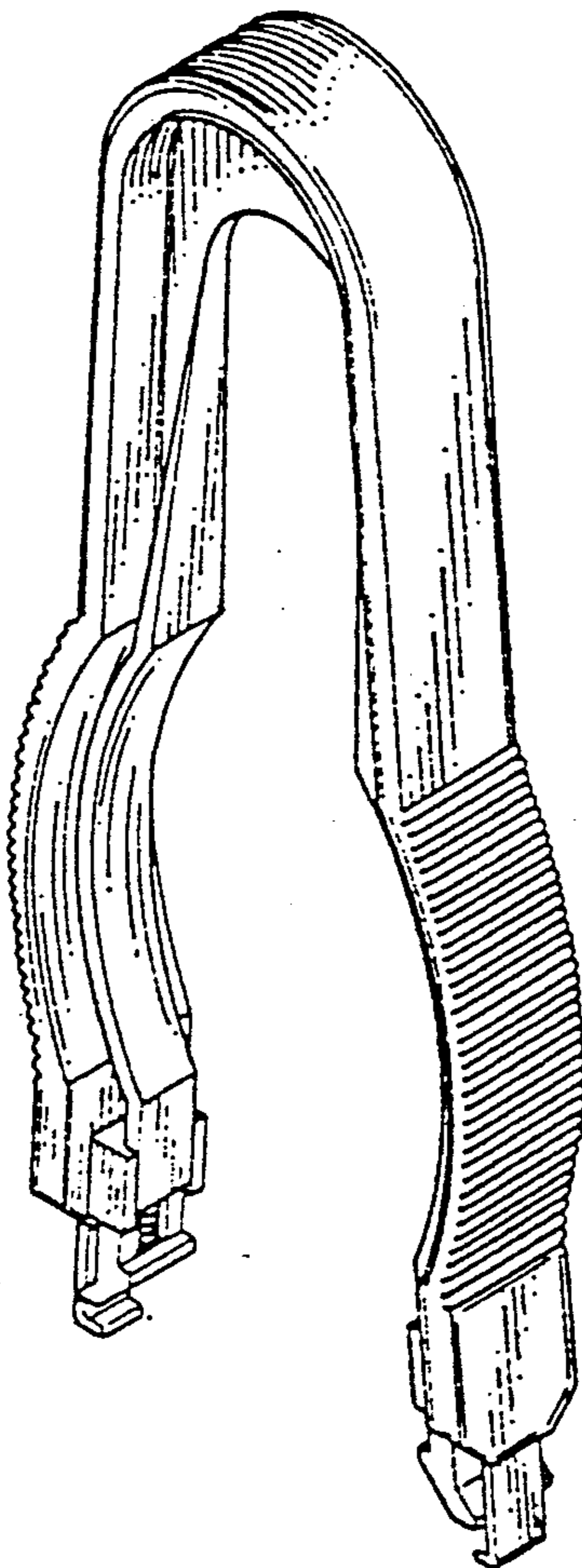
**DESCRIPTION**

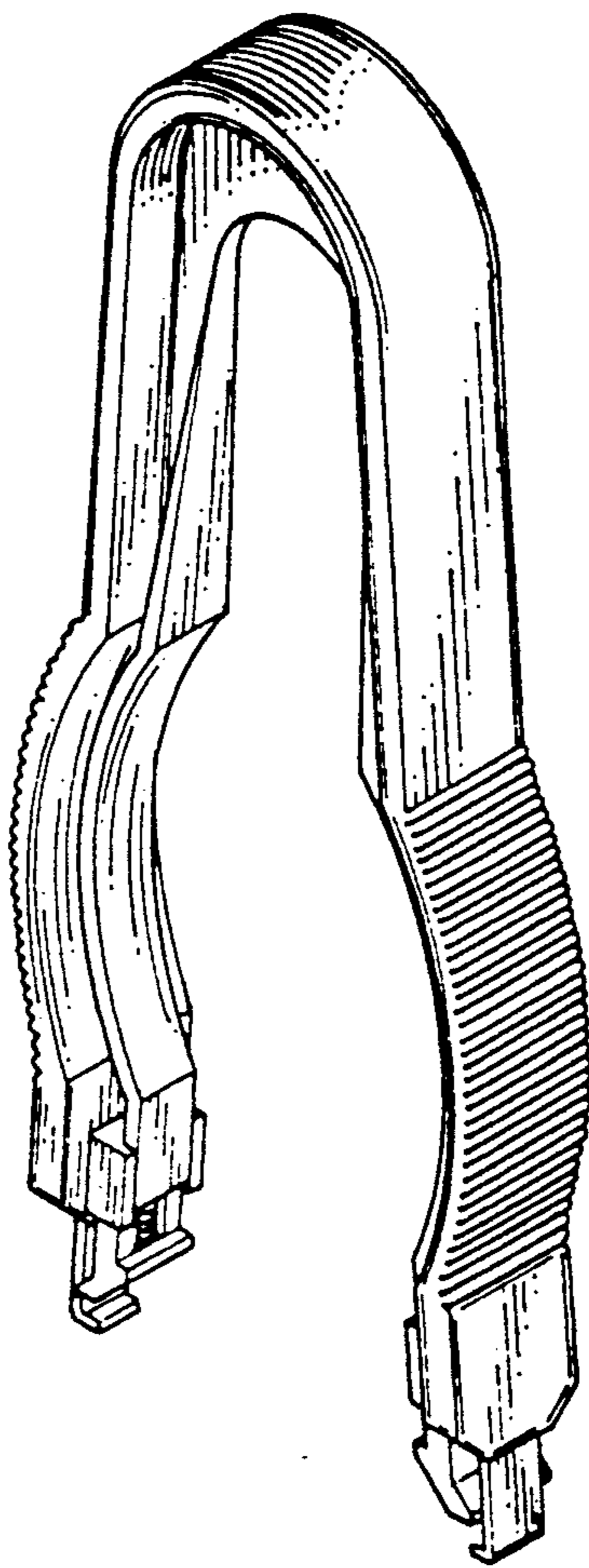
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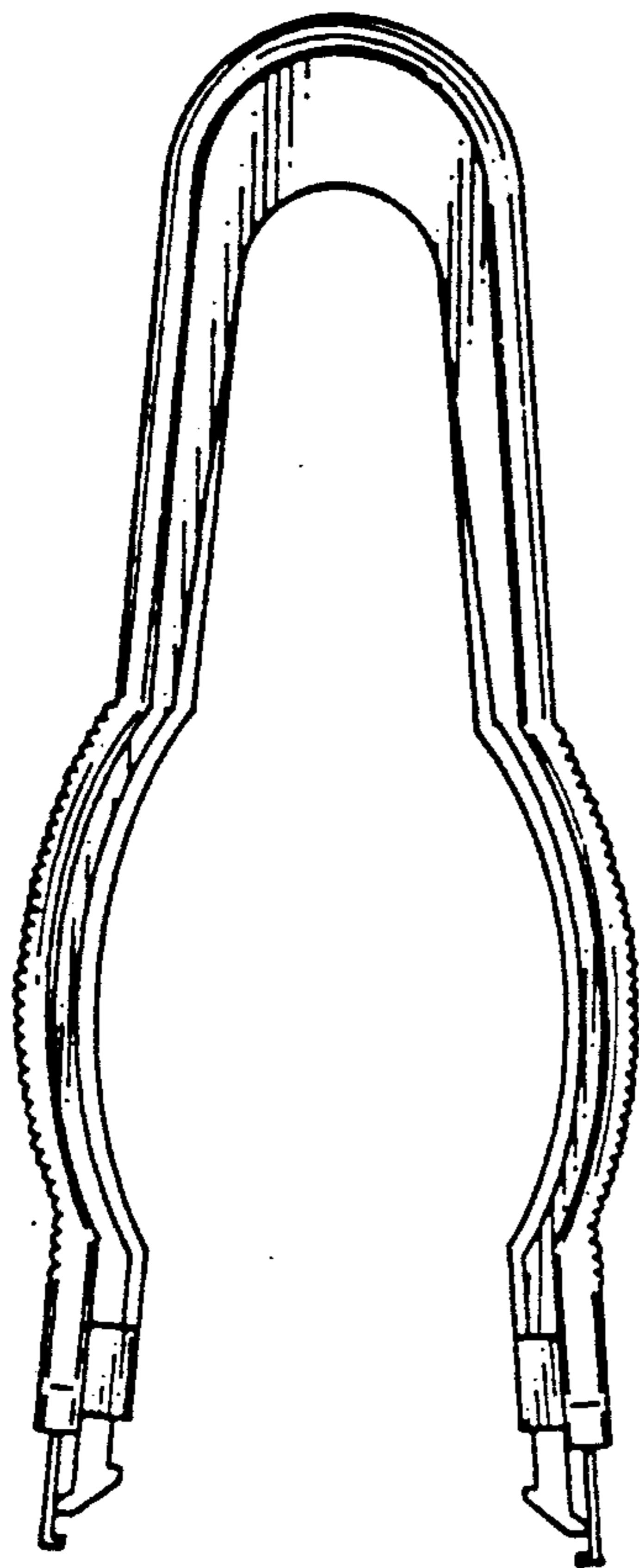
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D. 331,527	12/1992	Feng .....	D8/52
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FIG. 1 is a perspective view of a integrated circuit extraction tool showing my new design; FIG. 2 is a front elevational view thereof; FIG. 3 is a rear elevational view thereof; FIG. 4 is a left side elevational view thereof; FIG. 5 is a right side elevational view thereof; FIG. 6 is a top plan view thereof; and, FIG. 7 is a bottom plan view thereof.





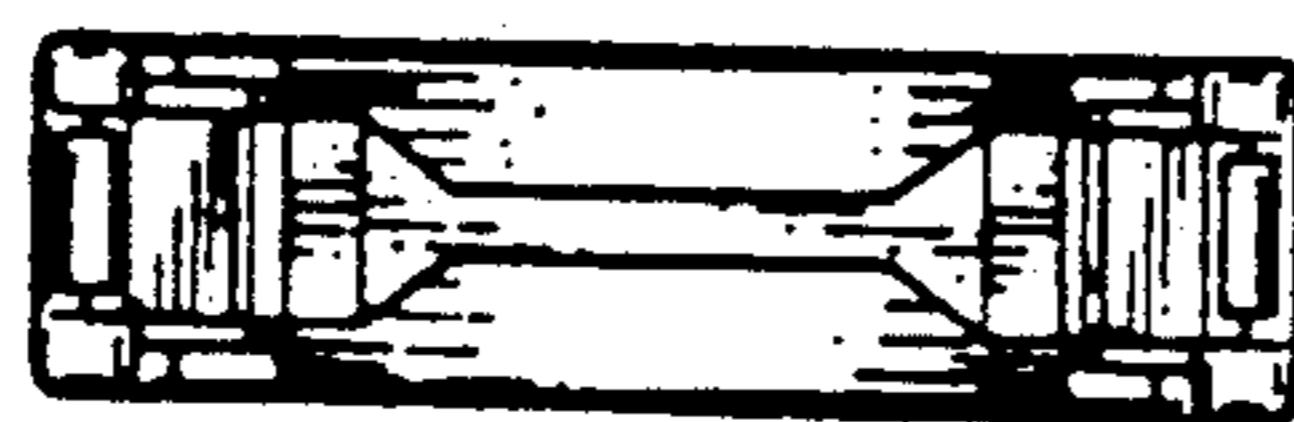
*FIG. 1*



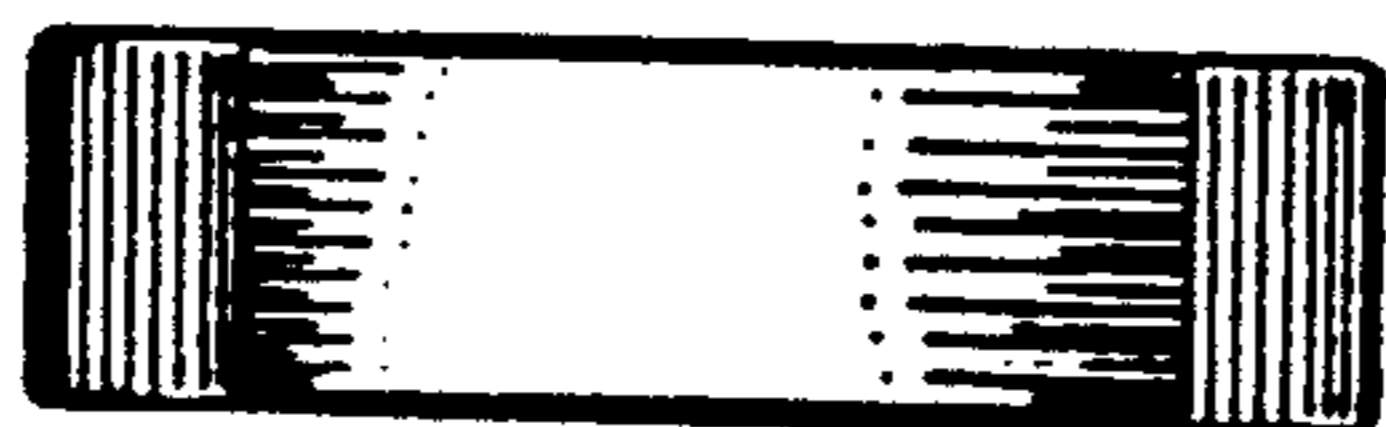
*FIG. 2*



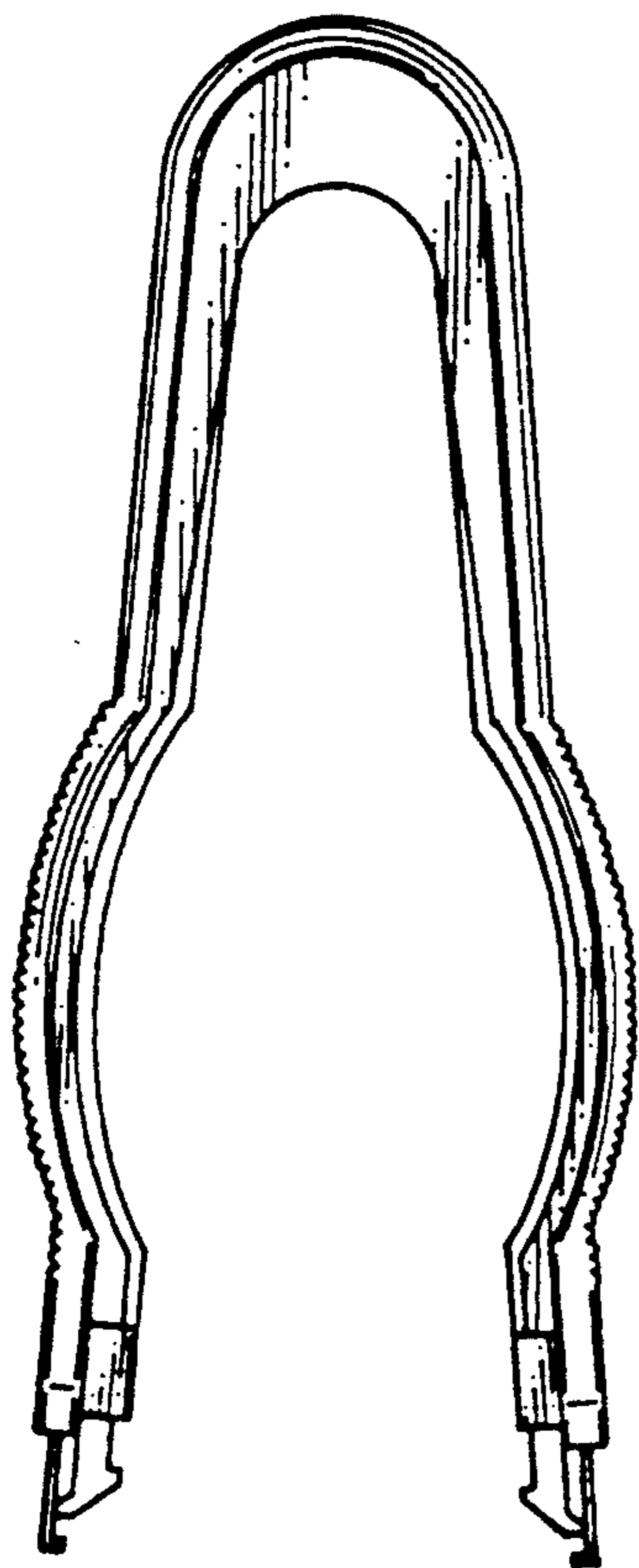
*FIG. 5*



*FIG. 7*



**FIG. 6**



**FIG. 3**



**FIG. 4**