



US00D339116S

United States Patent [19]

[11] Patent Number: **Des. 339,116**

Gates et al.

[45] Date of Patent: **** Sep. 7, 1993**

[54] **PARALLEL PORT MULTIPLEXOR FOR USE WITH A COMPUTER**

[75] Inventors: **Dirk I. Gates, Woodland Hills; James K. Matthews, Calabasas; Monte A. Pooley, Newbury Park, all of Calif.**

[73] Assignee: **Xircom, Inc., Calabasas, Calif.**

[**] Term: **14 Years**

[21] Appl. No.: **675,714**

[22] Filed: **Mar. 27, 1991**

[52] U.S. Cl. **D14/107**

[58] Field of Search **D14/100, 107, 114; D13/147, 184; 361/380-385, 390-395; 439/77, 610, 620**

[56] **References Cited**

U.S. PATENT DOCUMENTS

D. 304,331	10/1989	Pogue et al.	D14/107
D. 313,013	12/1990	Krietzman	D14/100
D. 326,447	5/1992	Hofland et al.	D14/107
D. 327,260	6/1992	Davis et al.	D14/107
D. 327,470	6/1992	Davis et al.	D13/147

D. 327,879	7/1992	Lou	D14/107
D. 327,880	7/1992	Lou	D14/107
D. 328,595	8/1992	Lou	D14/107
D. 329,426	9/1992	Hutton	D14/107 X
D. 331,395	12/1992	Lou	D14/107

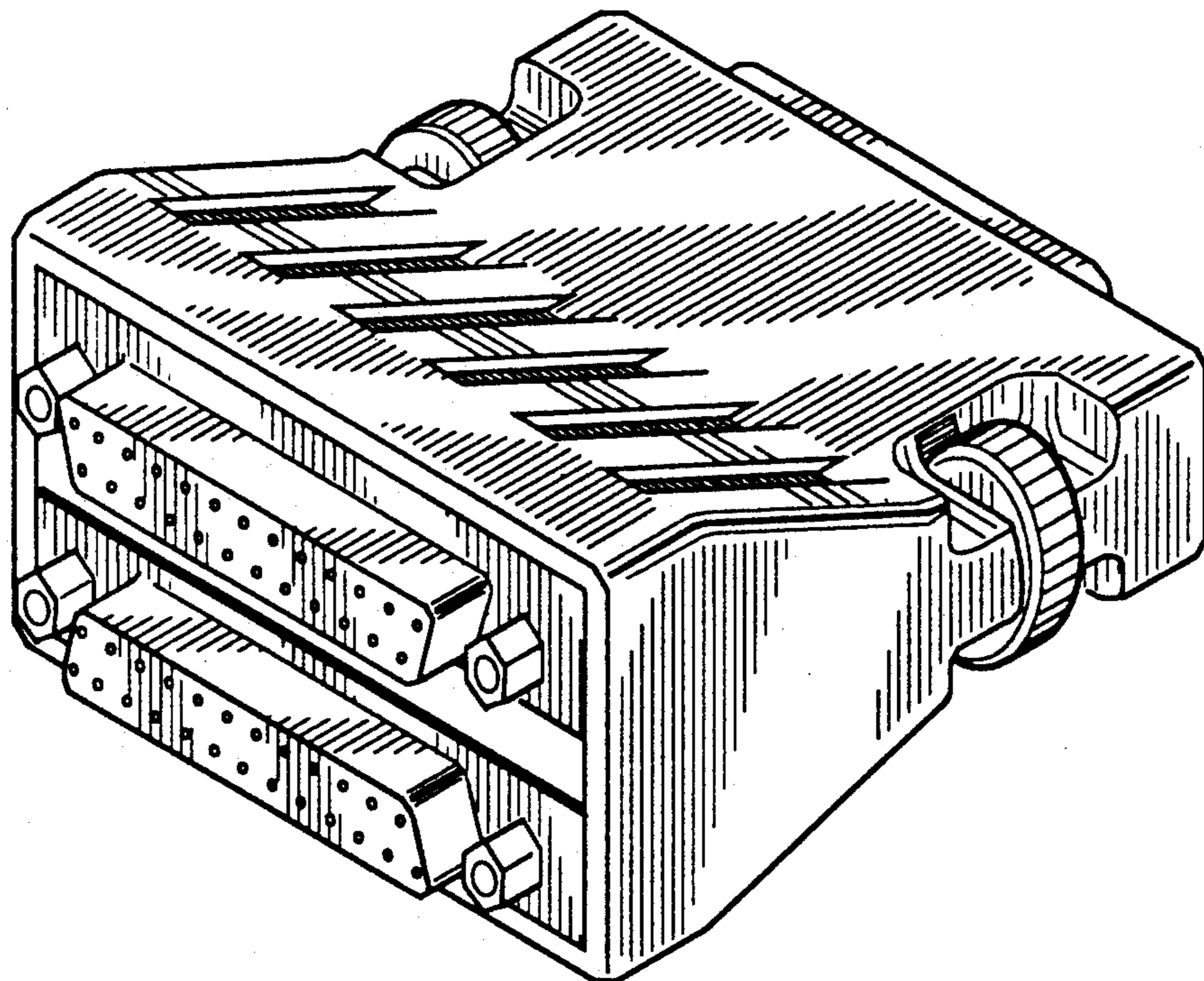
Primary Examiner—Wallace R. Burke
Assistant Examiner—Freda S. Nunn
Attorney, Agent, or Firm—Freilich, Hornbaker & Rosen

[57] **CLAIM**

The ornamental design for parallel port multiplexor for use with a computer, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a parallel port multiplexor for use with a computer showing our new design;
FIG. 2 is a front elevational view thereof on a reduced scale;
FIG. 3 is a top plan view thereof;
FIG. 4 is a rear elevational view thereof;
FIG. 5 is a side elevational view thereof opposite that shown in FIG. 1; and,
FIG. 6 is a bottom plan view thereof.



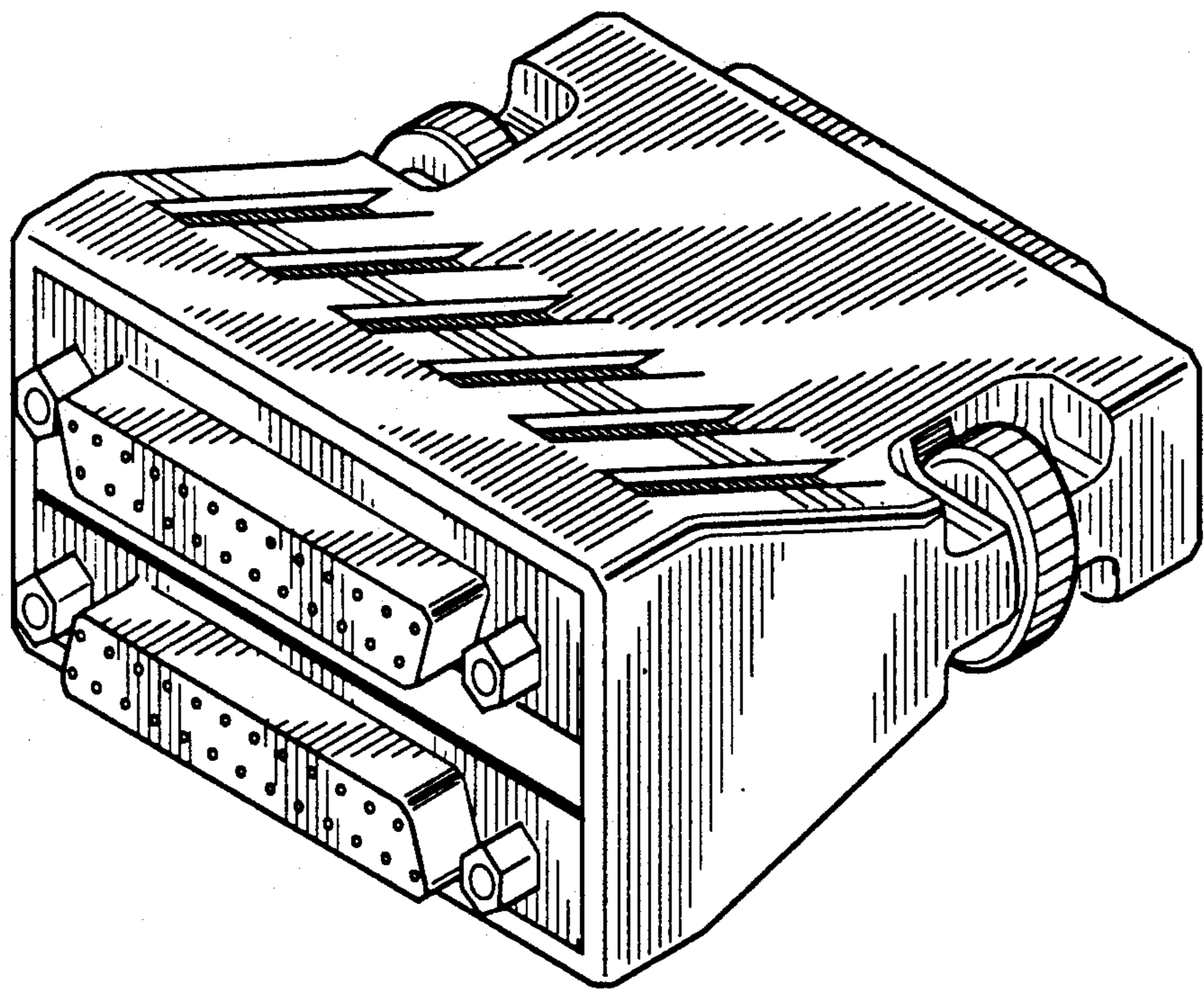


FIG. 1

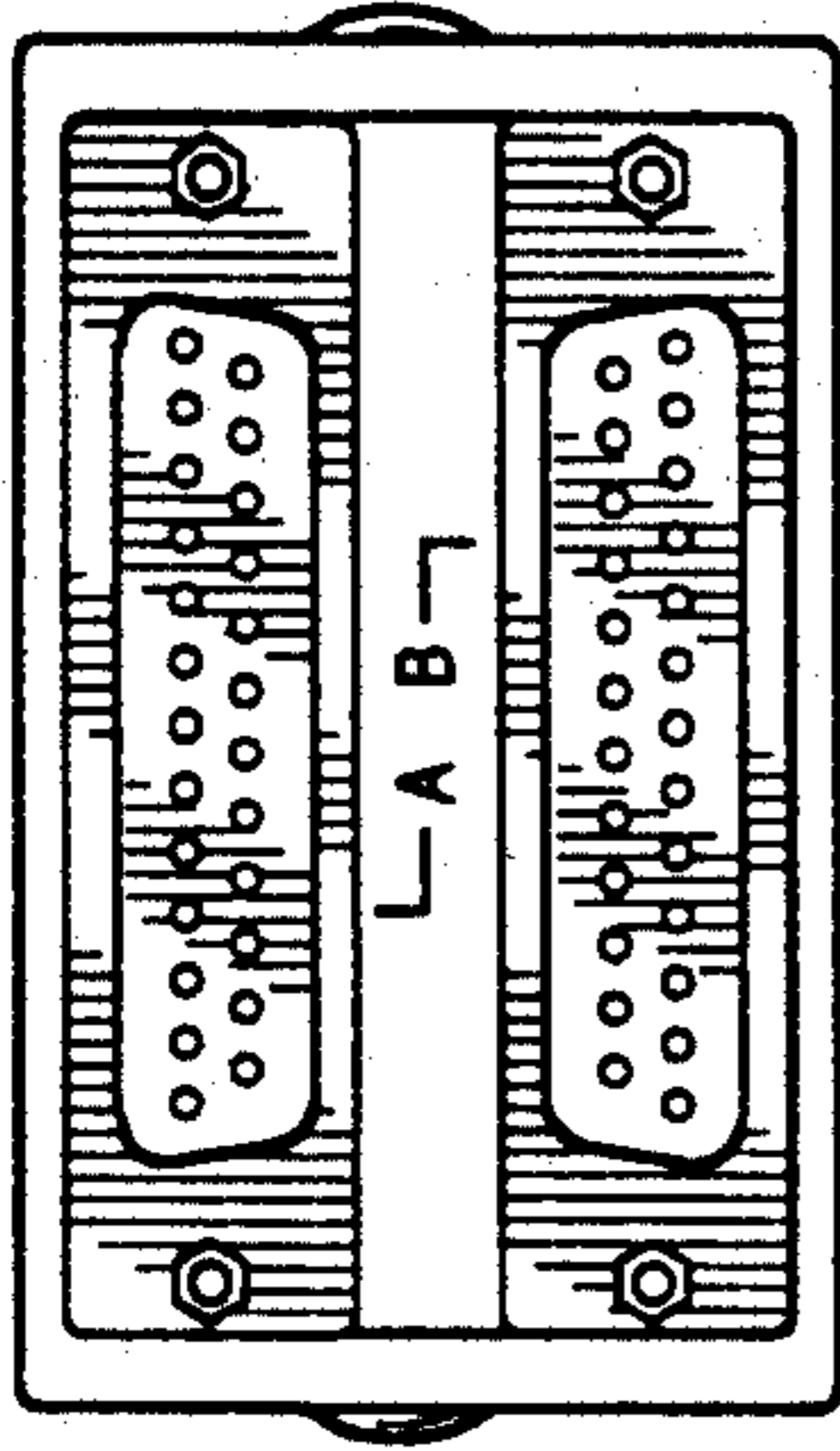


FIG. 2

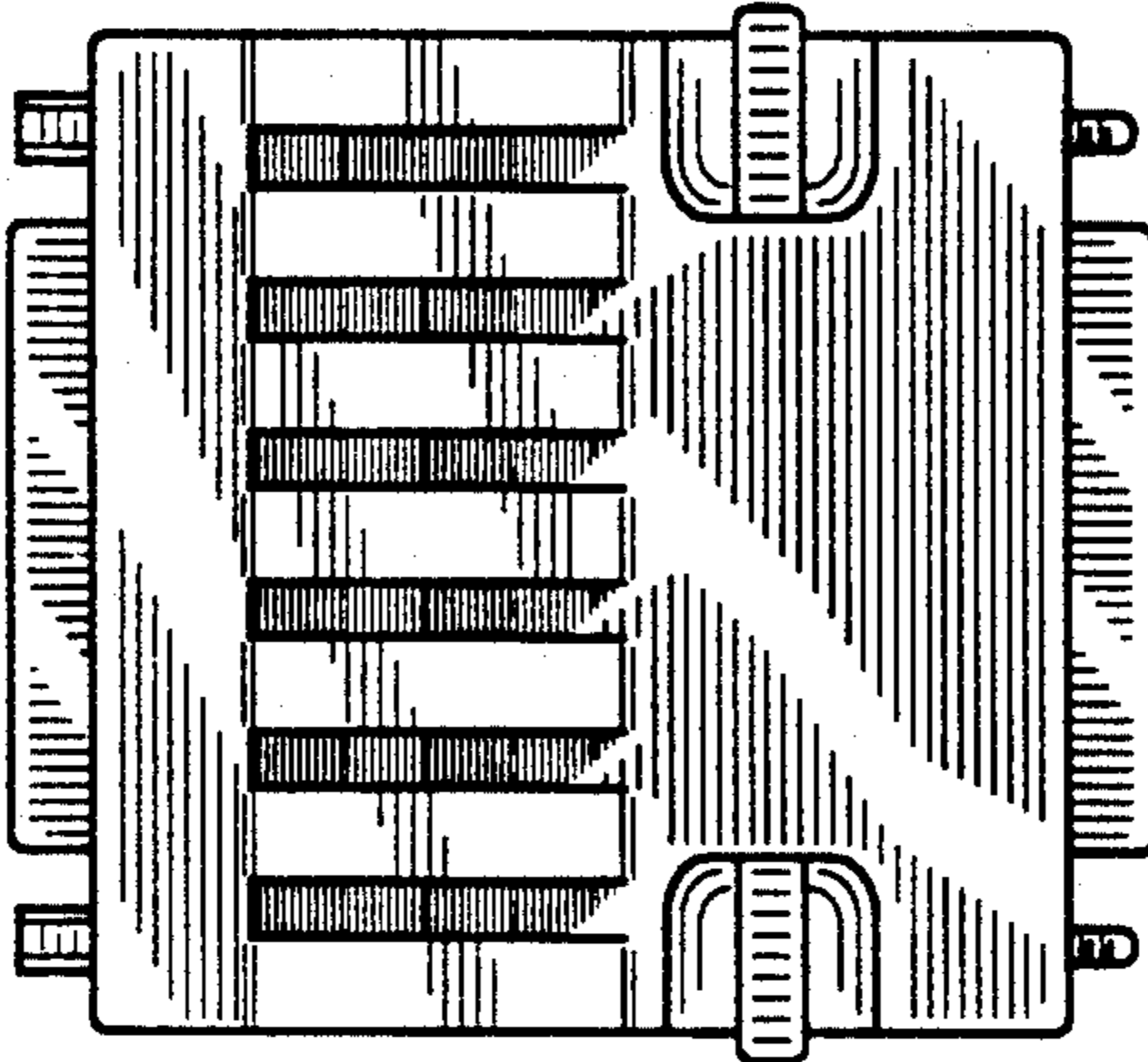


FIG. 3

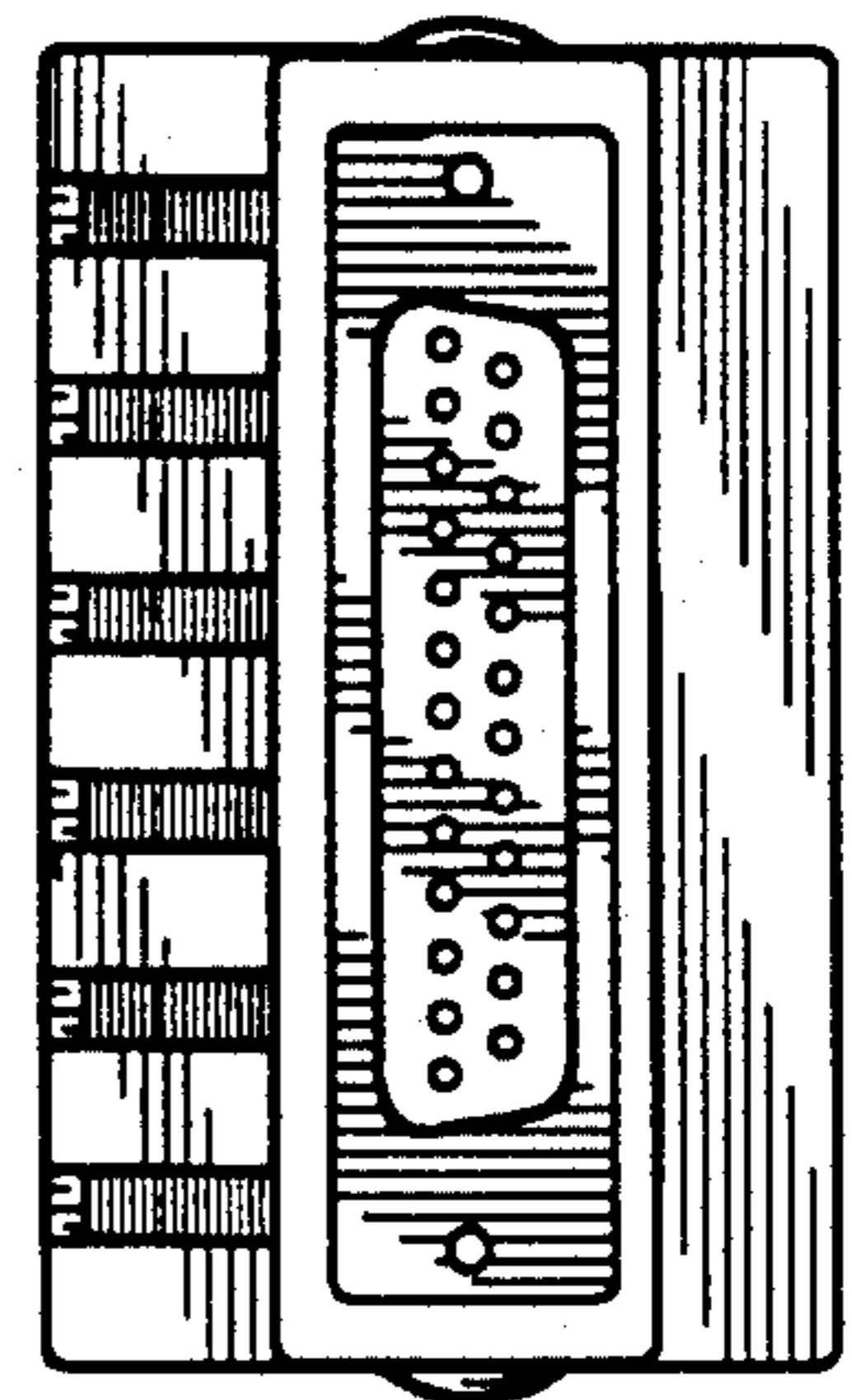


FIG. 4

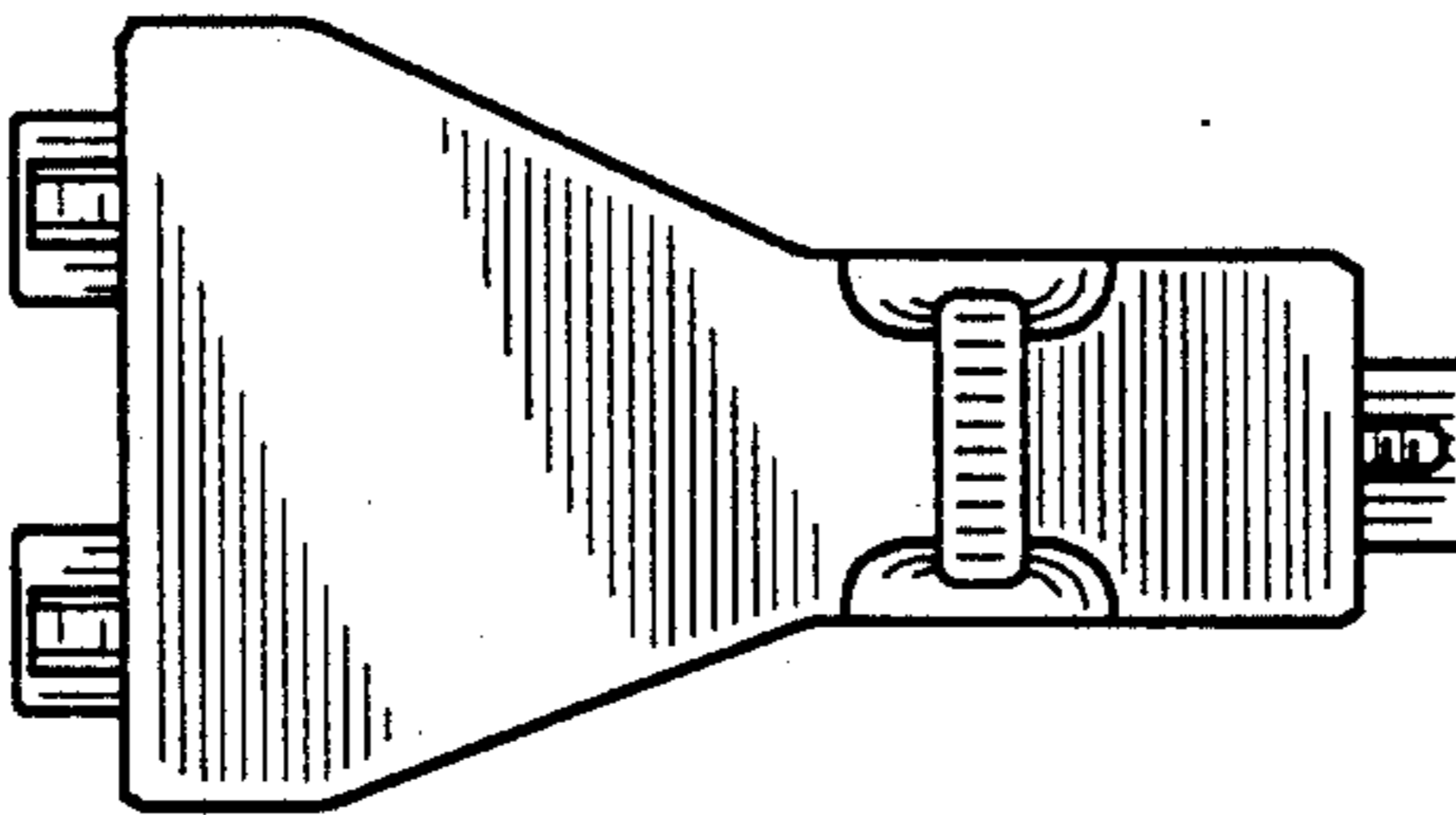


FIG. 5

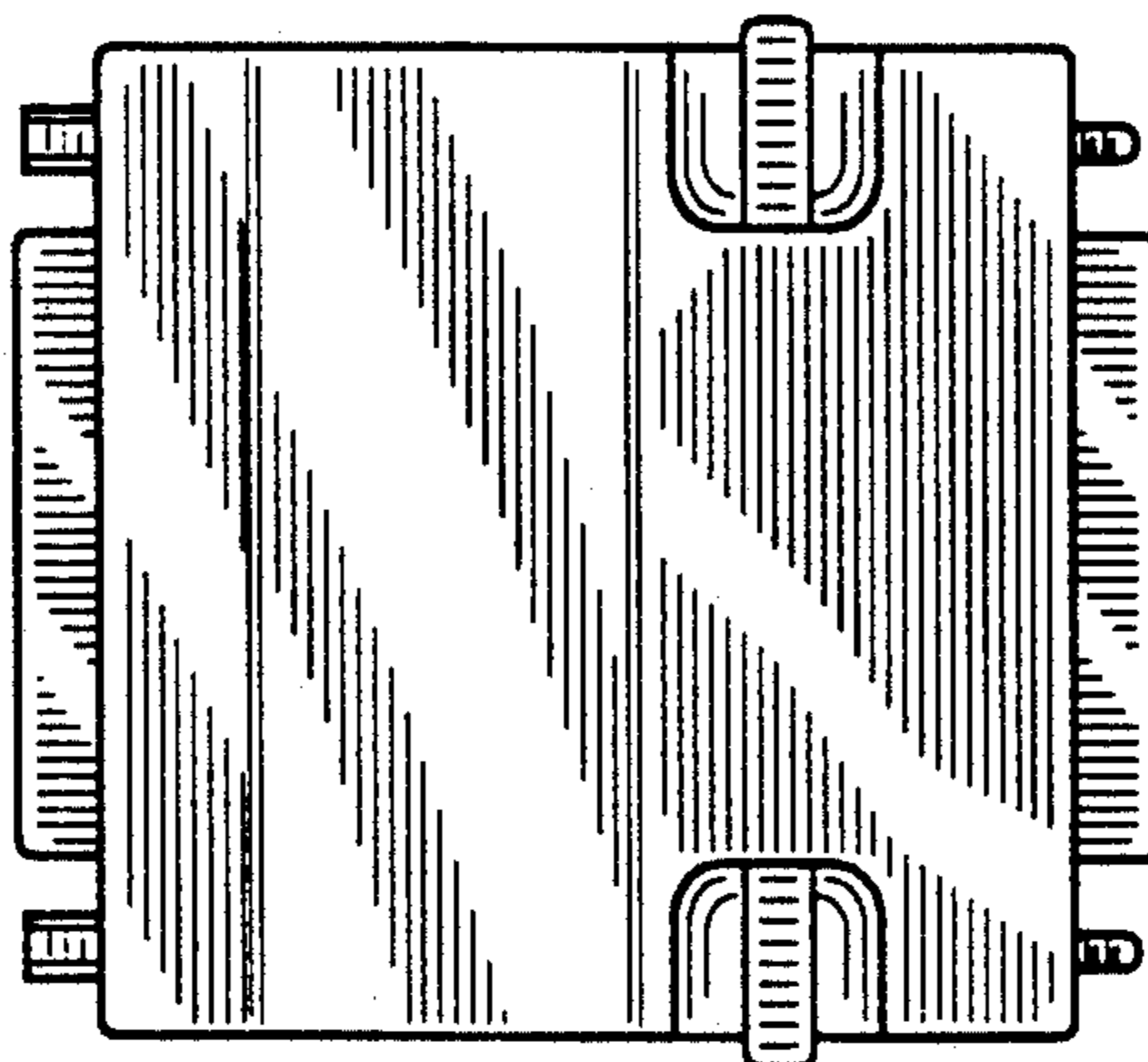


FIG. 6