



US00D339110S

United States Patent [19]

Steen, Jr. et al.

[11] Patent Number: **Des. 339,110**

[45] Date of Patent: **** Sep. 7, 1993**

[54] **HEAT SINK CLIP FOR SURFACE-MOUNT INTEGRATED CIRCUIT**

4,508,163 4/1985 McCarthy 165/80.2
4,961,125 10/1990 Jordan et al. 361/383

[75] Inventors: **Carl A. Steen, Jr., Park Ridge;
Robert B. Widmayer, Palatine, both
of Ill.**

FOREIGN PATENT DOCUMENTS

0220199 3/1985 Fed. Rep. of Germany 361/386

[73] Assignee: **Motorola, Inc., Schaumburg, Ill.**

Primary Examiner—Wallace R. Burke
Assistant Examiner—Joel Sincavage
Attorney, Agent, or Firm—James A. Coffing

[**] Term: **14 Years**

[57] CLAIM

[21] Appl. No.: **754,133**

The ornamental design for a heat sink clip for surface-mount integrated circuit, as shown and described.

[22] Filed: **Sep. 3, 1991**

DESCRIPTION

[52] U.S. Cl. **D13/179**

[58] Field of Search **D13/179; 165/80.2;
257/713, 718; 361/383, 386; 174/16.3**

FIG. 1 is a front, top, and right side perspective view showing our new design for a heat sink clip for surface-mount integrated circuit;

[56] References Cited

U.S. PATENT DOCUMENTS

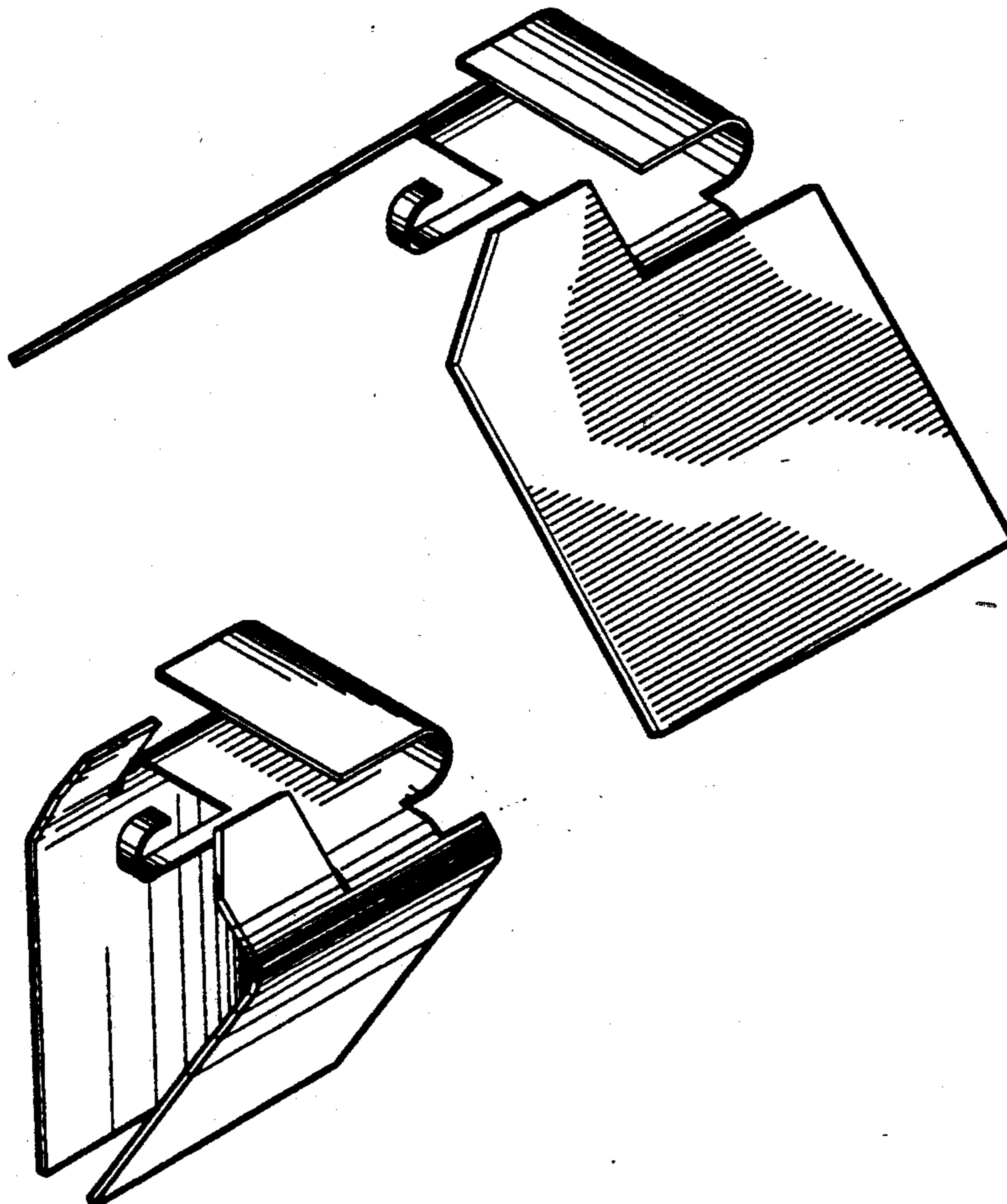
D. 262,203 12/1981 Johnson et al. D13/179
D. 275,749 10/1984 McCarthy D13/179
D. 282,838 3/1986 McCarthy D13/179
D. 283,503 4/1986 McCarthy D13/179
4,203,488 5/1980 Johnson et al. D13/179 X

FIG. 2 is a left elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a bottom plan view thereof; and,

FIG. 5 is a front, top, and right side perspective view showing an alternate embodiment for a heat sink clip for surface mount integrated circuit, the front, top, and left side perspective view being a mirror image.



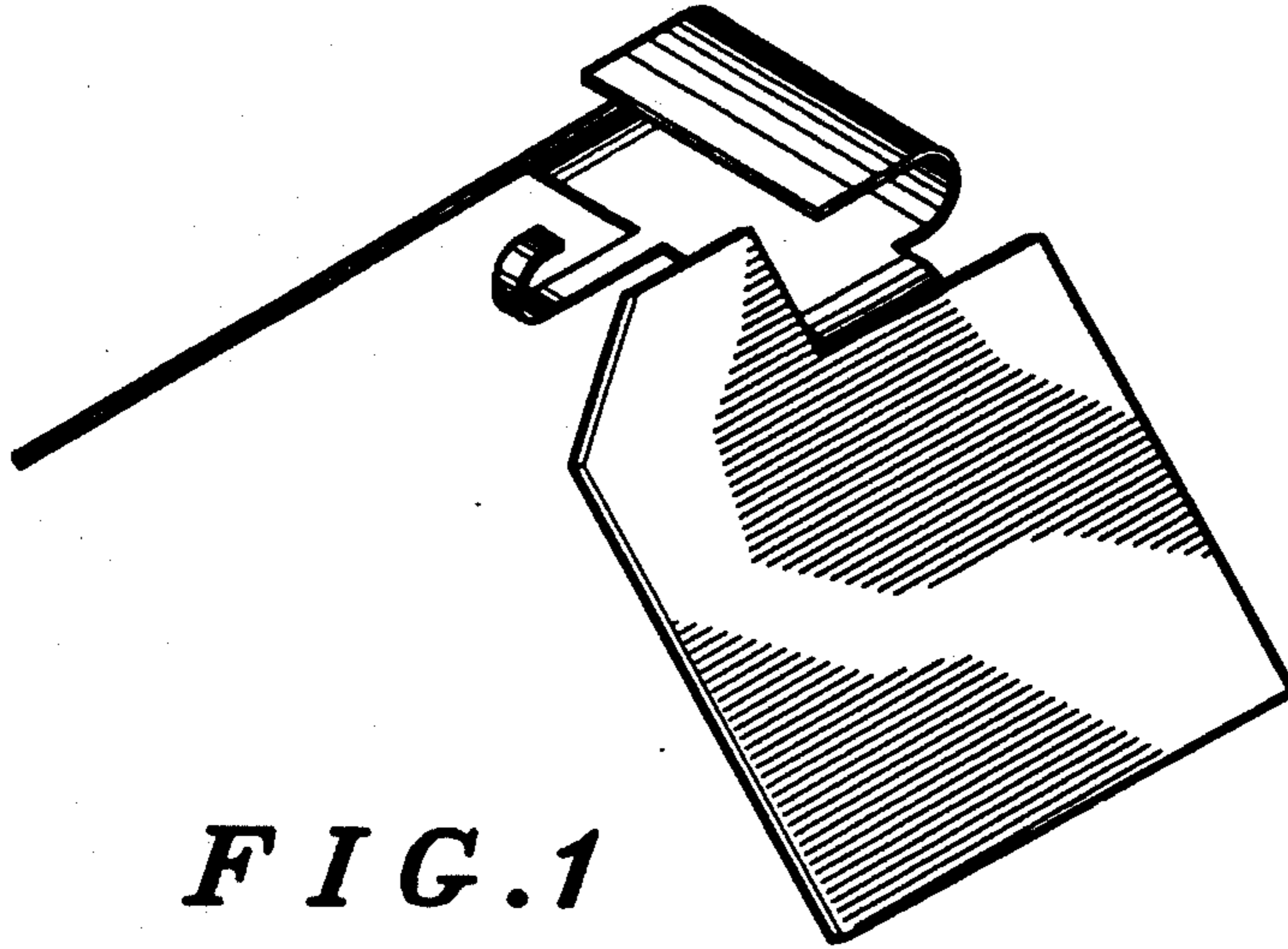


FIG. 1

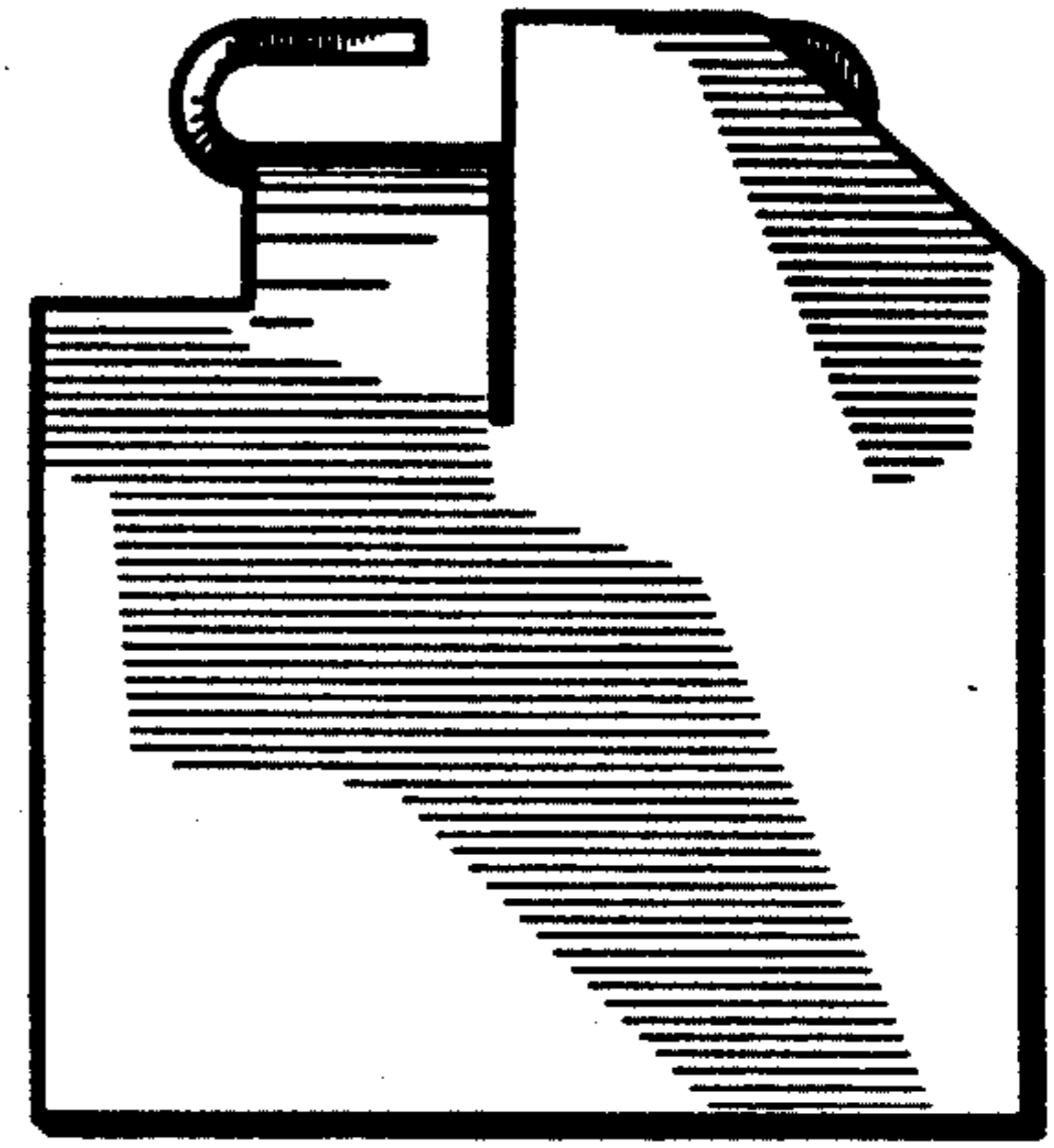


FIG. 2

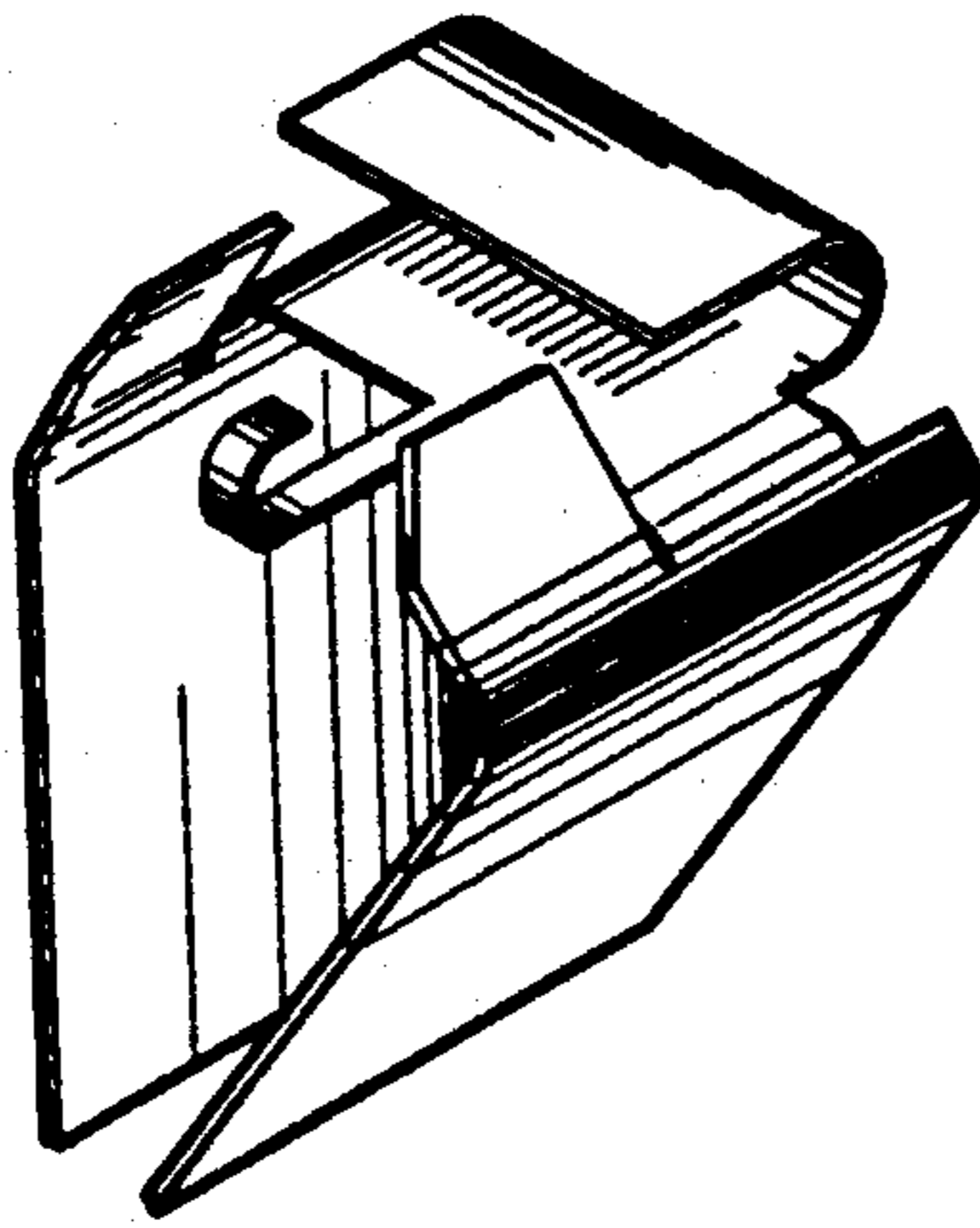


FIG. 5

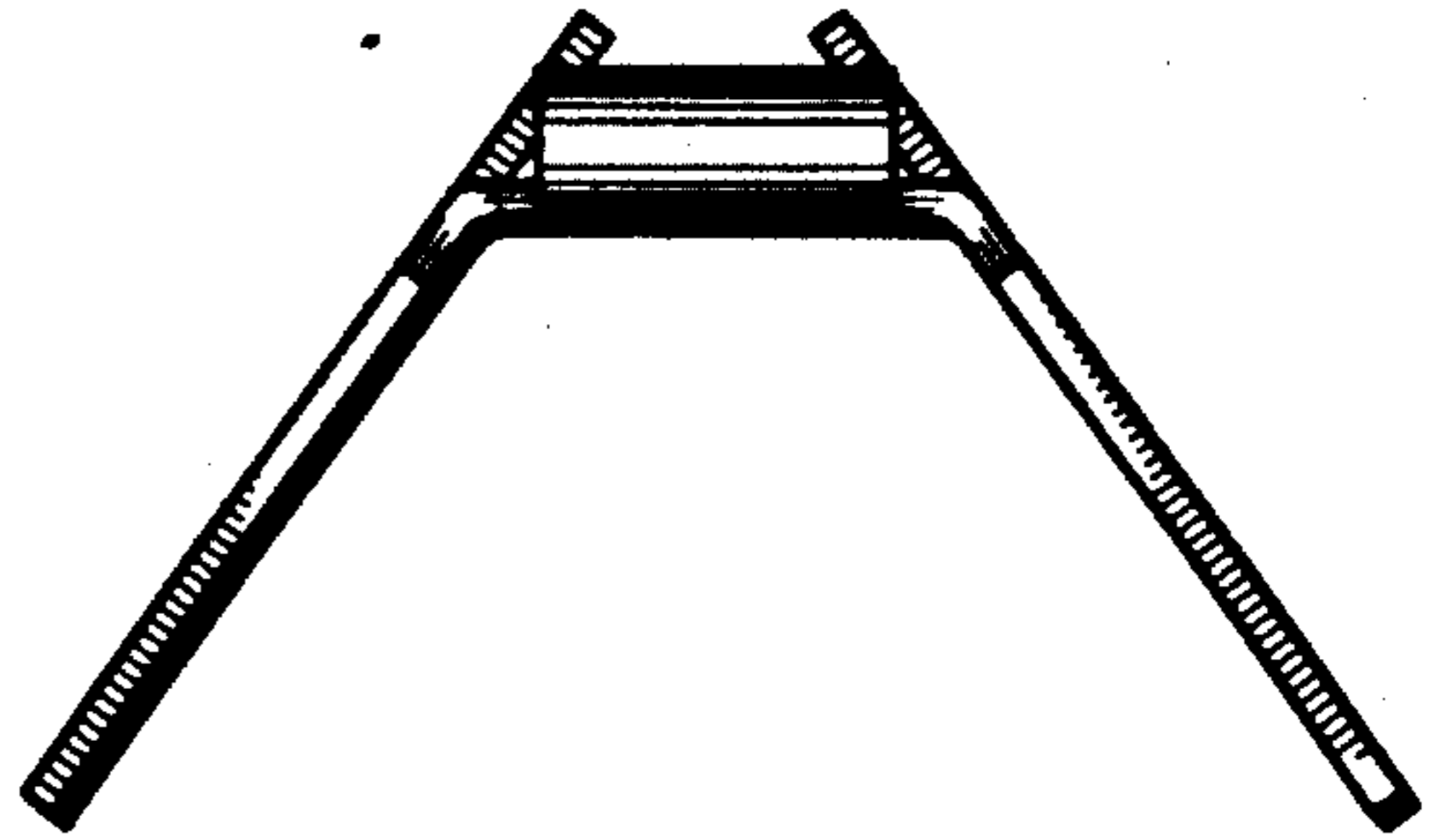


FIG. 3

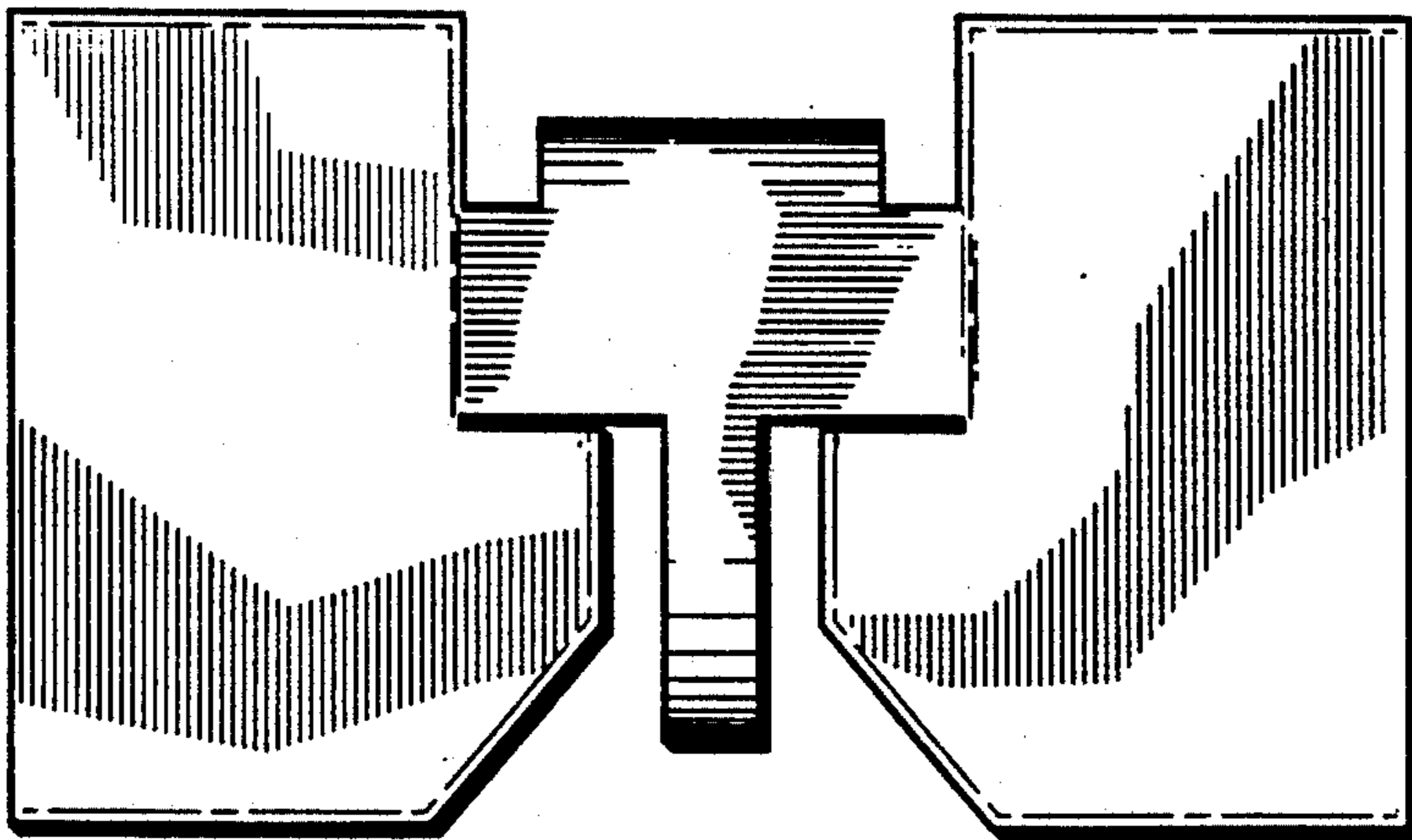


FIG. 4