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United States Patent [19]

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Berry et al.

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[54] PRINTED CIRCUIT BOARD TESTER

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[73] Assignee: **Hewlett-Packard Company**, Palo Alto, Calif.

[*] Notice: The portion of the term of this patent subsequent to Jun. 17, 2005 has been disclaimed.

[**] Term: **14 Years**

[21] Appl. No.: **224,501**

[22] Filed: **Jul. 25, 1988**

[52] U.S. Cl. **D10/75**

[58] Field of Search **D10/46, 75; D13/162, D13/163, 164, 184; D14/102, 103, 108; 324/158 F, 158 R, 156; 364/481**

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OTHER PUBLICATIONS

Zehntel 8000 Combination Test Service brochure; (note sections 3.1 and 3.2 and Figure 3.1c).
Page from the 1985 brochure showing the Teradyne L210 Circuit Board Tester.

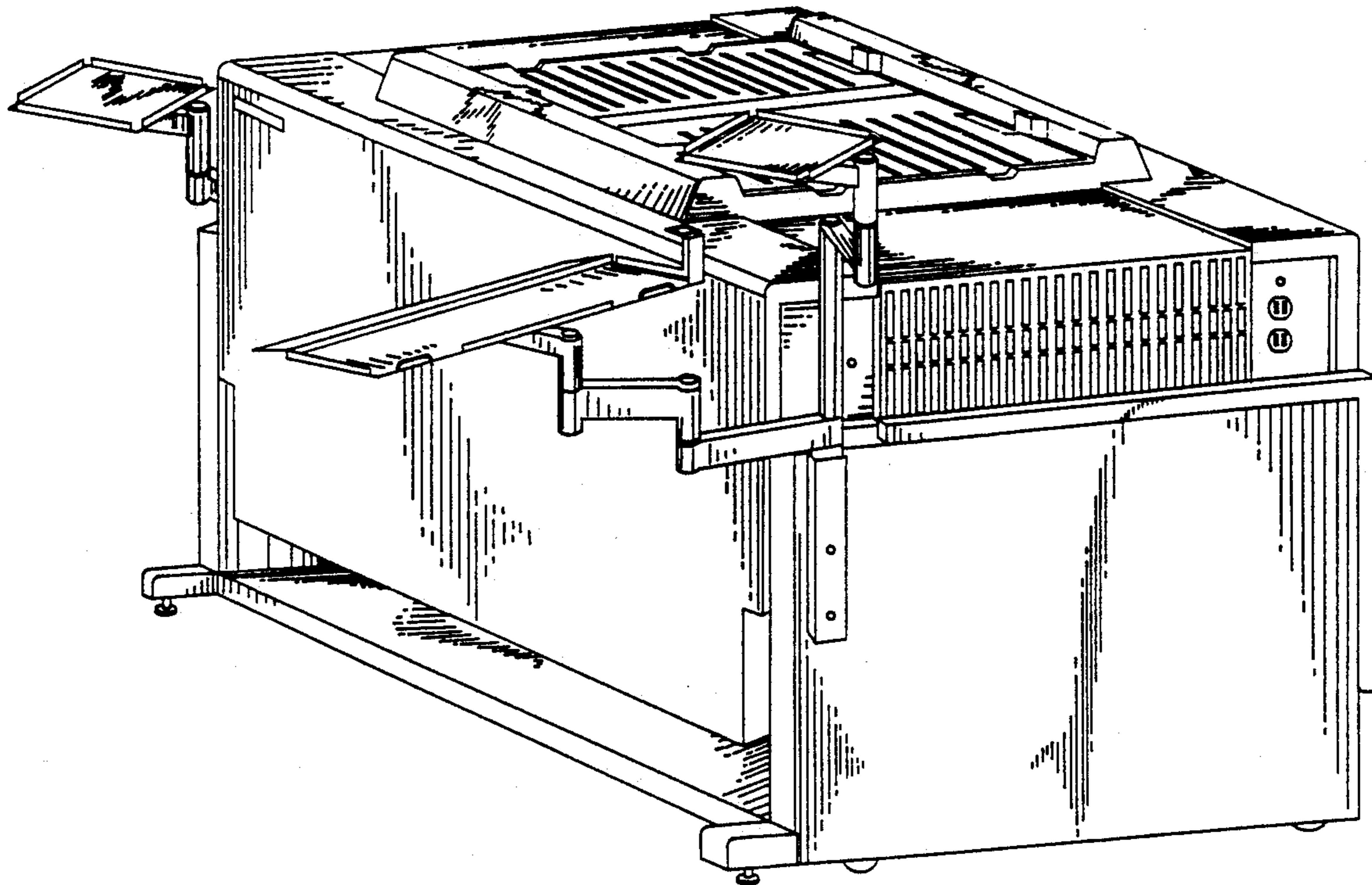
Primary Examiner—Nelson C. Holtje
Assistant Examiner—Antoine D. Davis
Attorney, Agent, or Firm—Christopher J. Byrne

[57] CLAIM

The ornamental design for a printed circuit board tester, as shown and described.

DESCRIPTION

FIG. 1 is a top, front, and right side perspective view of an printed circuit board tester showing our new design; FIG. 2 is another top, front, and right side perspective view, with the console in a rotated position; FIG. 3 is a front elevational view; FIG. 4 is a right side elevational view; FIG. 5 is a left side elevational view; FIG. 6 is a rear elevational view; and, FIG. 7 is a top plan view thereof.



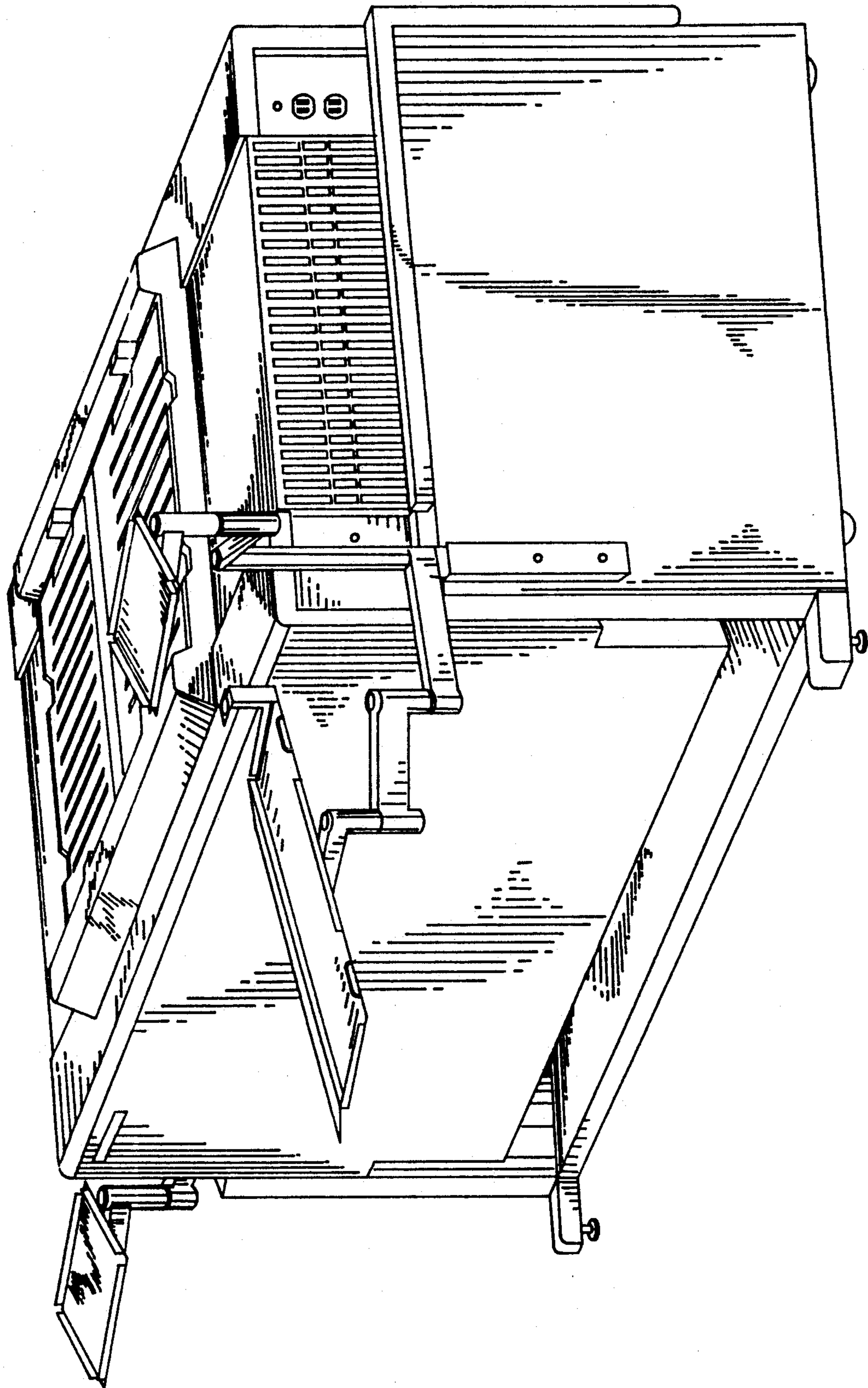


FIG 1

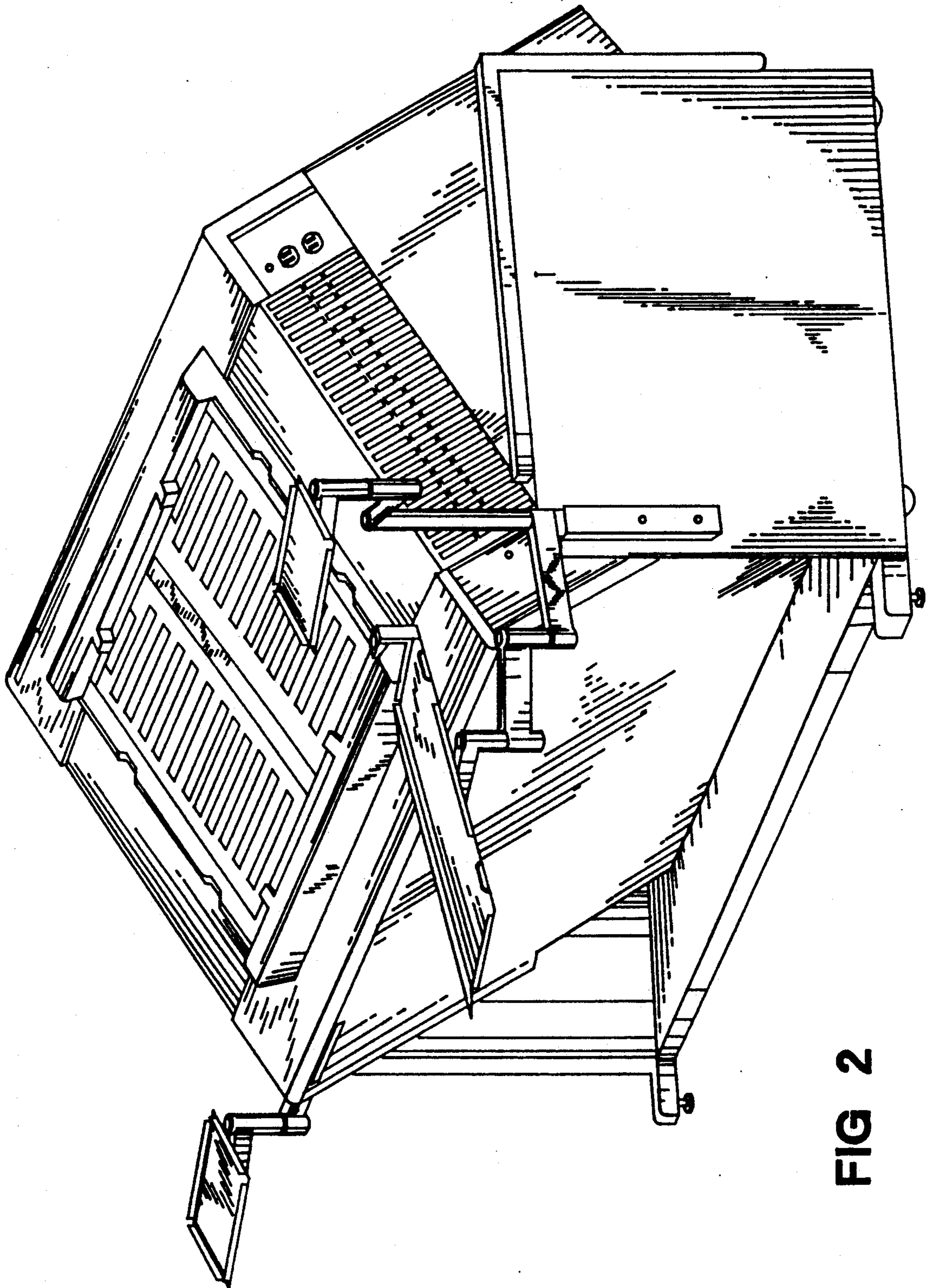


FIG 2

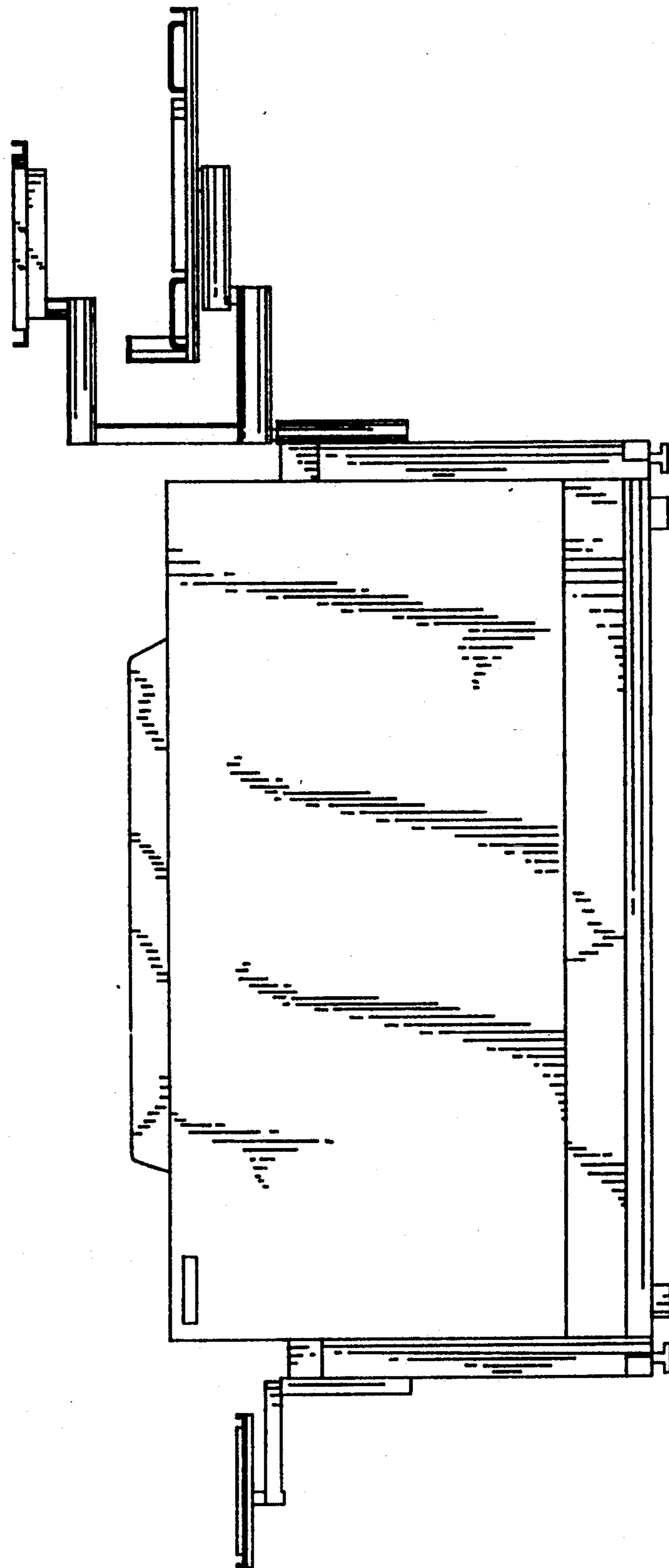


FIG 3

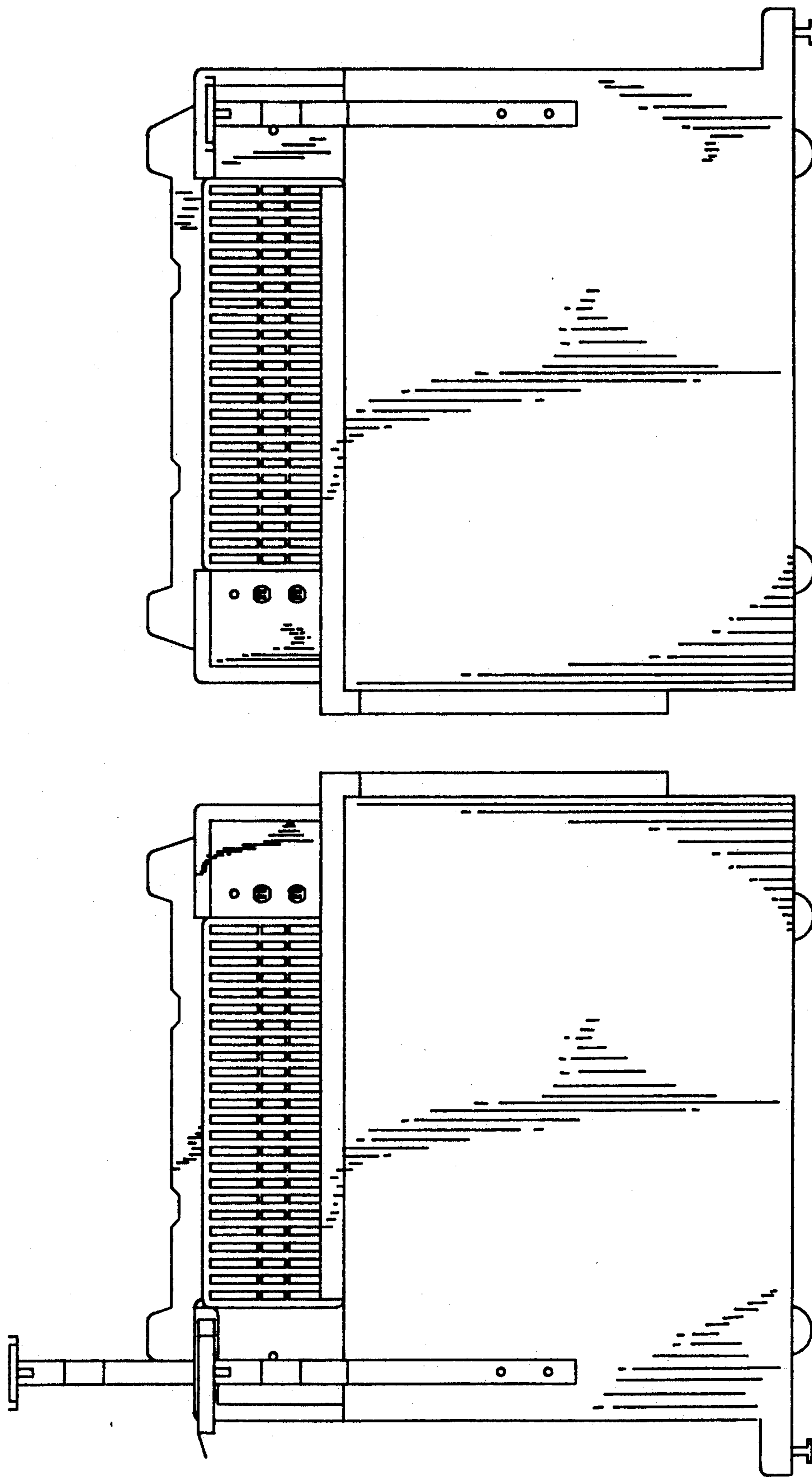


FIG 4

FIG 5

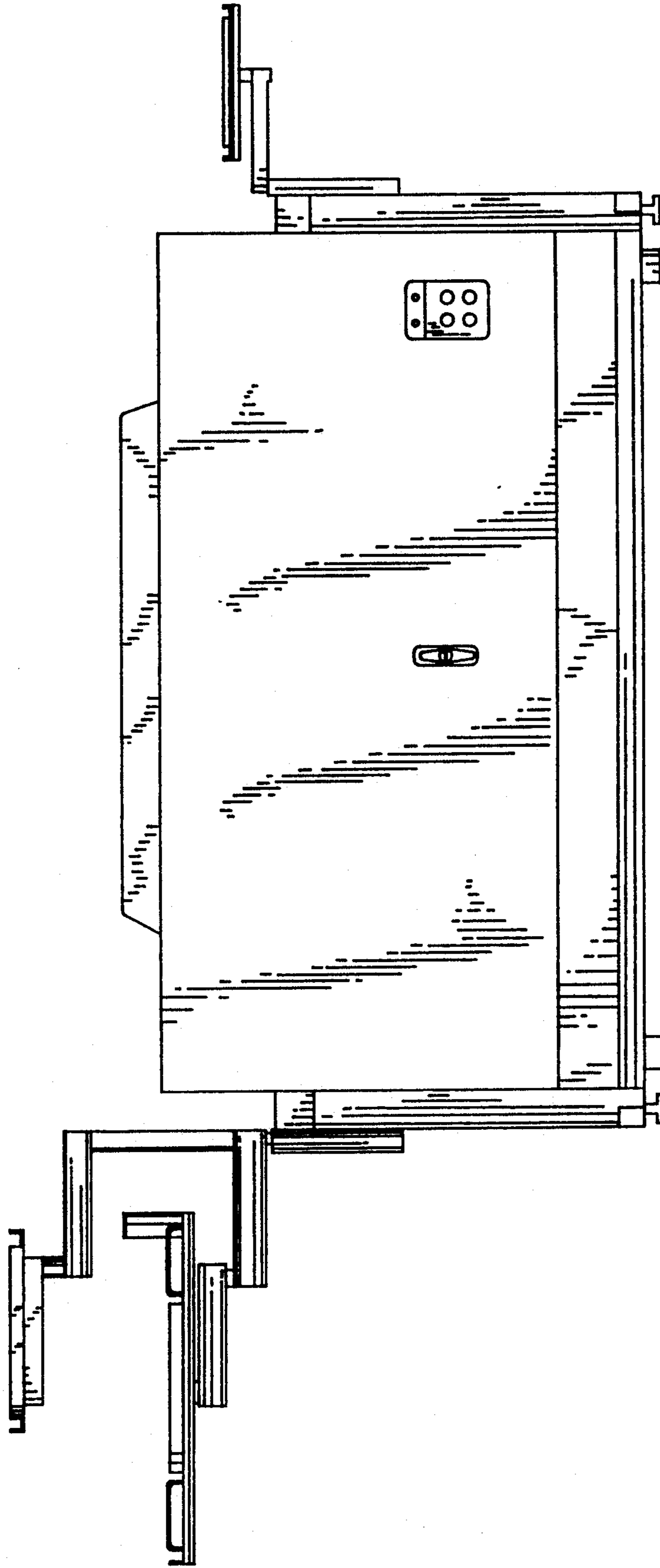


FIG 6

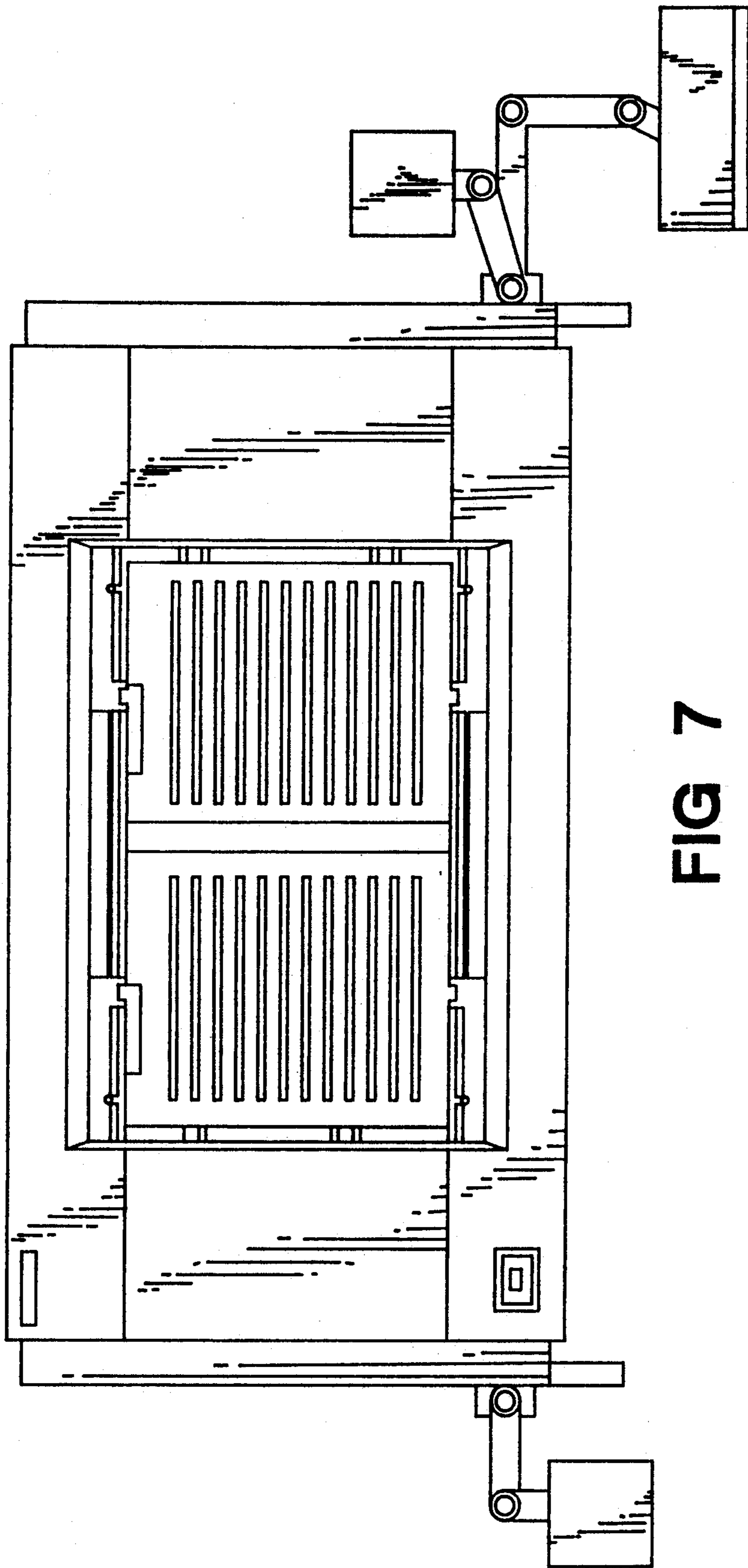


FIG 7