



US00D337761S

United States Patent [19]

Morgan et al.

[11] Patent Number: **Des. 337,761**

[45] Date of Patent: **** Jul. 27, 1993**

[54] **ELECTRONIC DEVICE MODULE**

[75] Inventors: **Stuart K. Morgan, Westford;**
Margaret L. Hetfield, Arlington,
both of Mass.

[73] Assignee: **Digital Equipment Corporation,**
Maynard, Mass.

[**] Term: **14 Years**

[21] Appl. No.: **683,371**

[22] Filed: **Apr. 8, 1991**

[52] U.S. Cl. **D14/114; D13/162**

[58] Field of Search **D14/100, 107, 114;**
D13/147, 162, 199, 123; 361/392, 393, 394;
200/5 A, 5 R

D. 303,246 9/1989 Freeman et al. D13/147
D. 303,524 9/1989 Siegner et al. D14/114
D. 307,741 5/1990 Gilbert D13/162
D. 326,447 5/1992 Hofland et al. D14/107
D. 330,198 10/1992 Lajara et al. D14/107 X

Primary Examiner—Wallace R. Burke
Assistant Examiner—Freda S. Nunn
Attorney, Agent, or Firm—Richard J. Paciulan; Barry
Young; Ronald Myrick

[57] **CLAIM**

The ornamental design for an electronic device module,
as shown and described.

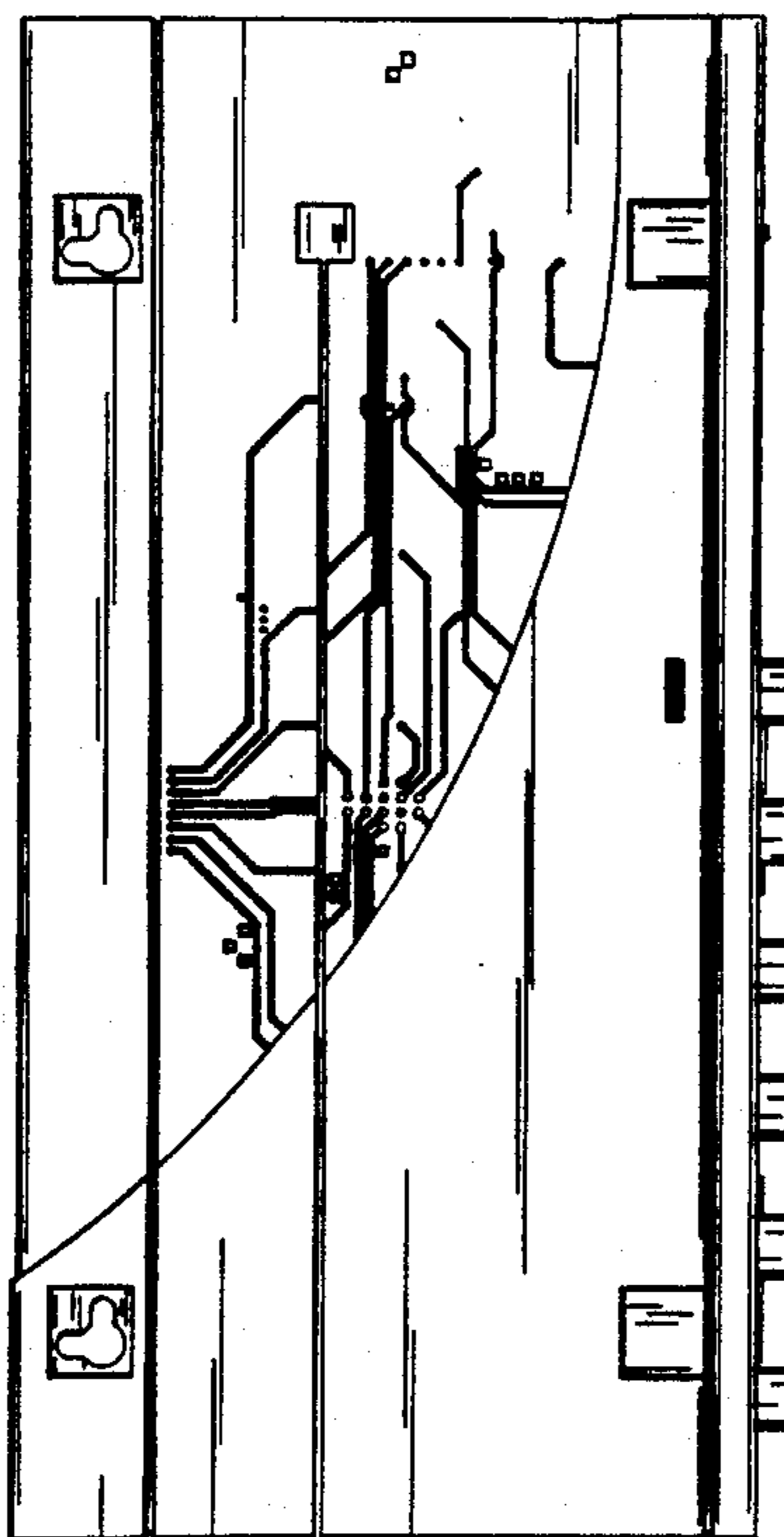
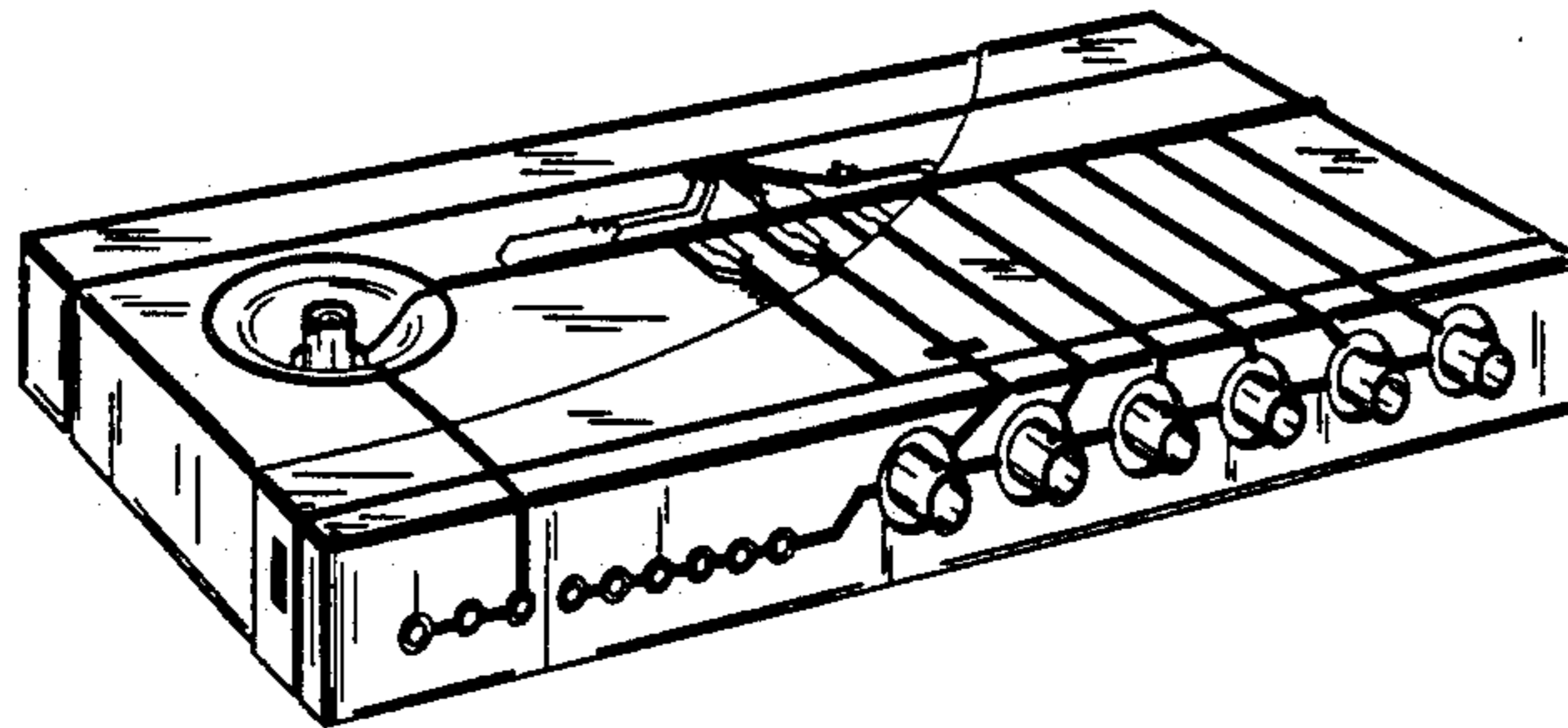
DESCRIPTION

FIG. 1 is a reduced front left side perspective view of an
electronic device module showing our new design;
FIG. 2 is a left side elevational view thereof;
FIG. 3 is a right side elevational view thereof;
FIG. 4 is a front elevational view thereof;
FIG. 5 is a top plan view thereof;
FIG. 6 is a rear elevational view thereof; and,
FIG. 7 is a bottom plan view thereof.

[56] **References Cited**

U.S. PATENT DOCUMENTS

D. 268,931 5/1983 Ellis et al. D14/107
D. 269,673 7/1983 Johansson et al. D14/114 X
D. 281,493 11/1985 Prager et al. D13/162
D. 284,195 6/1986 Davis et al. D14/114



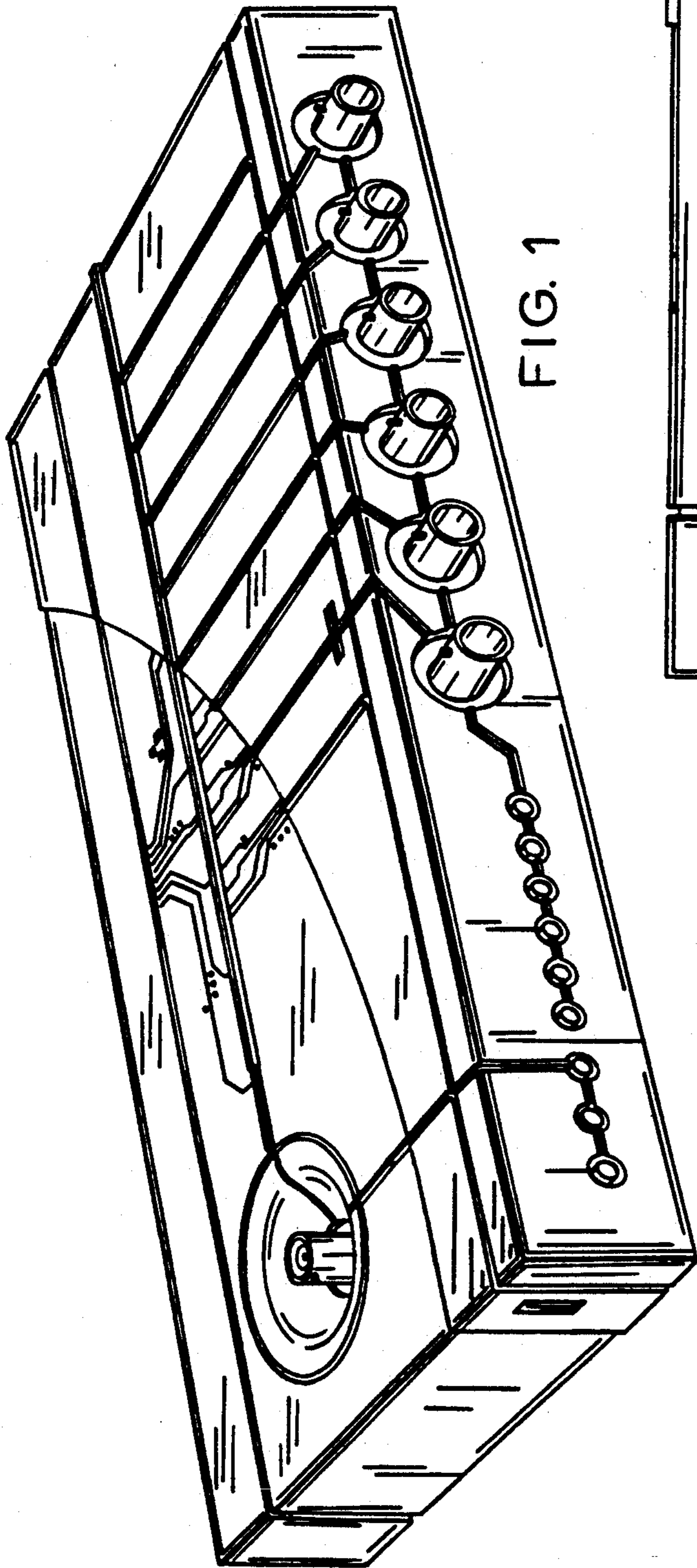


FIG. 1

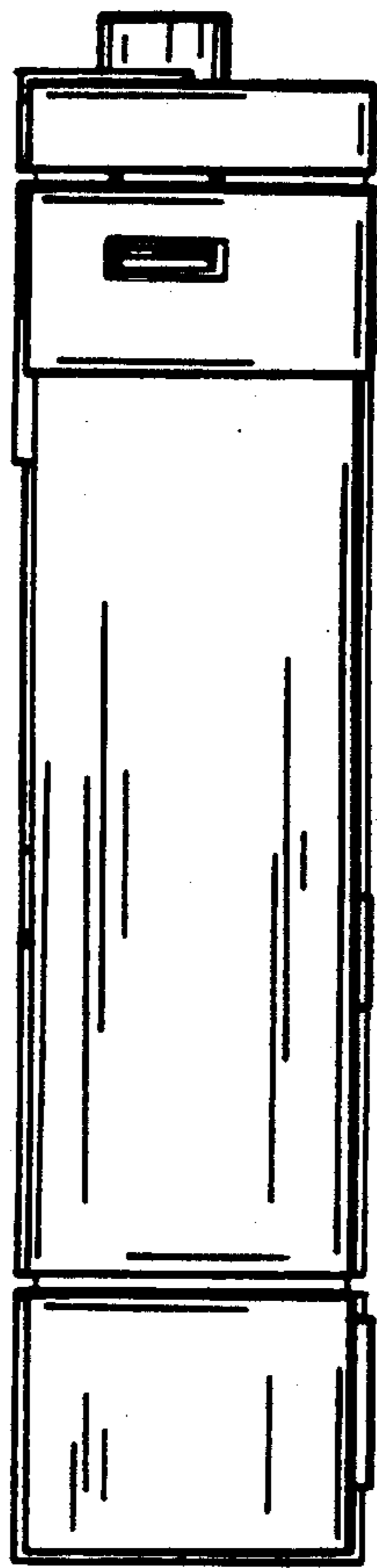


FIG. 2

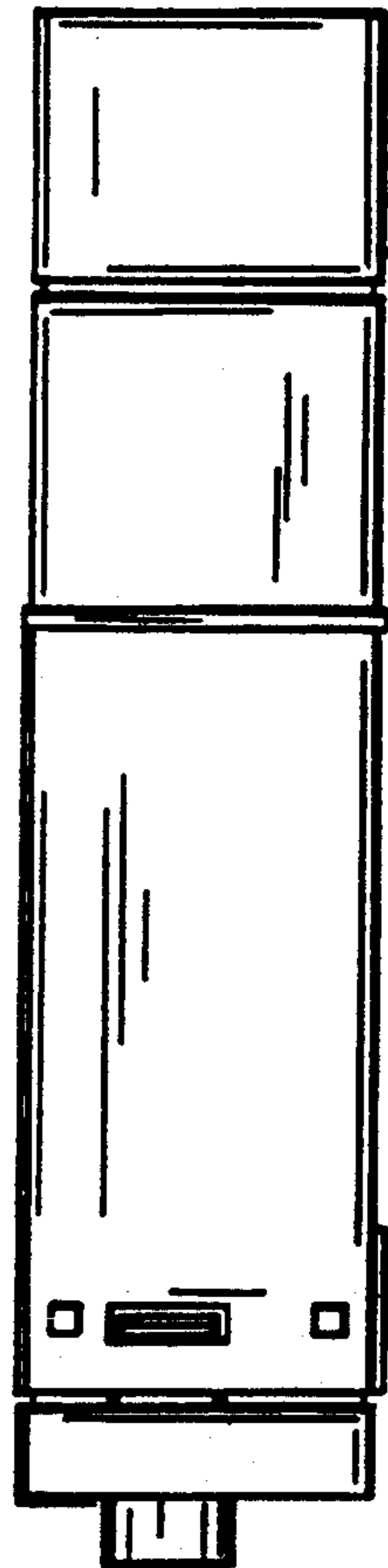


FIG. 3

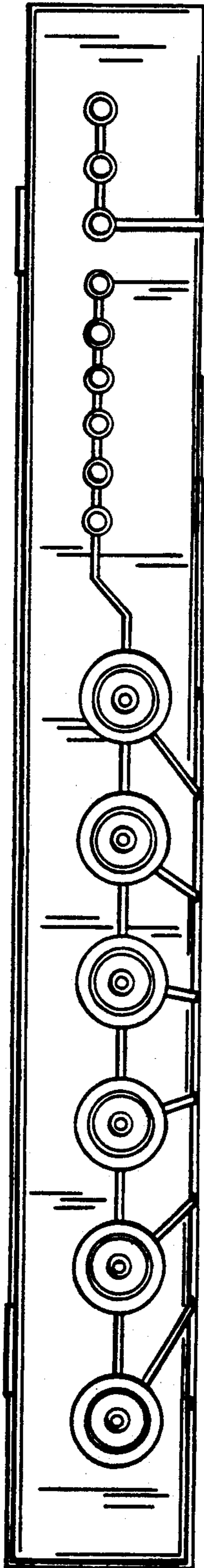


FIG. 4

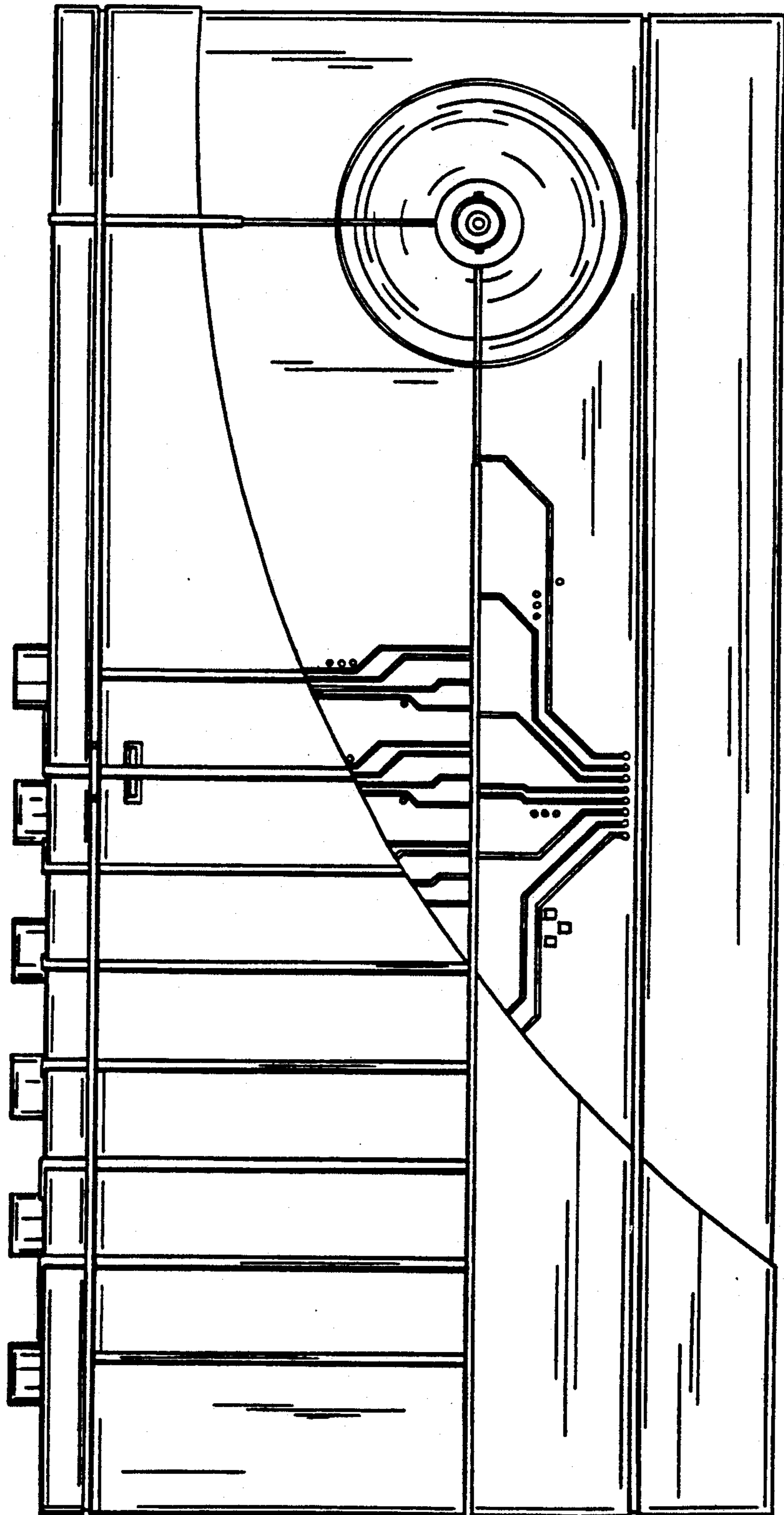


FIG. 5

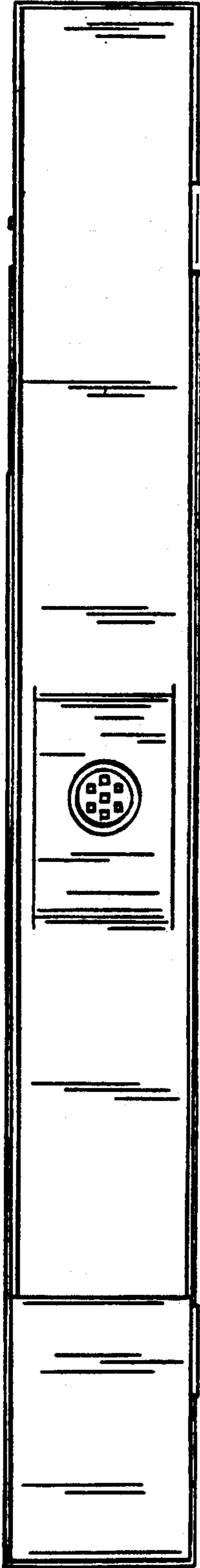


FIG. 6

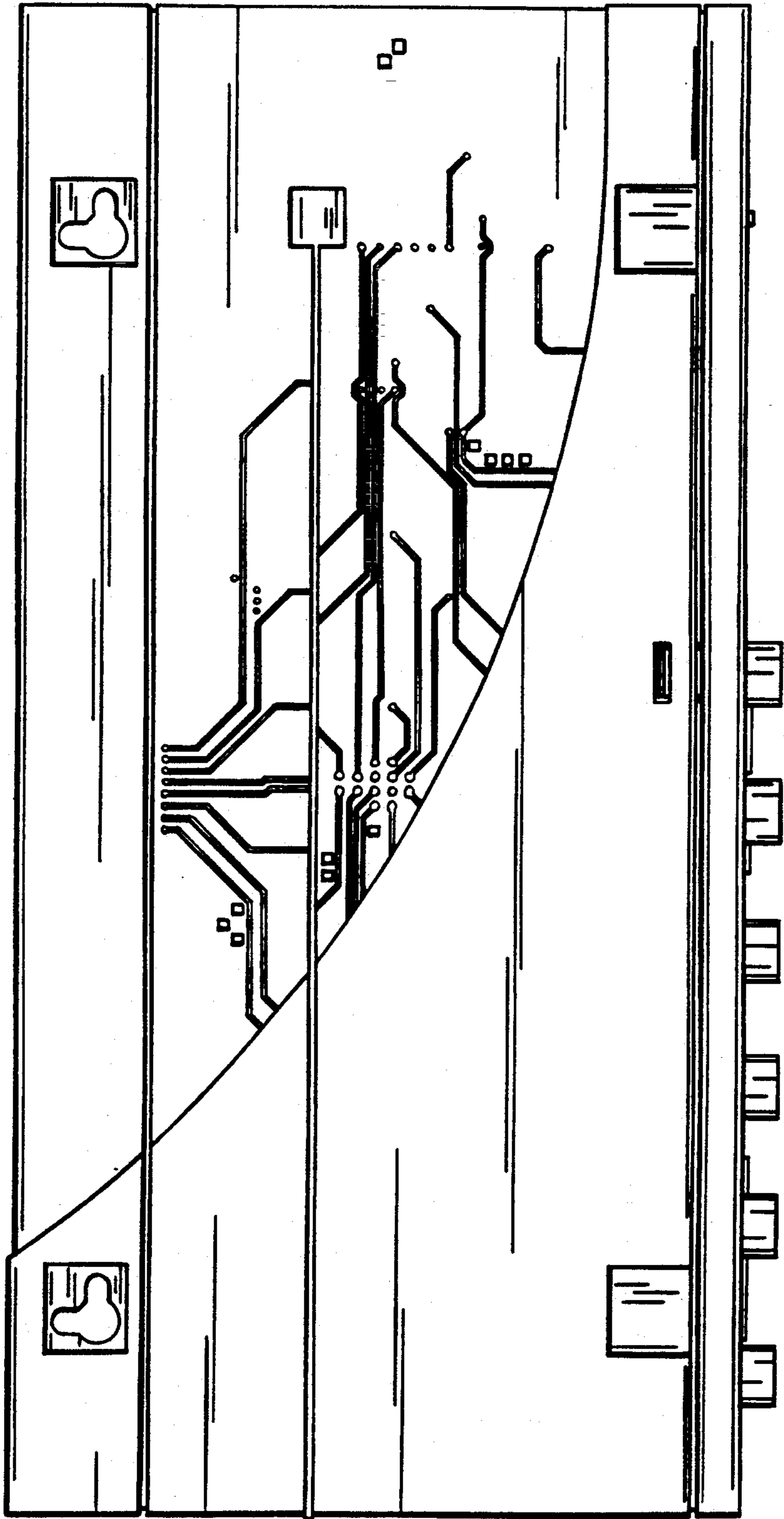


FIG. 7