



US00D336634S

United States Patent [19]

[11] Patent Number: Des. 336,634

Richardson et al.

[45] Date of Patent: ** Jun. 22, 1993

- [54] ELECTRONIC CIRCUIT SETUP STATION FOR EDUCATION AND TRAINING
- [75] Inventors: William F. Richardson, East Haven; Robert Melone, North Haven, both of Conn.
- [73] Assignee: Interplex Electronics, Inc., New Haven, Conn.
- [**] Term: 14 Years
- [21] Appl. No.: 744,012
- [22] Filed: Aug. 12, 1991
- [52] U.S. Cl. D13/164; D19/62
- [58] Field of Search D13/162, 164, 184, 199; D10/75; D19/62; 361/380, 413, 417; 439/49; 174/250, 254

PB-503 protoboard on p. 659 of Allied Electronics Catalog No. 900, copyright 1990.
 BL-4 breadboard lab on p. 675 of Allied Electronics Catalog No. 900, copyright 1990.

Primary Examiner—Wallace R. Burke
 Assistant Examiner—J. Sincavage
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[57] CLAIM

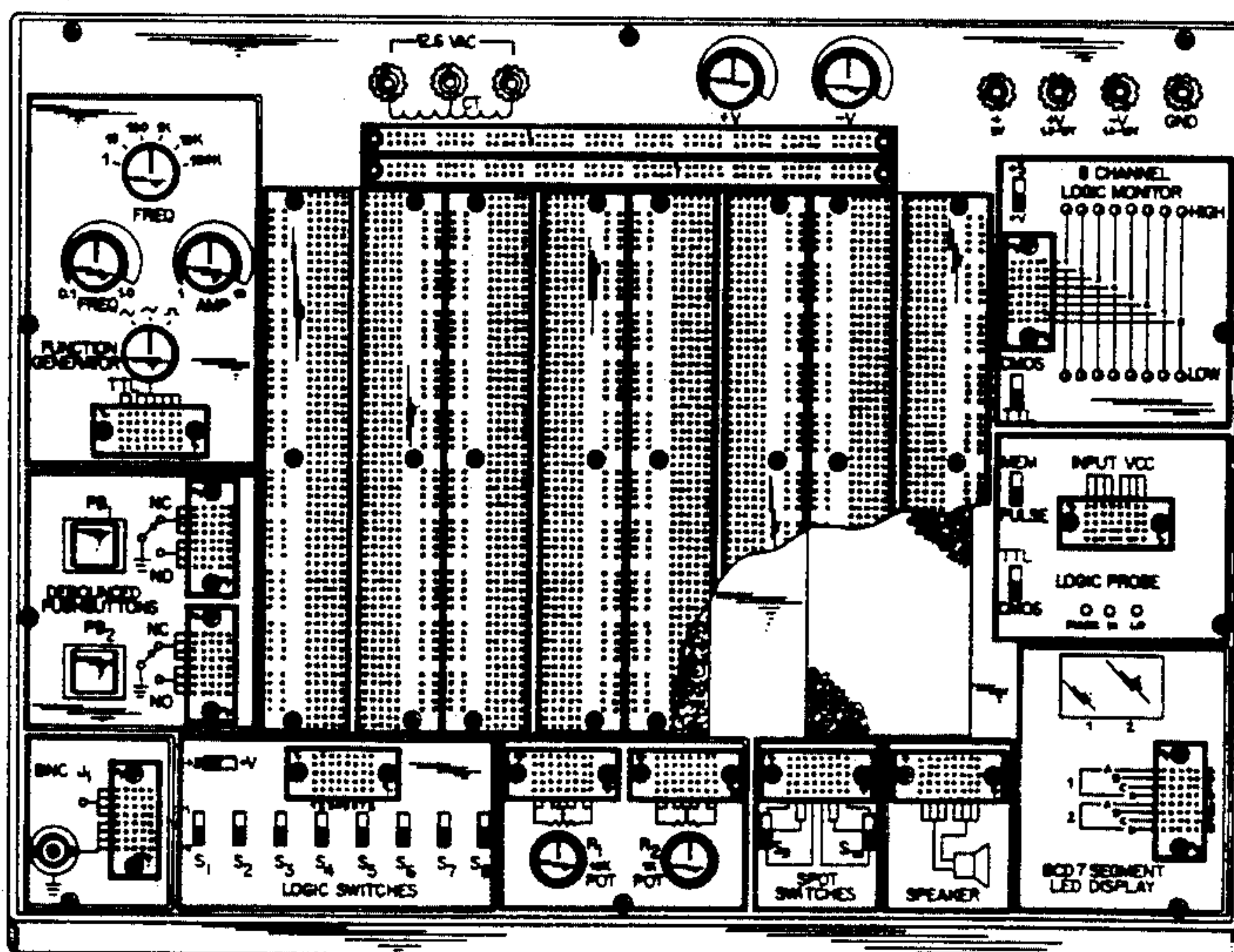
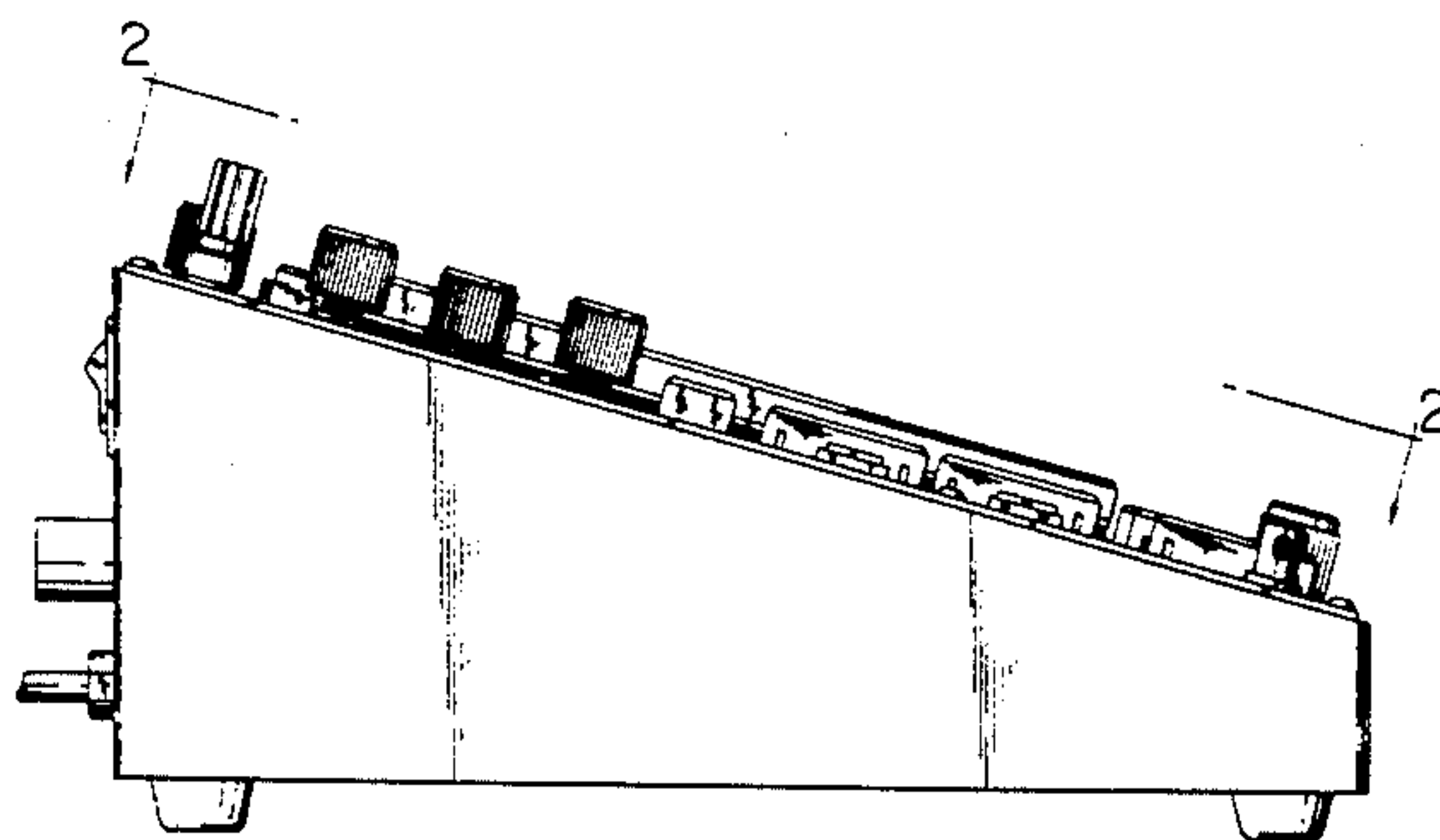
The ornamental design for an electronic circuit setup station for education and training, as shown and described.

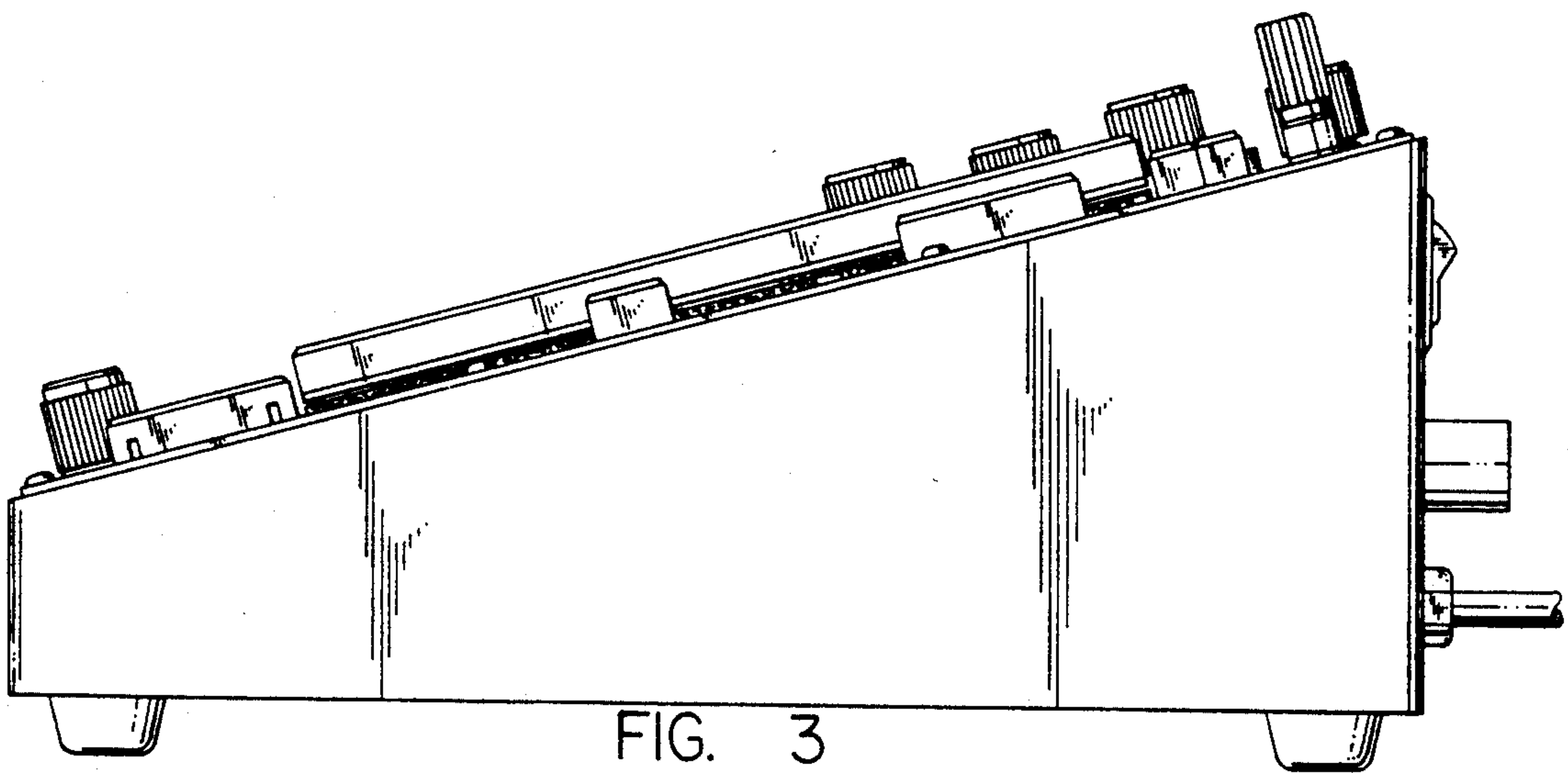
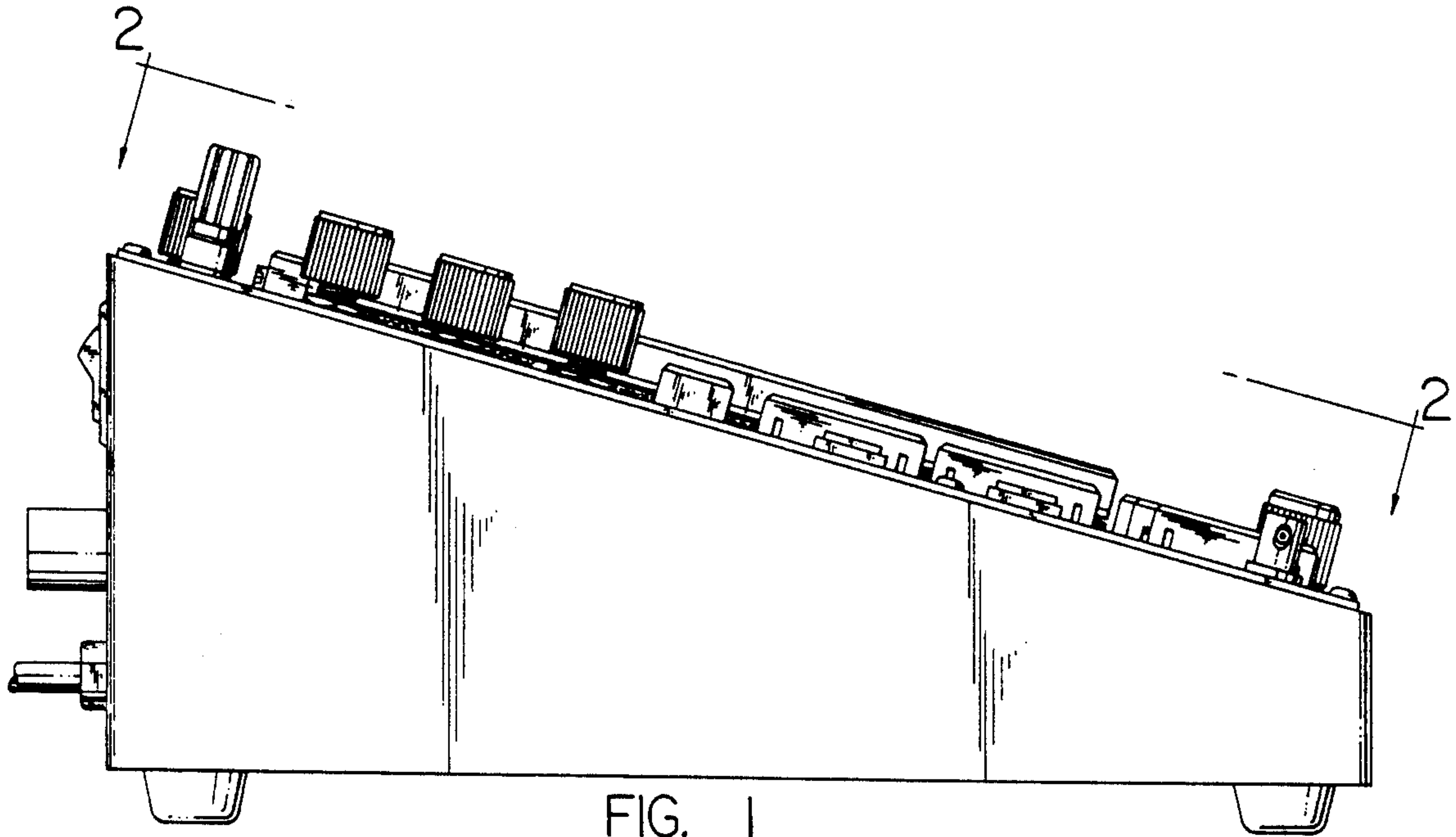
DESCRIPTION

FIG. 1 is a left side elevational view of an electronic circuit setup station for education and training showing my new design;
 FIG. 2 is a view thereof taken along line 2—2 of FIG. 1. For the purpose of a complete disclosure, the breadboard socket panel is shown partially cut away to reveal the velcro mounting strips shown in FIGS. 1 and 3;
 FIG. 3 is a right side elevational view thereof;
 FIG. 4 is a rear elevational view thereof;
 FIG. 5 is a bottom view thereof; and,
 FIG. 6 is a view of an electronic setup station taken along a line in a second embodiment corresponding to line 2—2 of FIG. 1. The second embodiment is the same as the first except the velcro mounted breadboard panel is replaced with a screw mounted circuit panel.

- [56] **References Cited**
- U.S. PATENT DOCUMENTS**
- D. 216,984 3/1970 Stanford et al. D10/75
- D. 237,596 11/1975 Jack D10/75
- D. 242,248 11/1976 Jack D19/62
- D. 304,957 12/1989 Melone D16/62
- D. 308,046 5/1990 Melone D13/164

- OTHER PUBLICATIONS**
- Microprocessor trainer on p. 65 of 1987 Heathkit Catalog.
- Circuit design trainer on p. 77 of 1987 Heathkit Catalog.





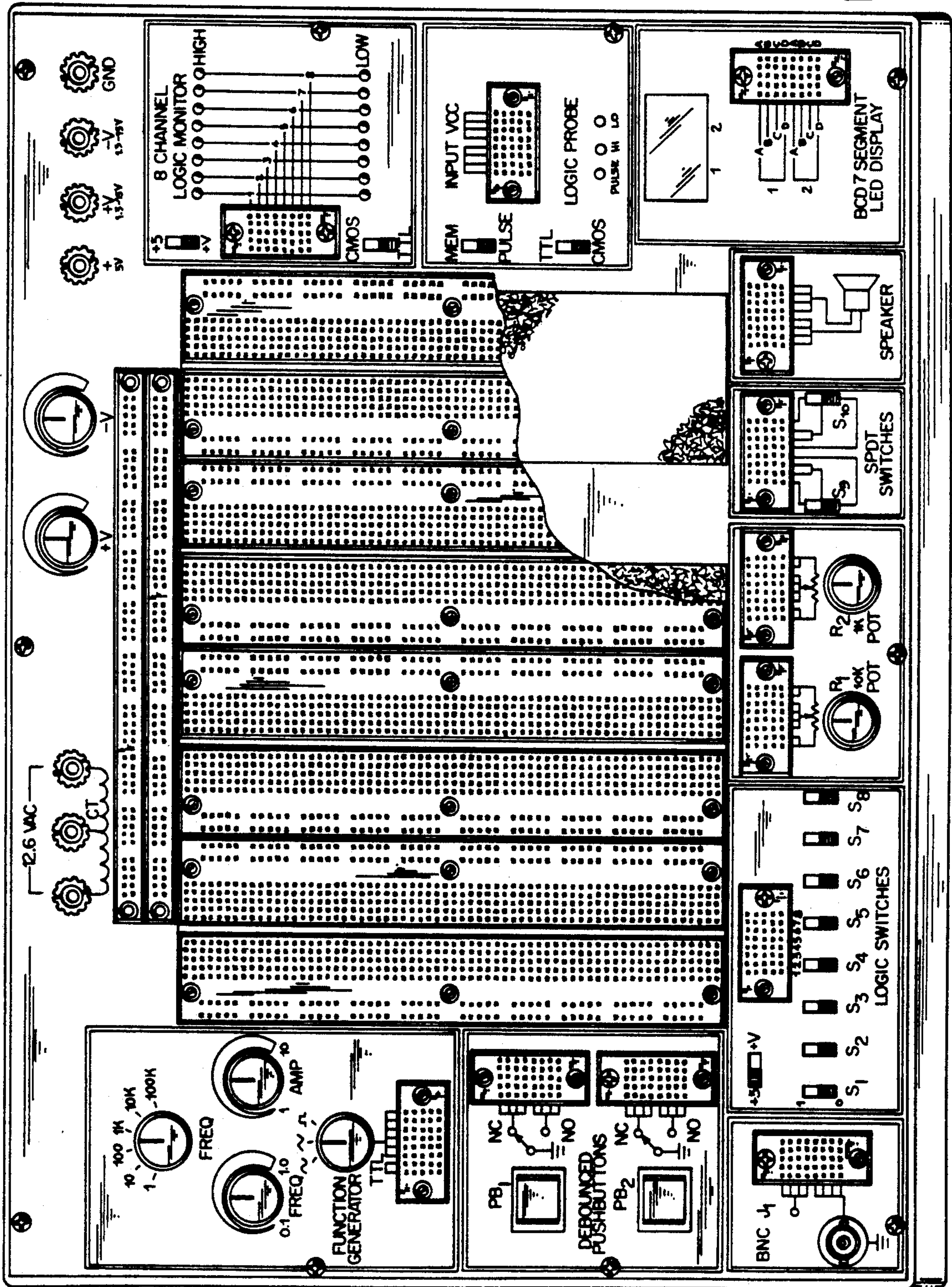


FIG. 2

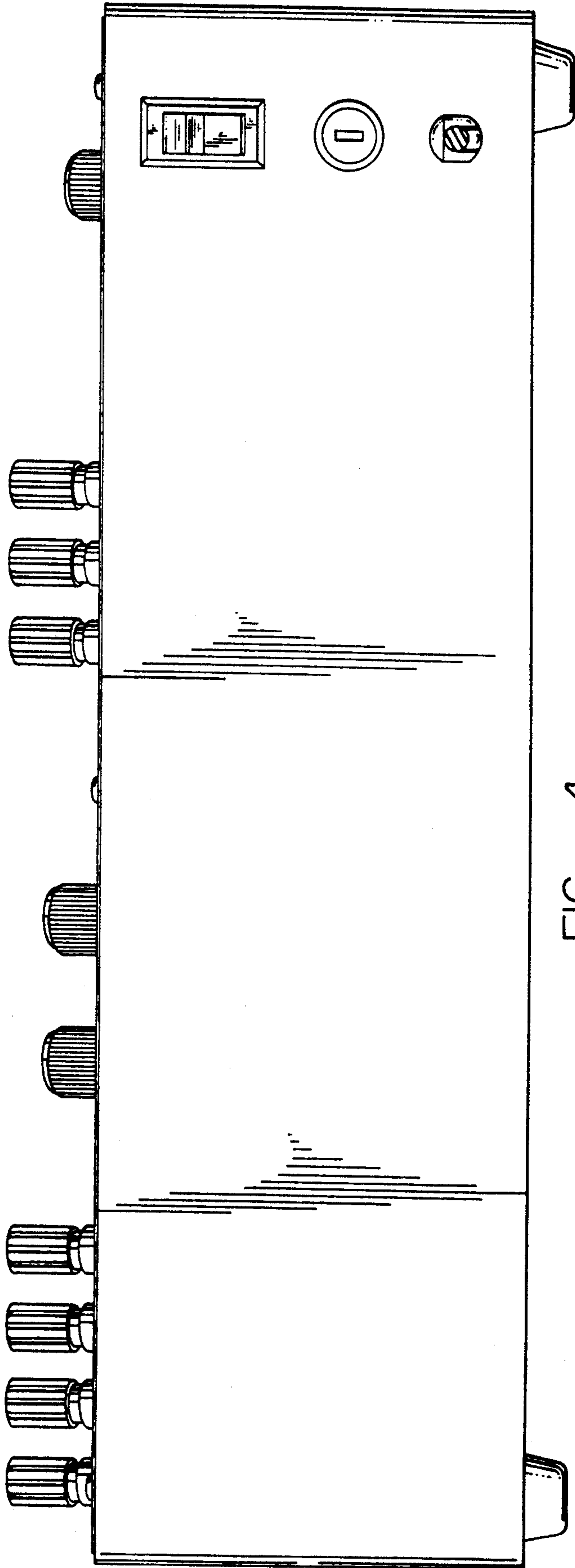


FIG. 4

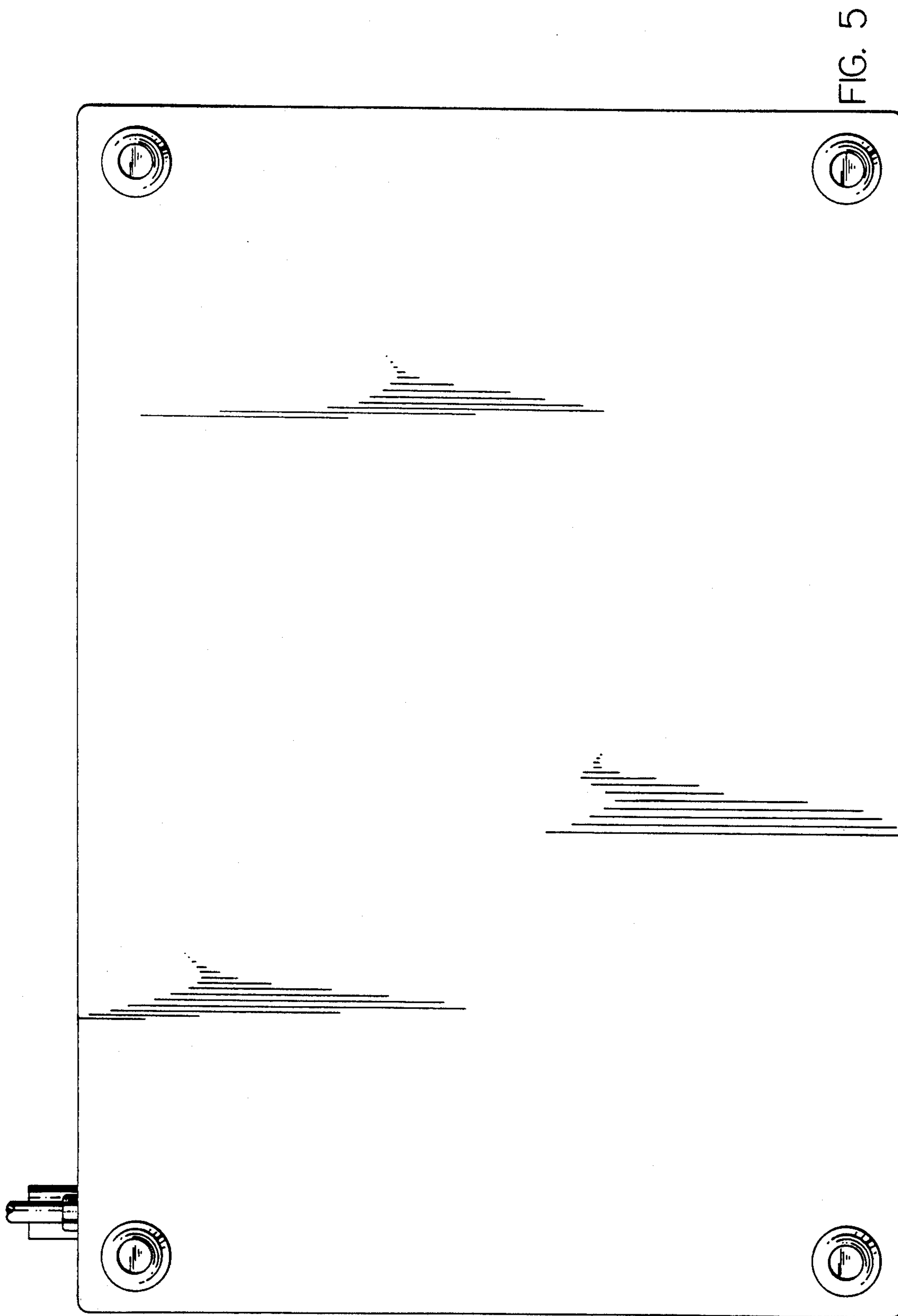


FIG. 5

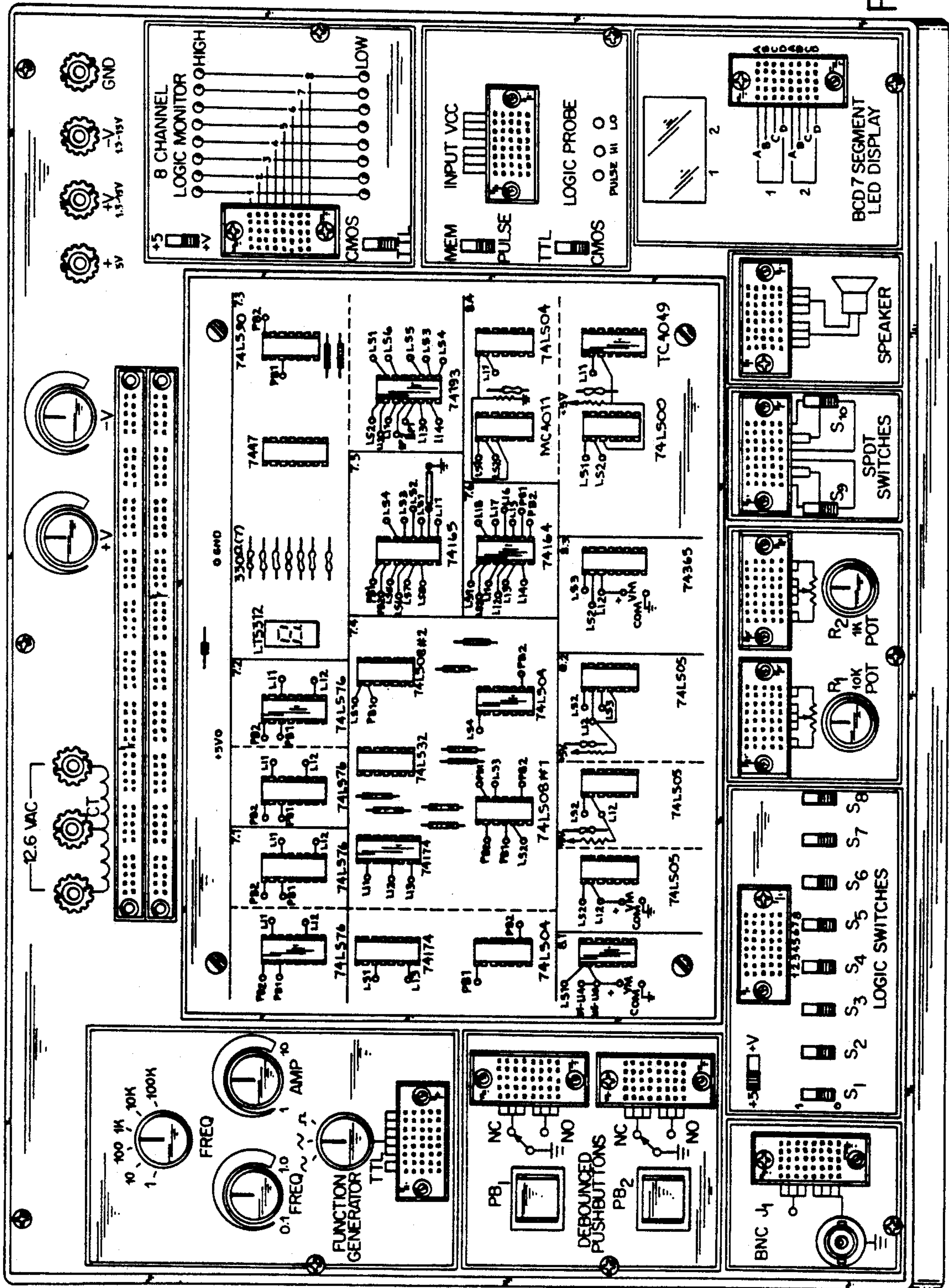


FIG. 6