US00D335124S

United States Patent [19]

Mizusugi et al.

[11] **Patent Number: Des. 335,124**

[45] **Date of Patent: ** Apr. 27, 1993**

[54] **COMPUTER**

[75] Inventors: **Kanji Mizusugi**, Mie; **Kensho Tuji**; **Hiroshi Yamamizu**, both of Nara, all of Japan

[73] Assignee: **Sharp Kabushiki Kaisha**, Osaka, Japan

[**] Term: **14 Years**

[21] Appl. No.: **714,759**

[22] Filed: **Jun. 17, 1991**

[30] **Foreign Application Priority Data**

Dec. 18, 1990 [JP] Japan 2-42321
Dec. 18, 1990 [JP] Japan 2-42327

[52] **U.S. Cl.** **D14/106**

[58] **Field of Search** ..... D14/100, 101, 106, 113, D14/115, 124–127; D18/1, 7; 235/145 A, 145 R; 340/700, 706; 341/22, 23; 361/390, 394; 364/708, 709.04

[56] **References Cited**

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Primary Examiner—Wallace R. Burke
Assistant Examiner—Freda S. Nunn
Attorney, Agent, or Firm—Nixon & Vanderhye

[57] **CLAIM**

The ornamental design for a computer, as shown and described.

DESCRIPTION

FIG. **1** is a top front and right side perspective view of a computer according to the present invention, with the top thereof illustrated in an open position;
FIG. **2** is a top plan view thereof;
FIG. **3** is a front elevational view thereof;
FIG. **4** is a right side elevational view thereof, with the left side being the mirror-image thereof;
FIG. **5** is a rear elevational view thereof;
FIG. **6** is a bottom plan view thereof;
FIG. **7** is a top plan view thereof, with the top of the computer in a closed position;
FIG. **8** is a front elevational view of the computer illustrated in FIG. **7**;
FIG. **9** is a right side elevational view thereof, with the left side being a mirror-image thereof;
FIG. **10** is a rear elevational view thereof;
FIG. **11** is a bottom plan view thereof;
FIG. **12** is a top front and right side perspective view of a second embodiment thereof with the top thereof illustrated in and open position;
FIG. **13** is a top plan view thereof;
FIG. **14** is a front elevational view thereof;
FIG. **15** is a right side elevational view thereof, with the left side being the mirror-image thereof;
FIG. **16** is a rear elevational view thereof;
FIG. **17** is a bottom plan view thereof;
FIG. **18** is a top plan view thereof, with the top of the computer illustrated in the closed position;
FIG. **19** is a right side elevational view thereof, with the left side being a mirror-image thereof; and,
FIG. **20** is a bottom plan view thereof.
The front elevational view of the second embodiment of FIGS. **12–20** hereof is identical to the front elevational view as illustrated in FIG. **8**, with the top of the computer in the closed position.

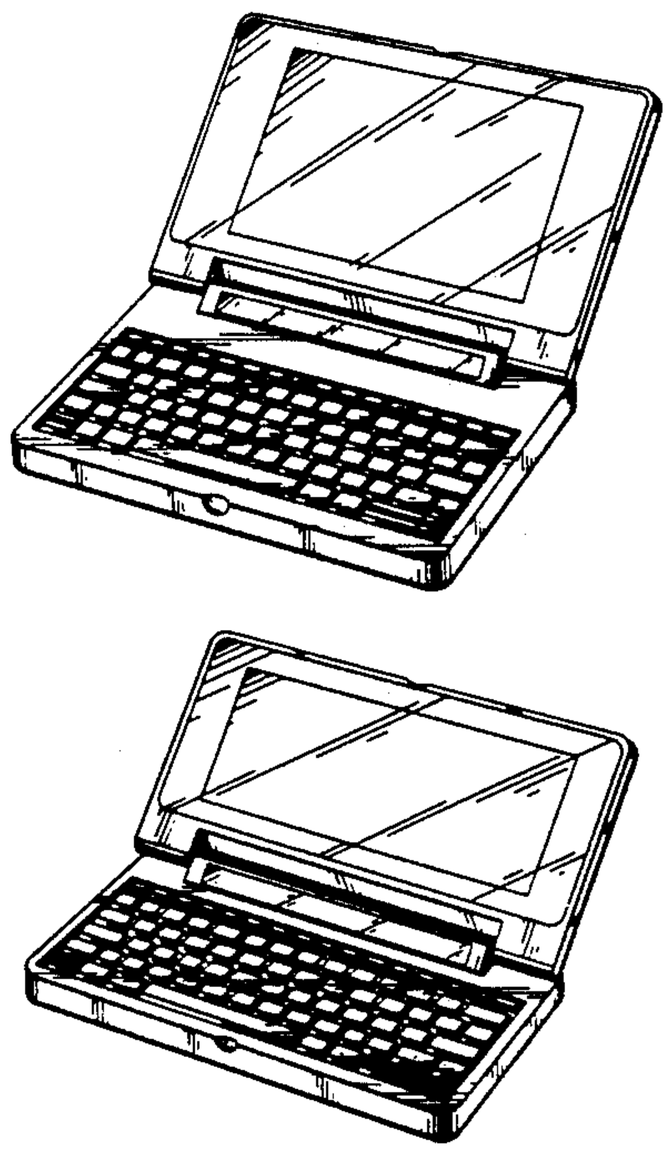
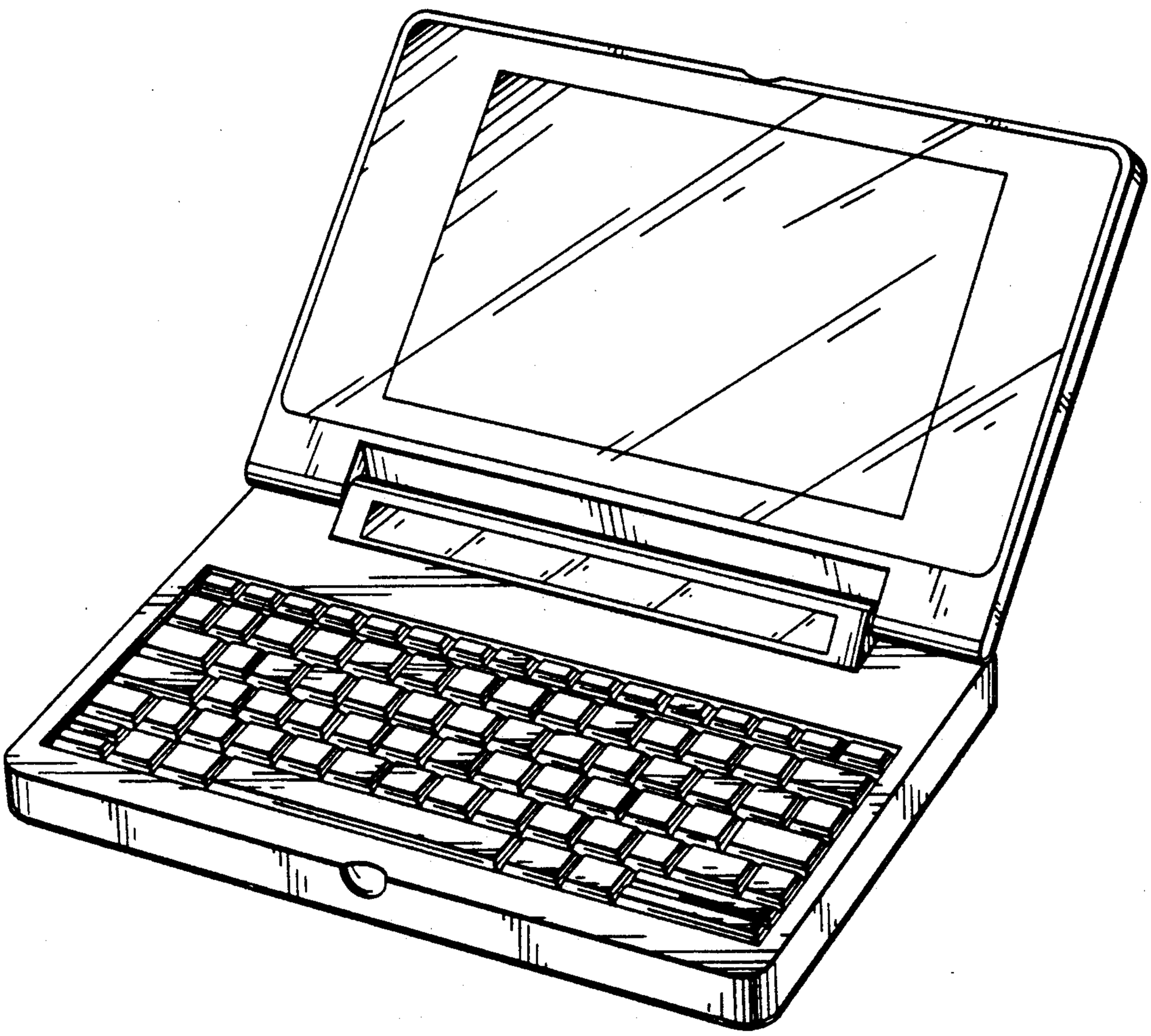


FIG. 1



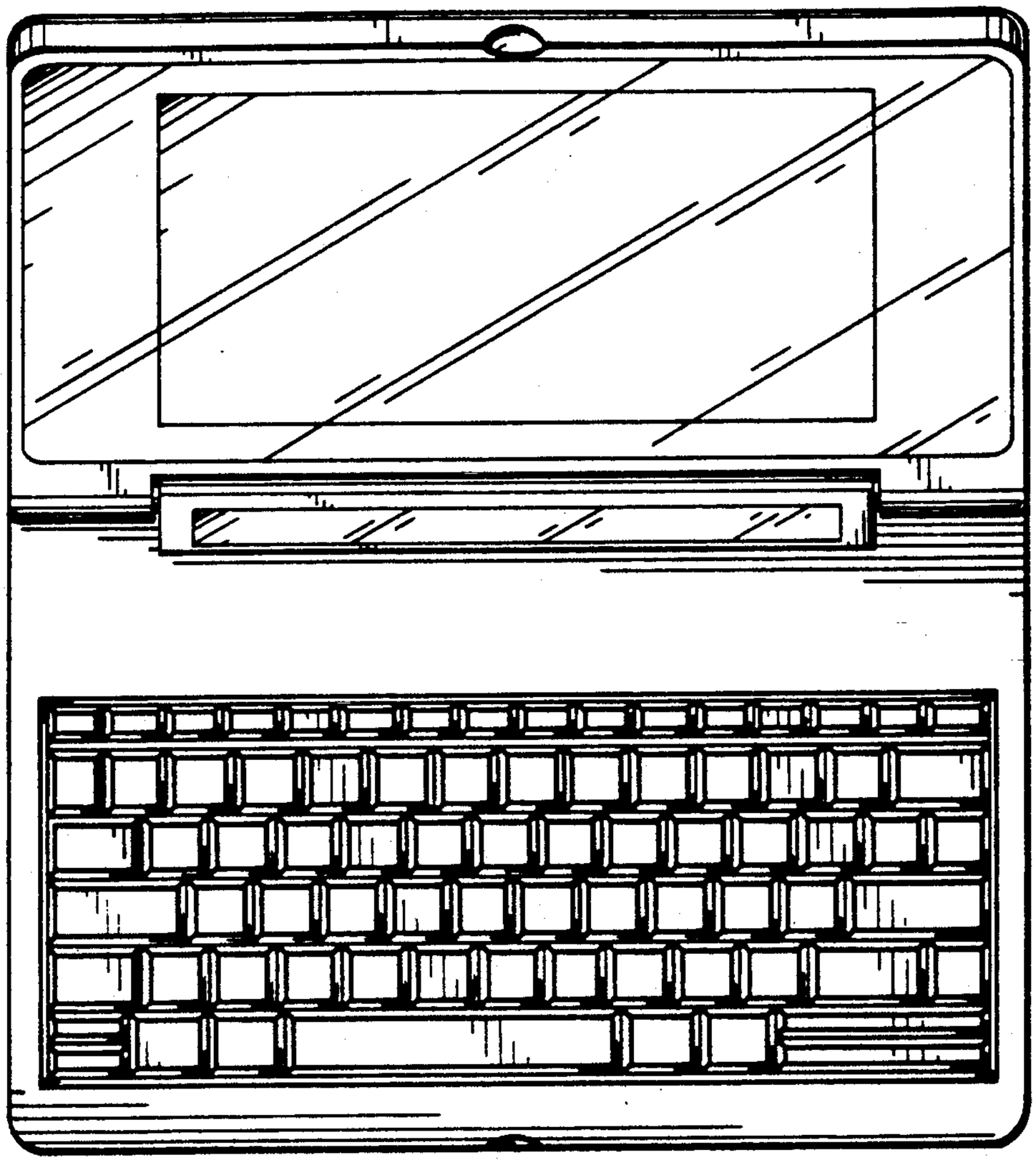


FIG. 2

FIG. 3

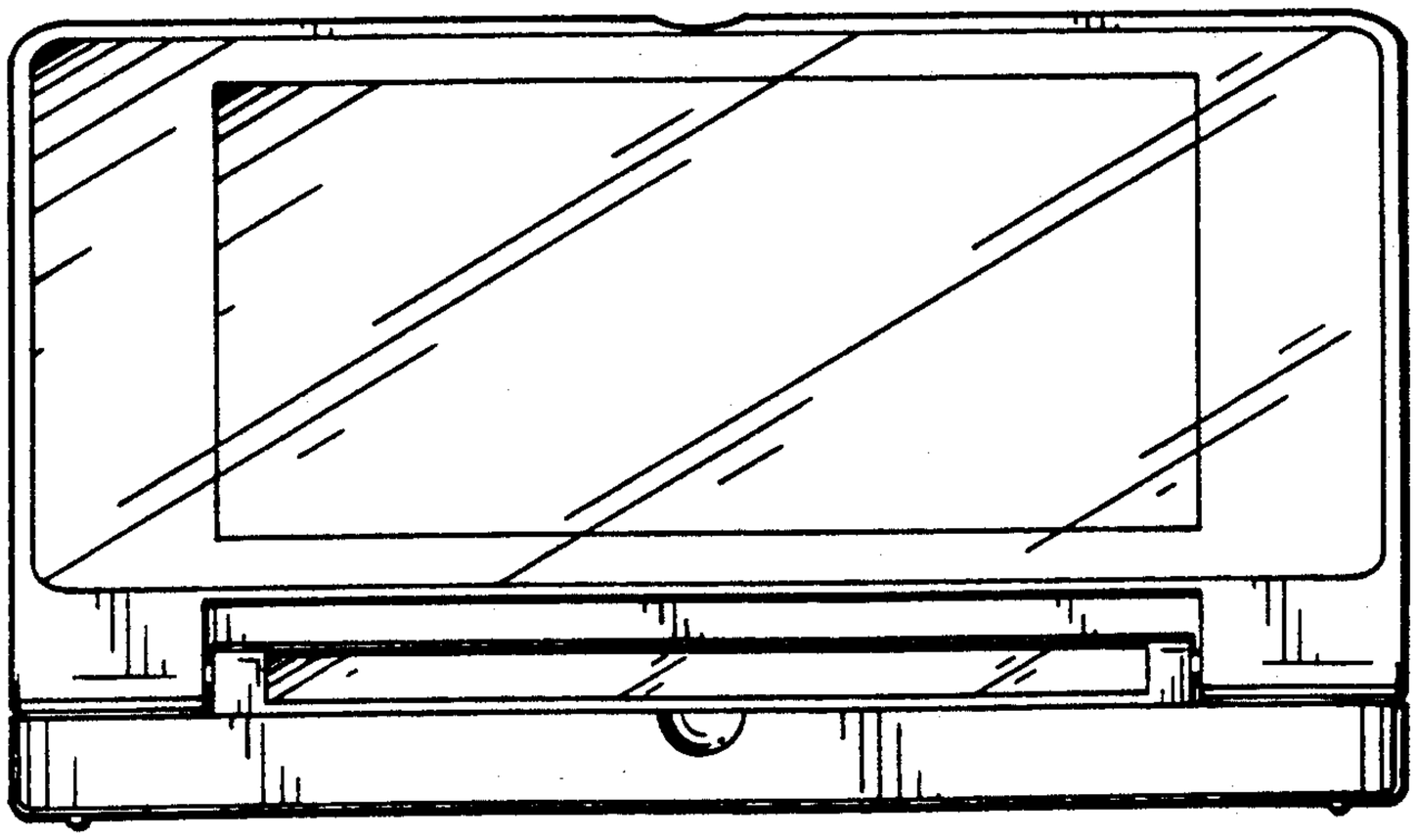


FIG. 4

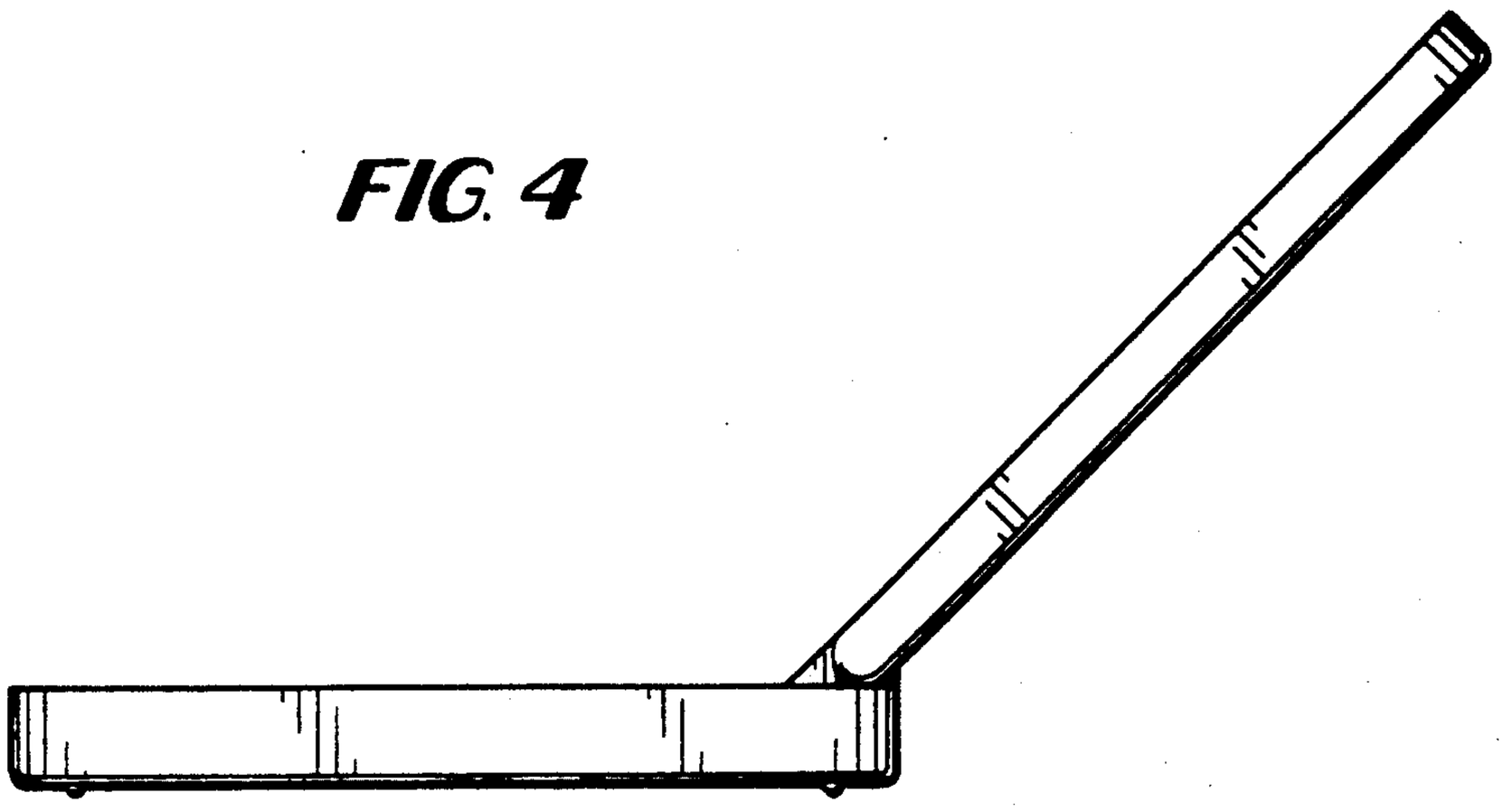
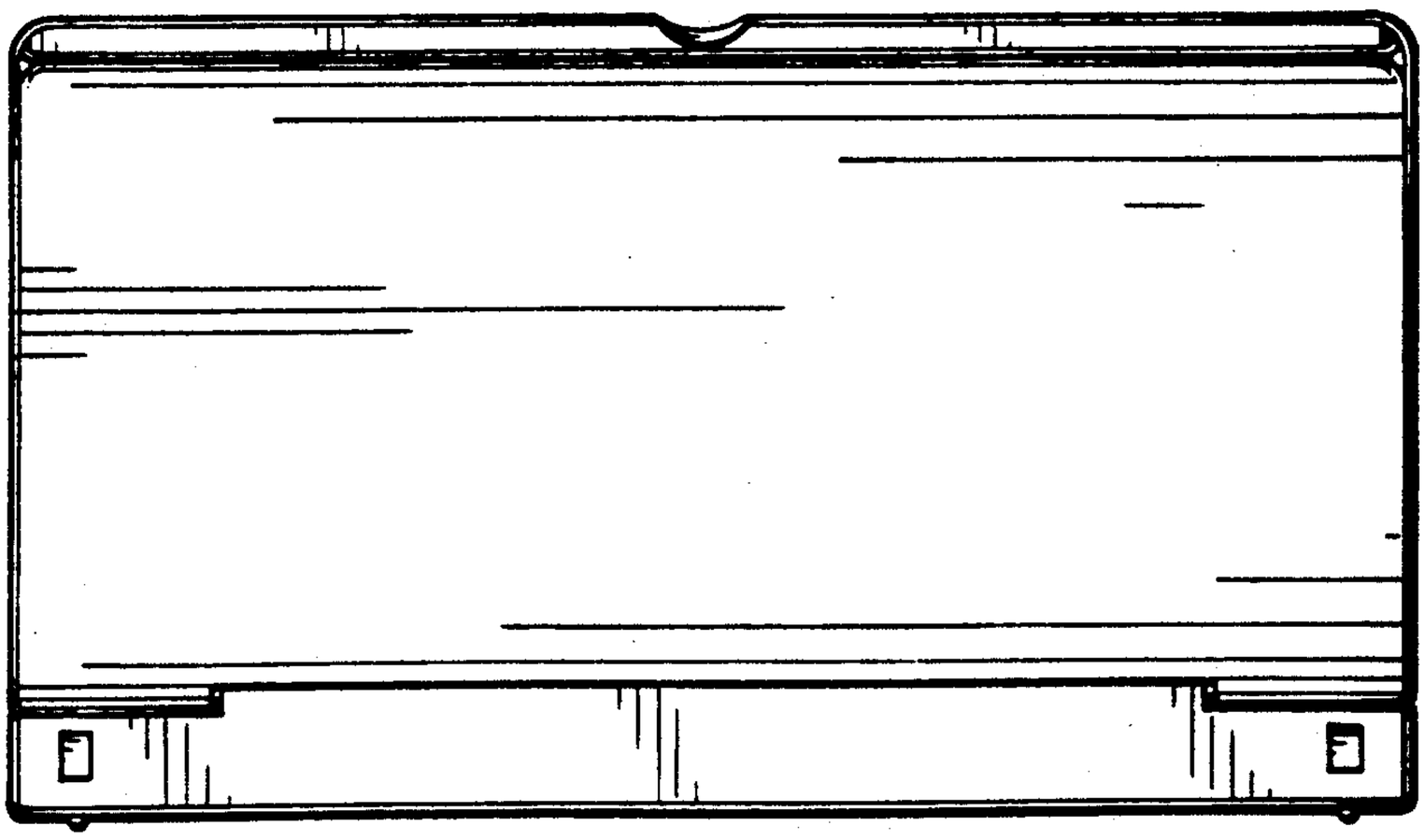


FIG. 5



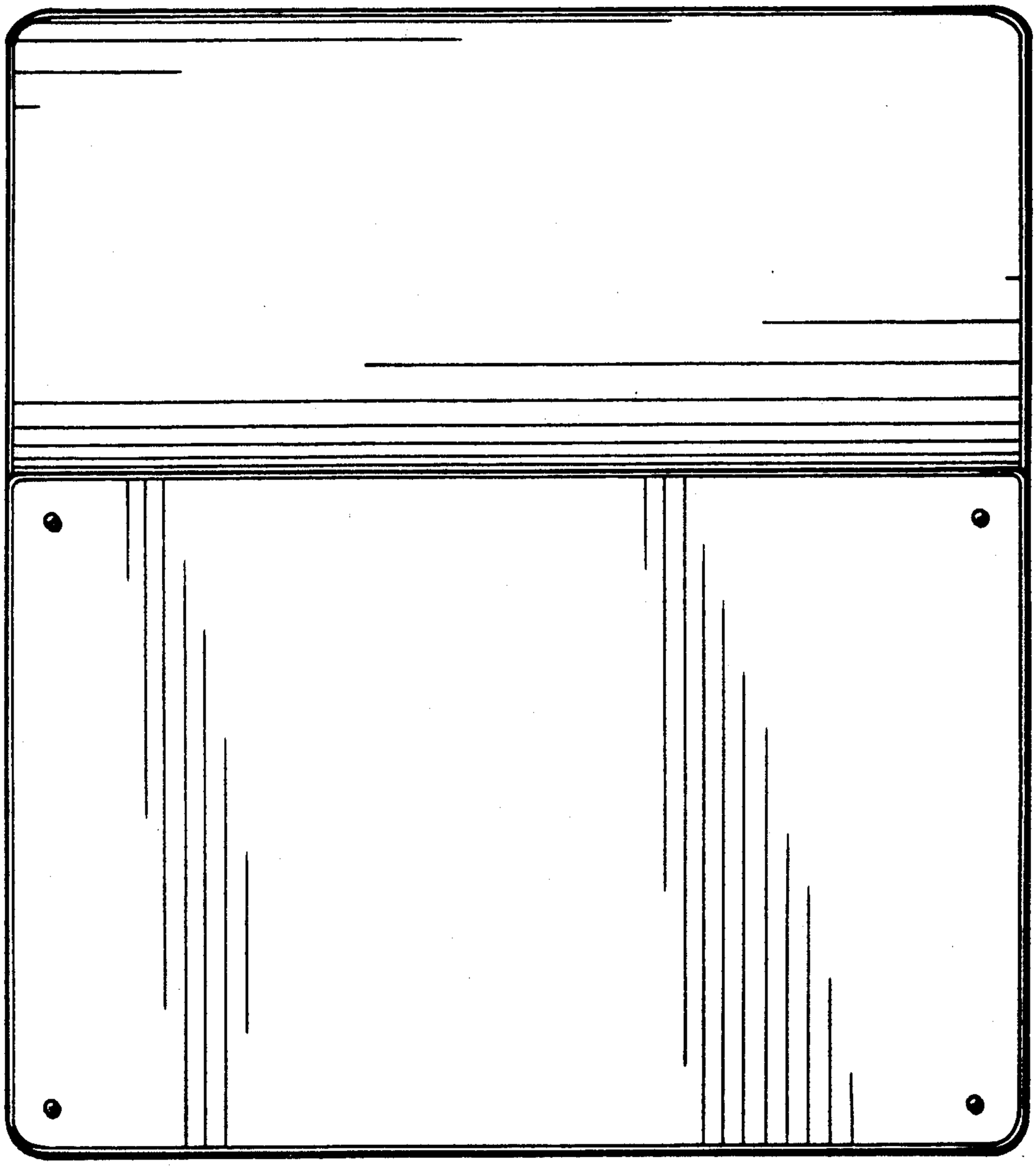


FIG. 6

FIG. 7

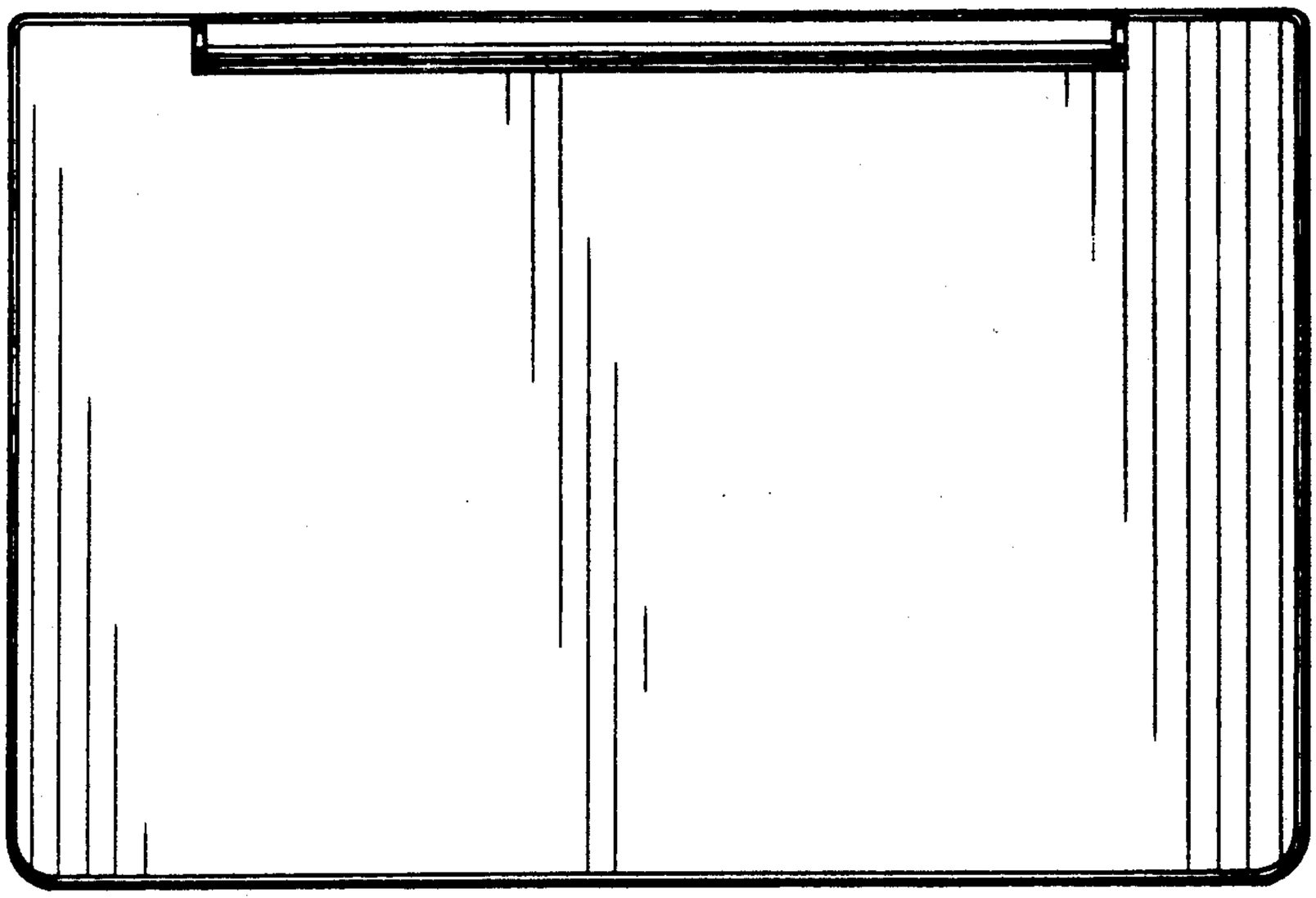


FIG. 8

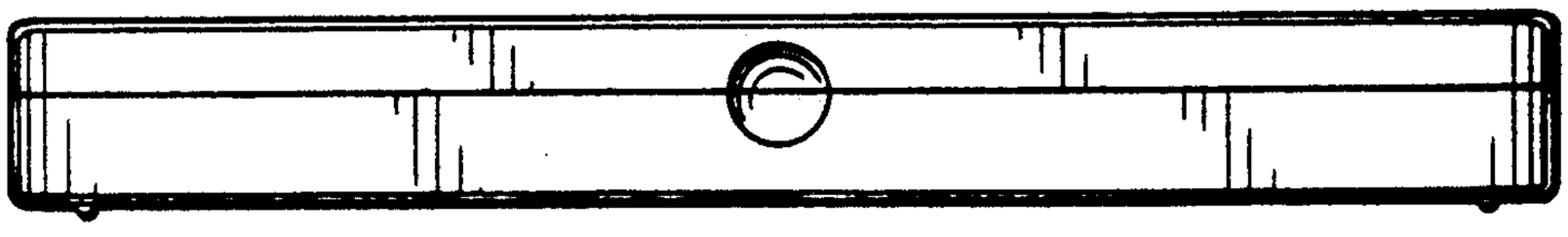
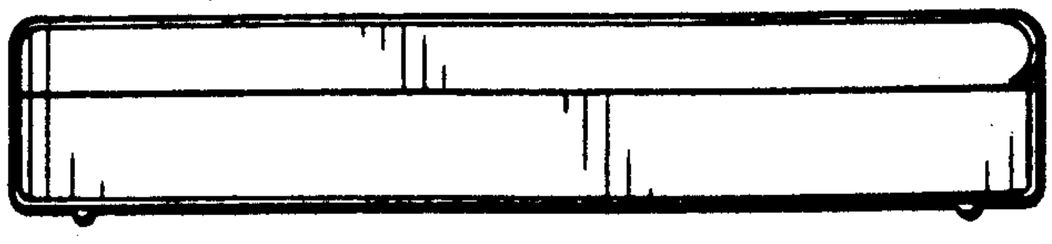


FIG. 9



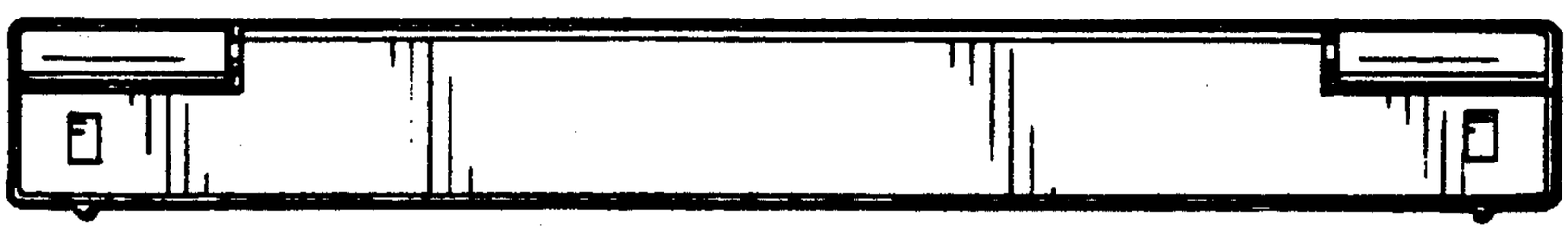


FIG. 10

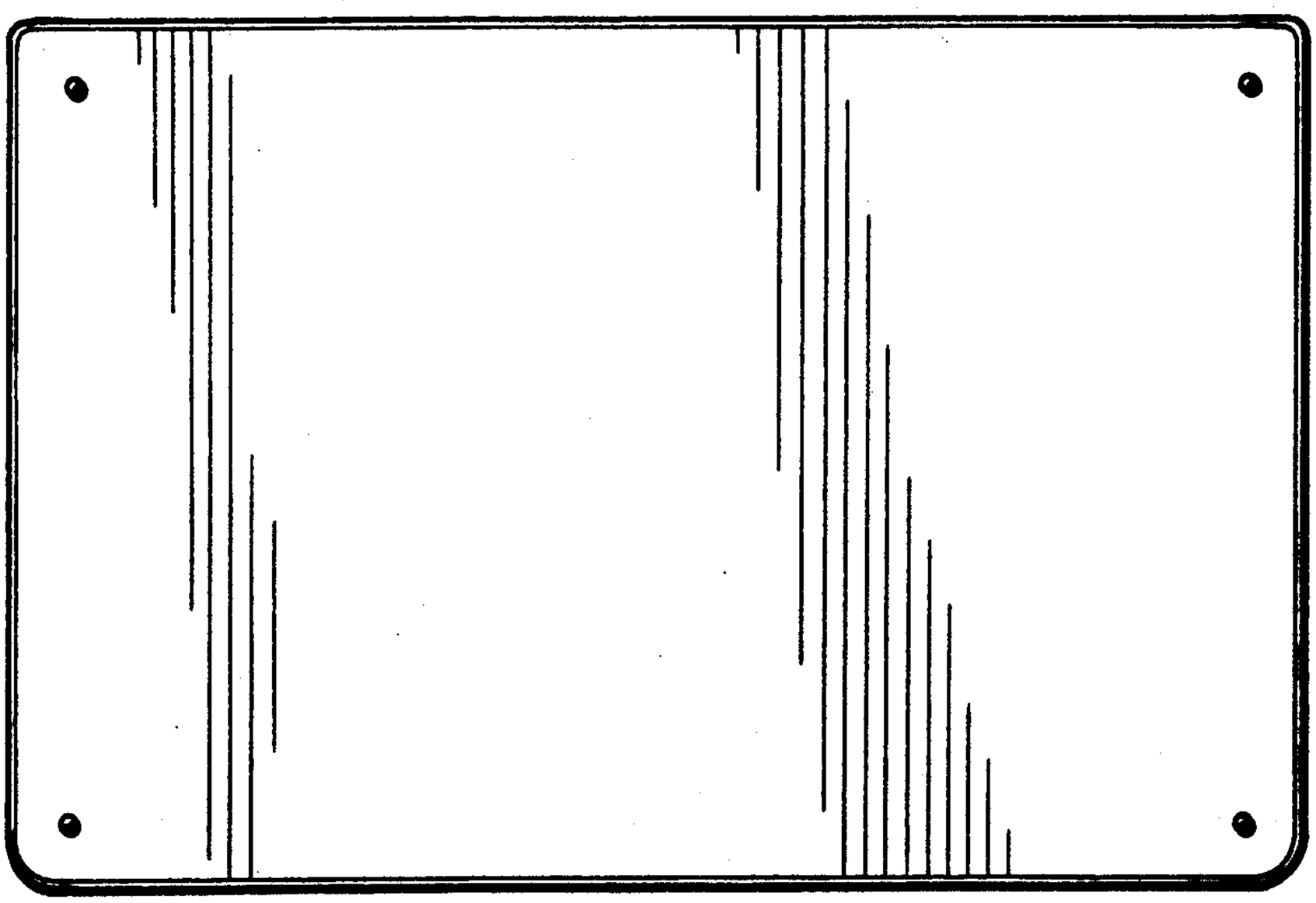
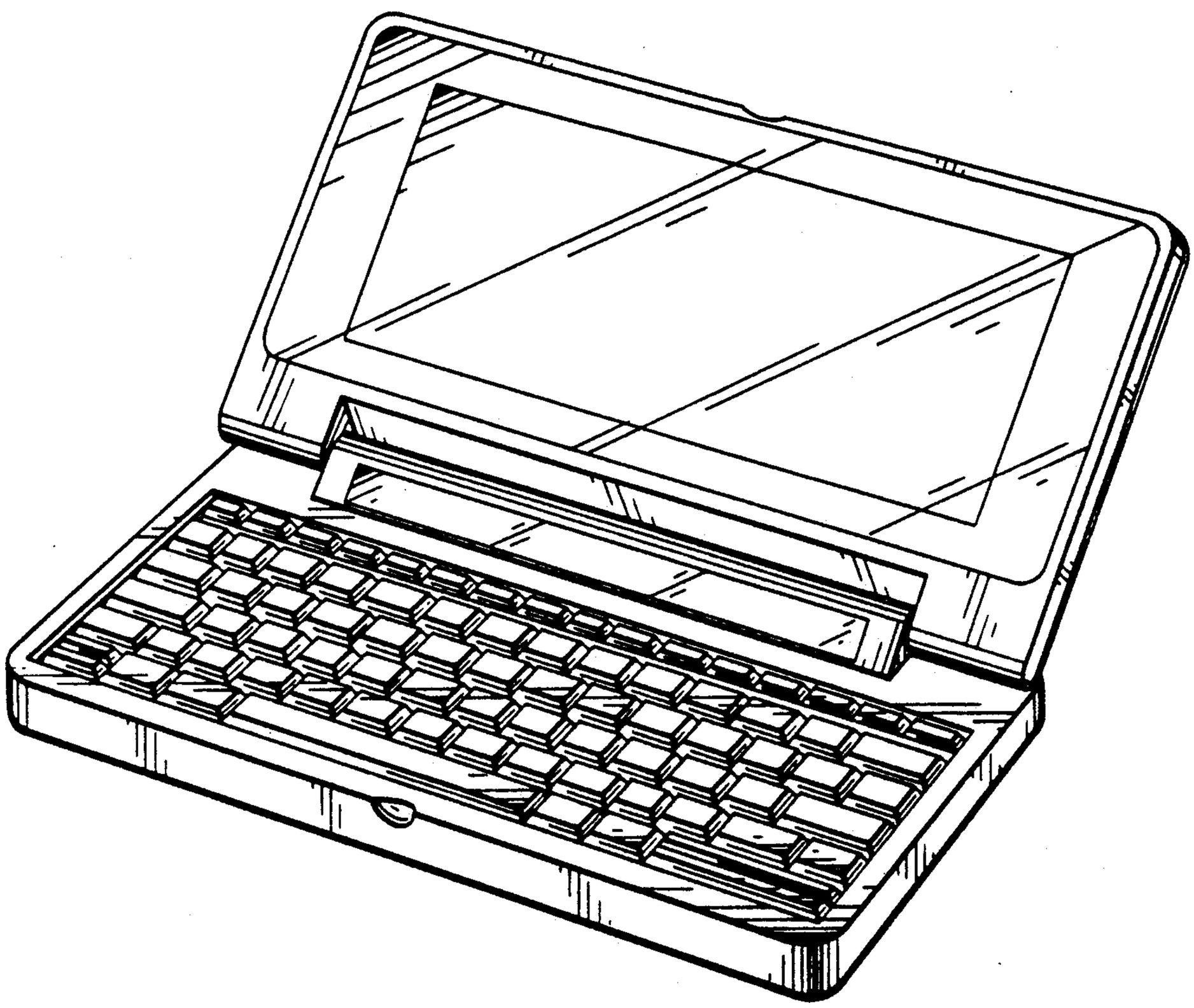


FIG. 11

FIG. 12



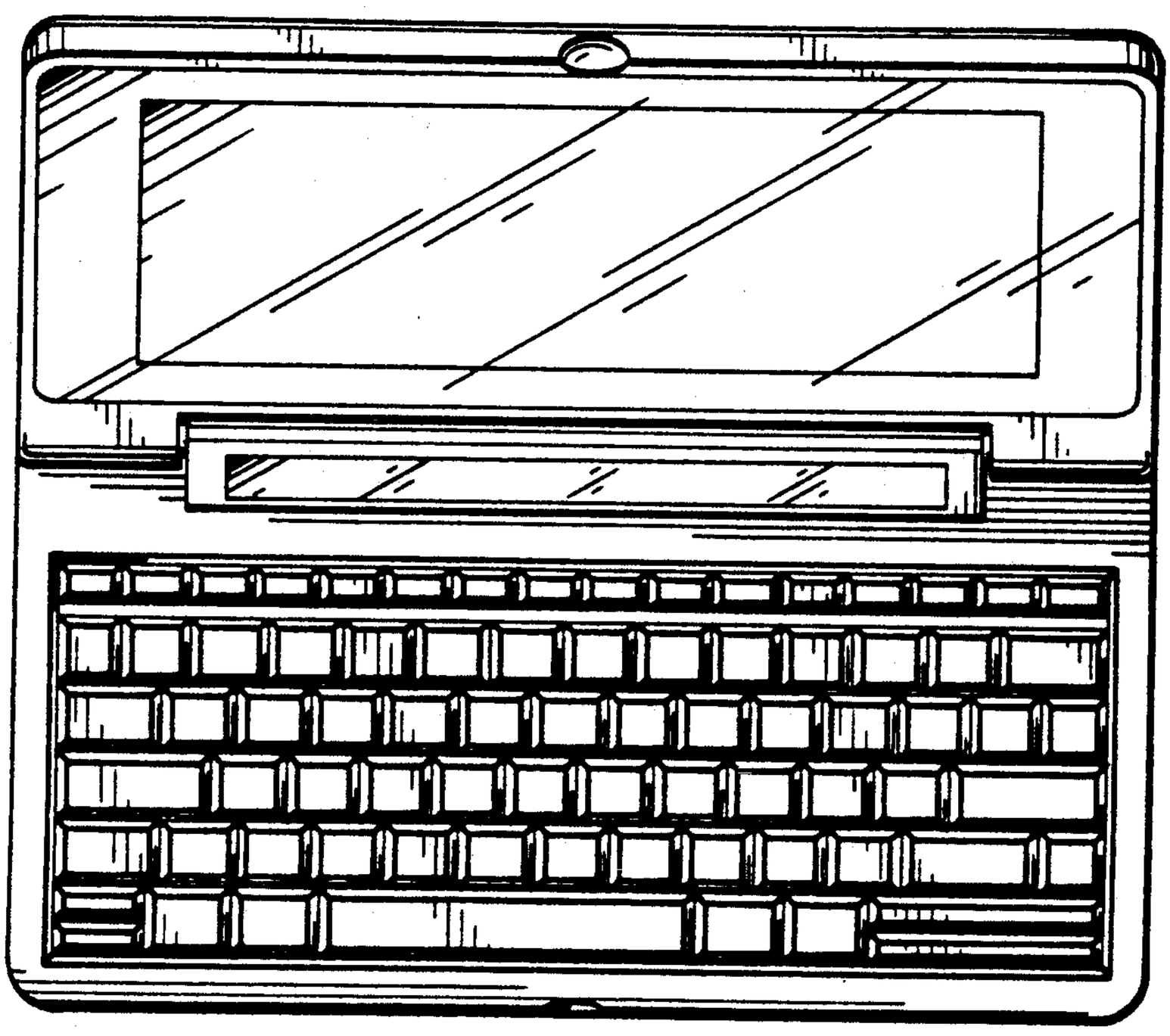
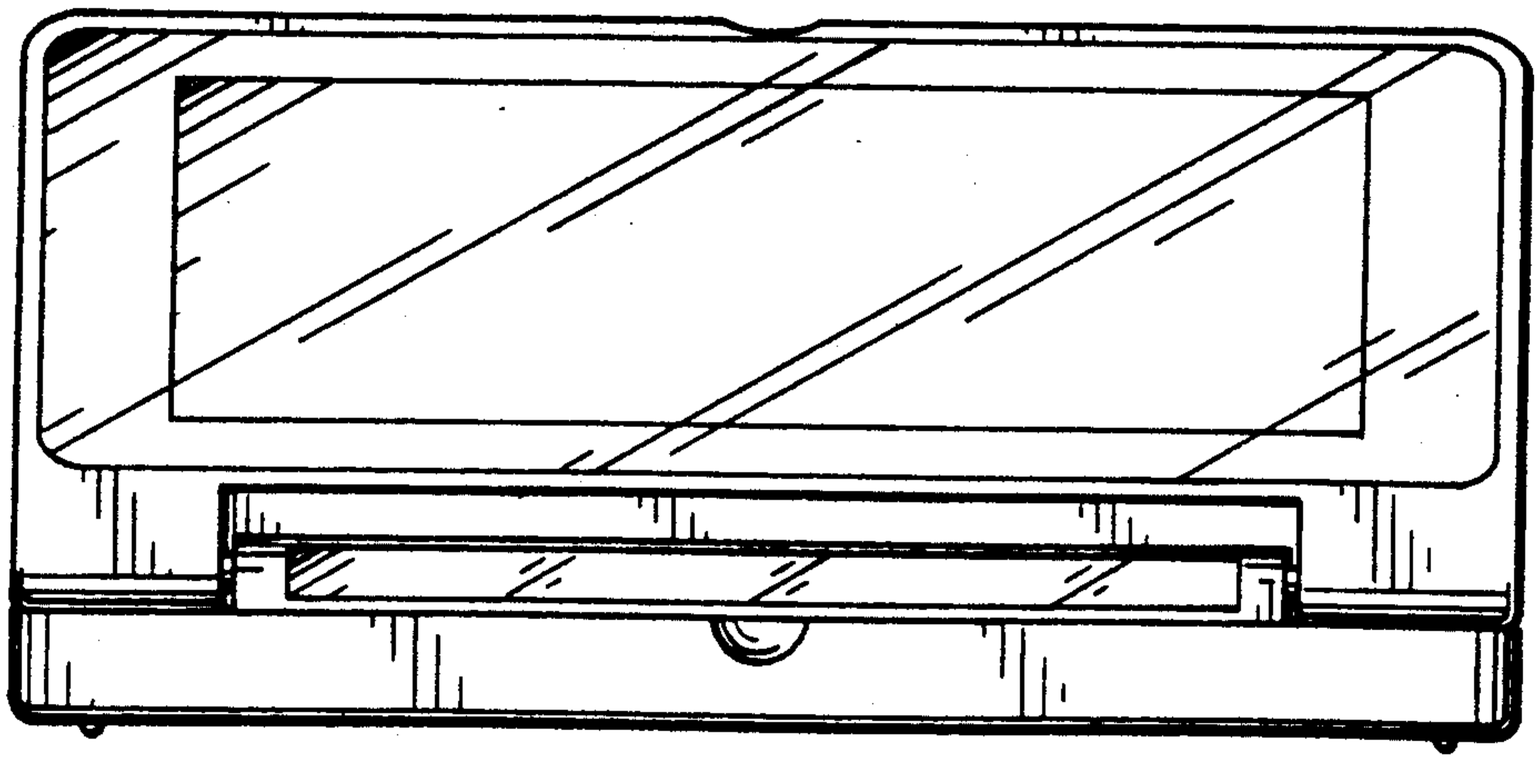


FIG. 13

FIG. 14



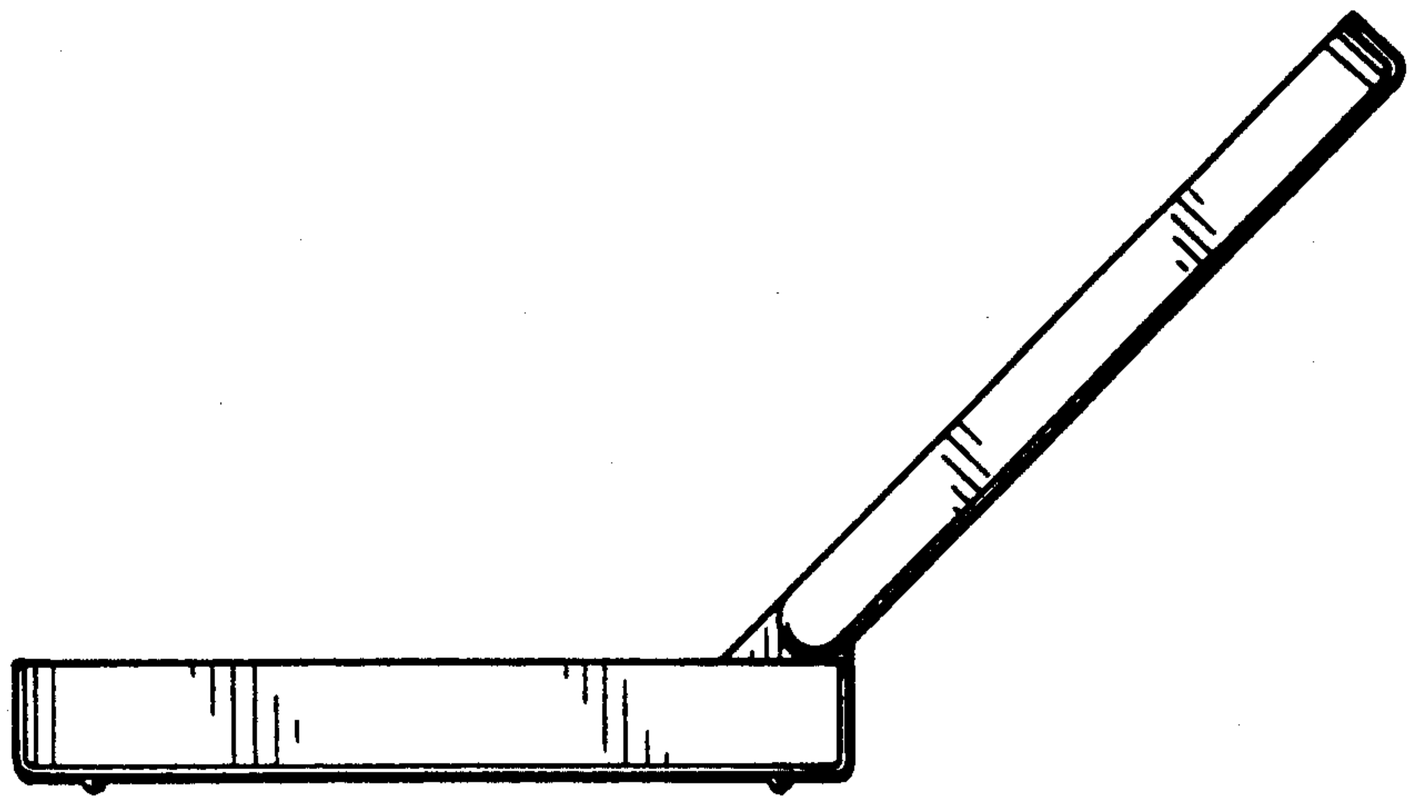


FIG. 15

FIG. 16

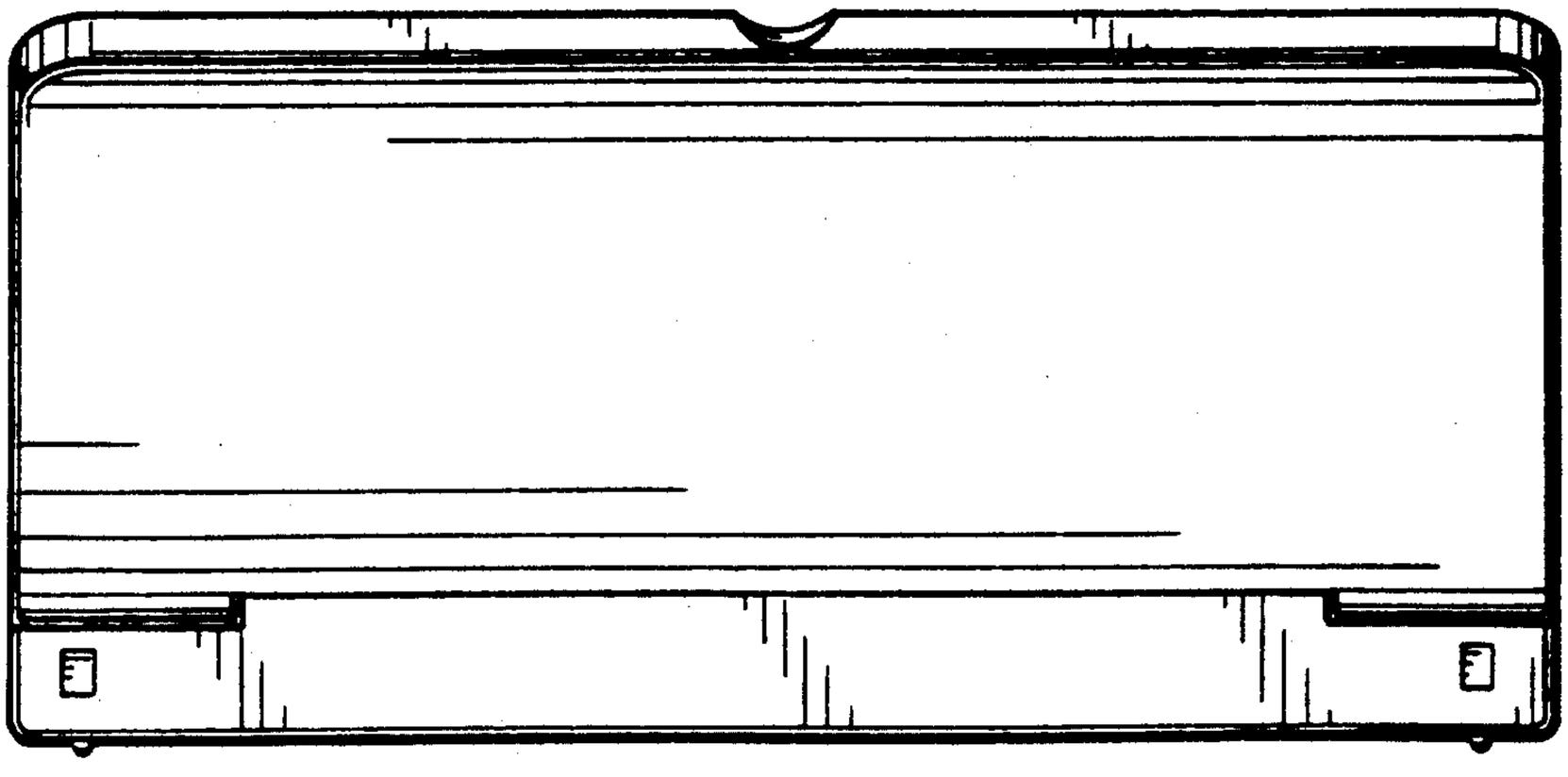


FIG. 17

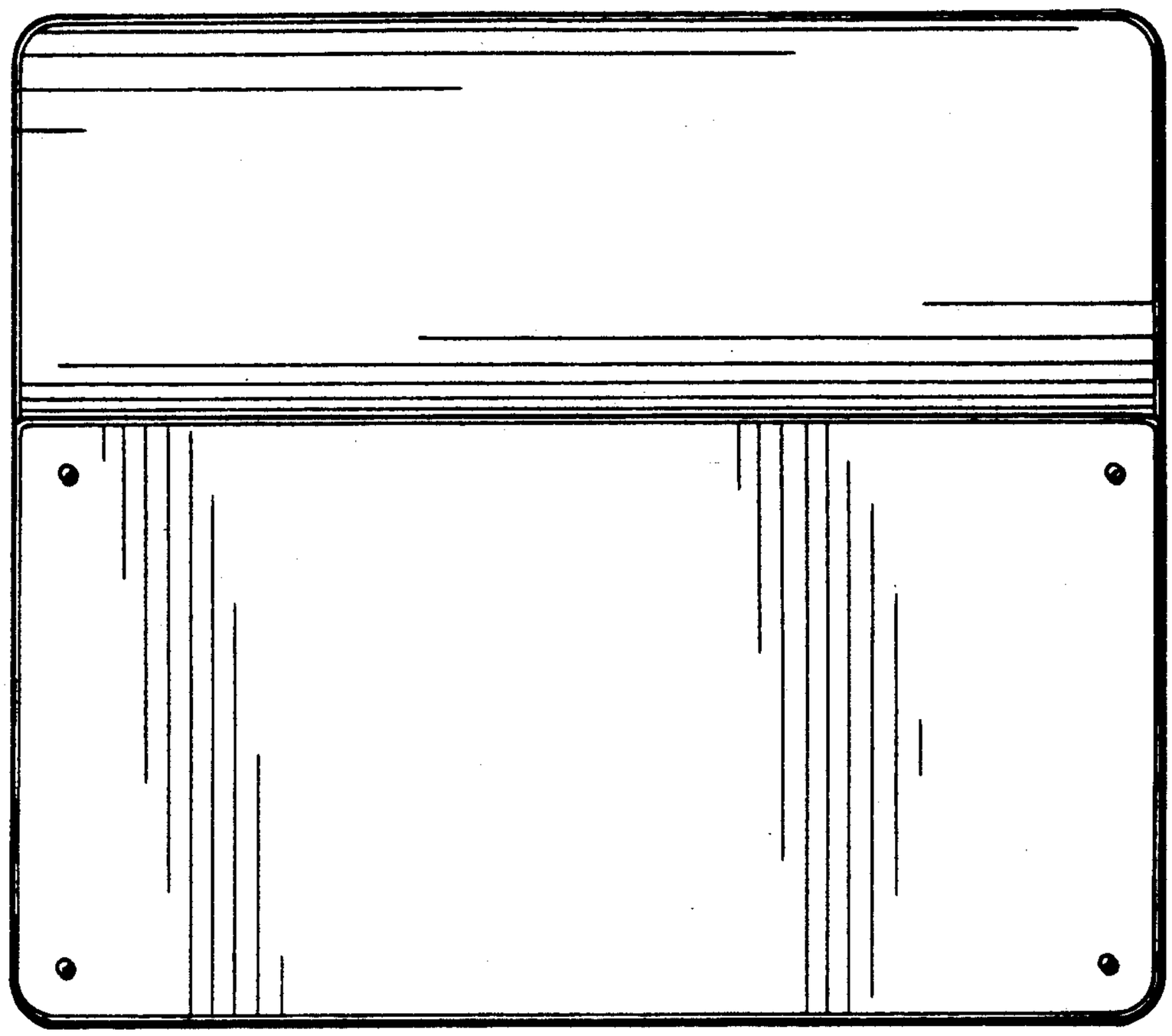


FIG. 18

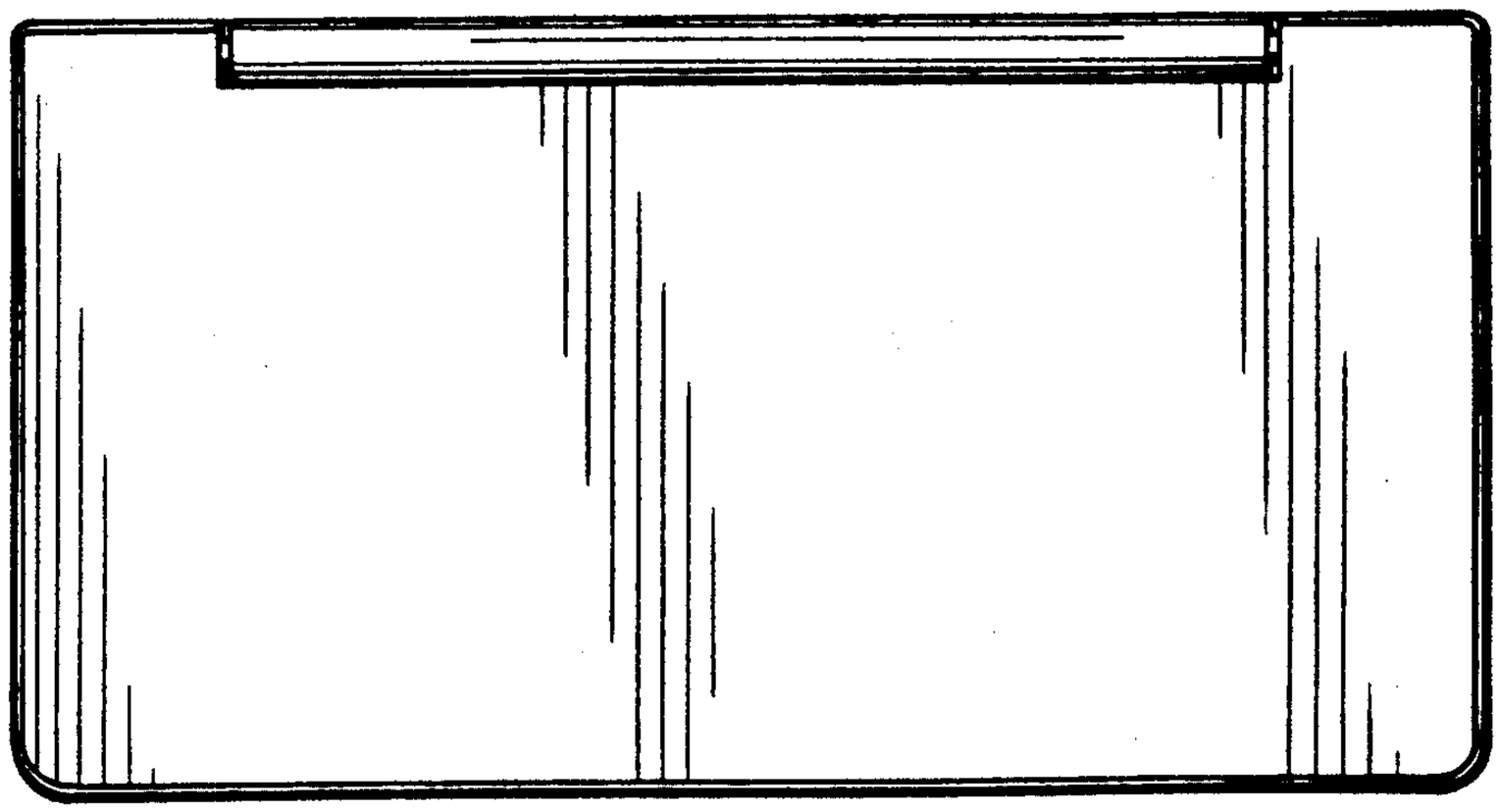


FIG. 19

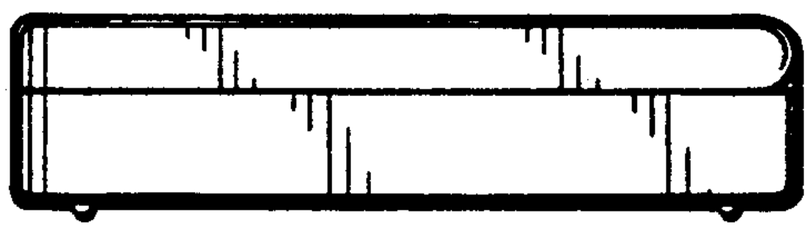


FIG. 20