



US00D331749S

# United States Patent [19]

[11] Patent Number: **Des. 331,749**

Mizusugi et al.

[45] Date of Patent: **\*\* Dec. 15, 1992**

[54] **MEMORY EXPANSION UNIT FOR A COMPUTER**

D. 310,998 10/1990 Lee et al. .... D14/114 X  
D. 313,011 12/1990 Searle et al. .... D14/100  
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[\*\*] Term: **14 Years**

[21] Appl. No.: **584,184**

[22] Filed: **Sep. 18, 1990**

### [57] CLAIM

### [30] Foreign Application Priority Data

Mar. 20, 1990 [JP] Japan ..... 2-9339

The ornamental design for a memory expansion unit for a computer, as shown and described.

[52] U.S. Cl. .... **D14/107**

### DESCRIPTION

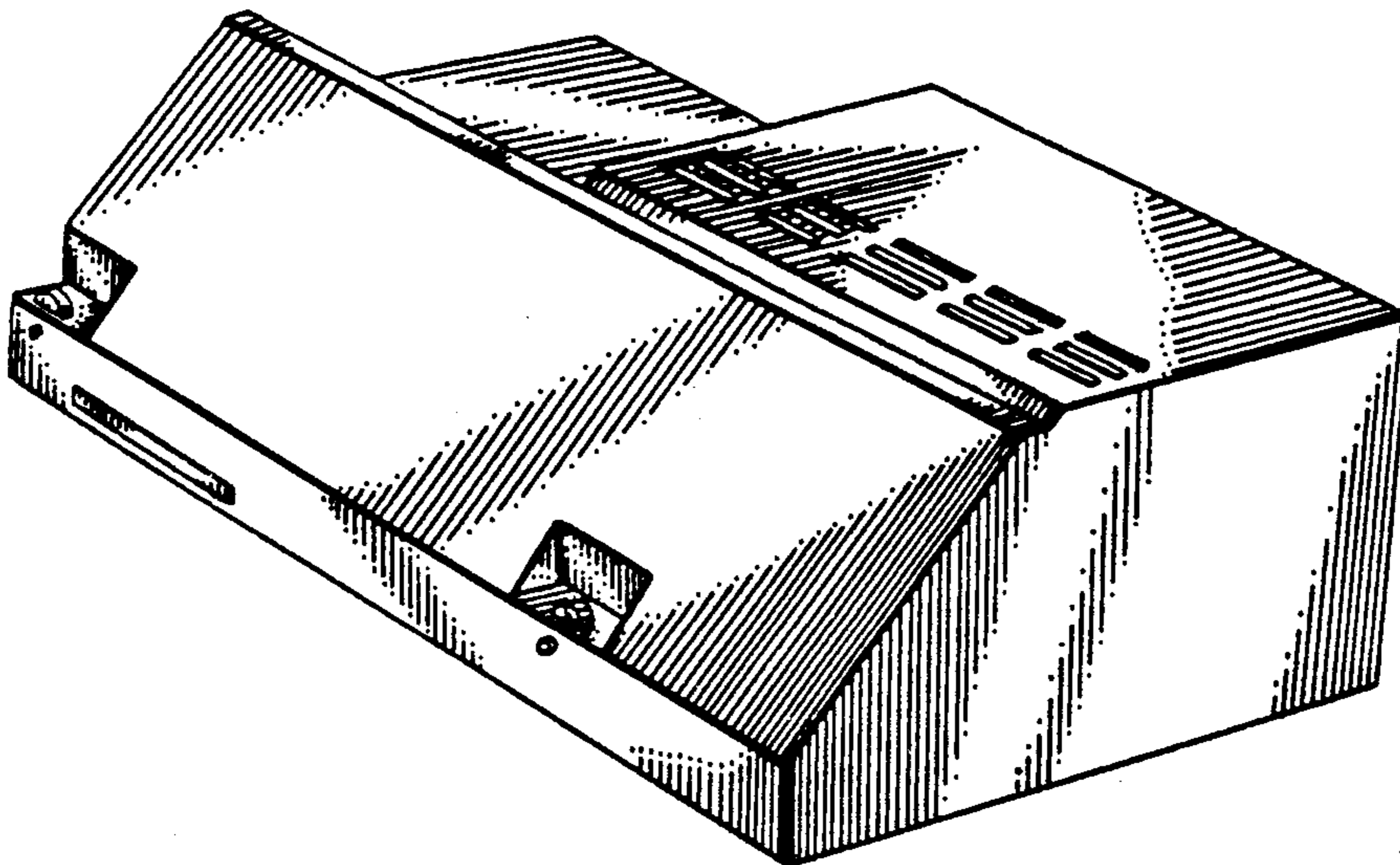
[58] Field of Search ..... D14/100, 102, 107, 114; 360/97-99, 132-133; 361/334, 380-381, 390-395; 200/5 A, 5 R, 6 A, 6 R; D13/184, 199

FIG. 1 is a perspective view of a memory expansion unit for a computer showing our new design; FIG. 2 is a front elevational view thereof; FIG. 3 is a rear elevational view thereof; FIG. 4 is a top plan view thereof; FIG. 5 is a bottom plan view thereof; FIG. 6 is a right side elevational view thereof; and, FIG. 7 is a left side elevational view thereof.

### [56] References Cited

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D. 293,245 12/1987 Hujimon et al. .... D14/100  
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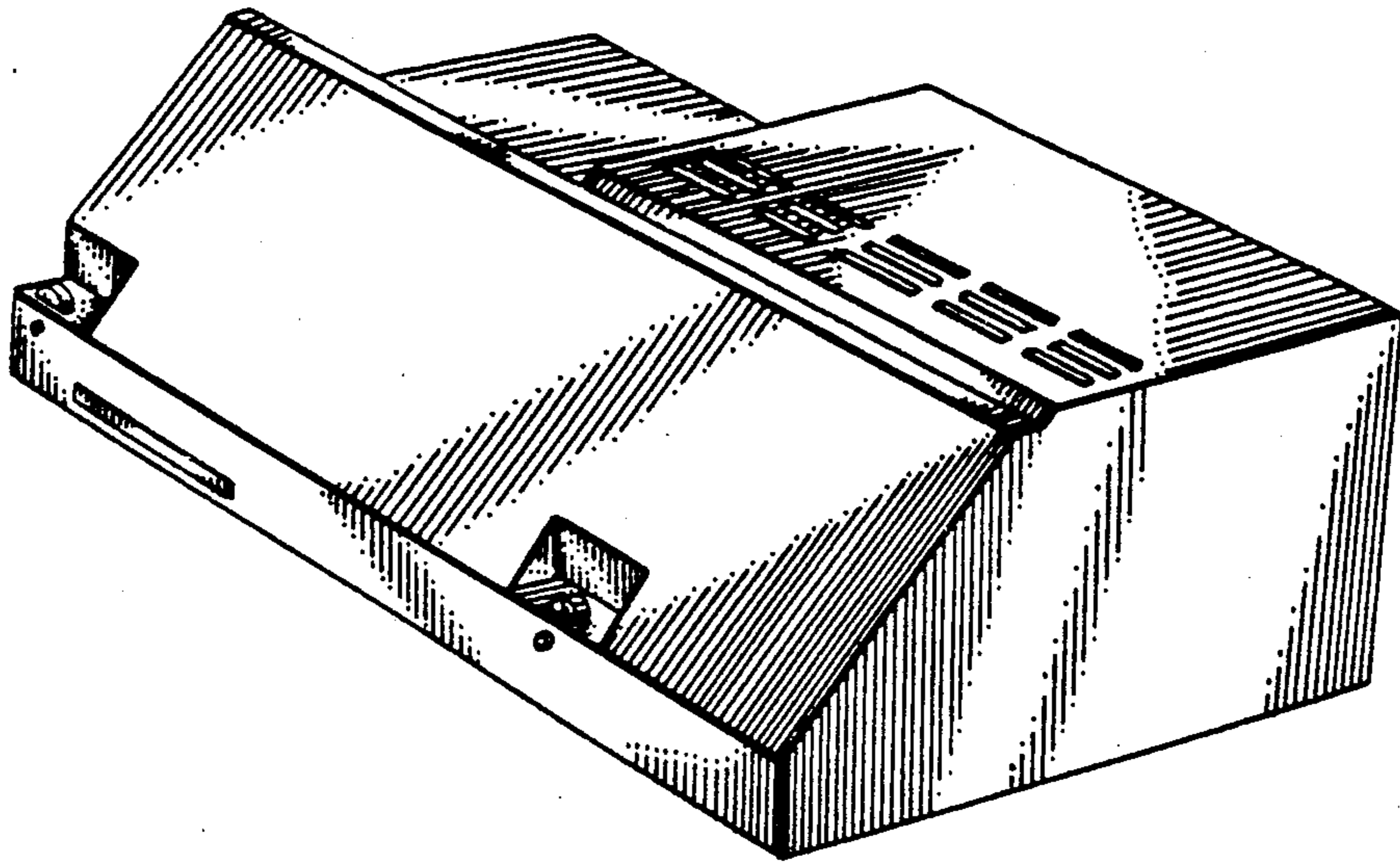


FIG. -1

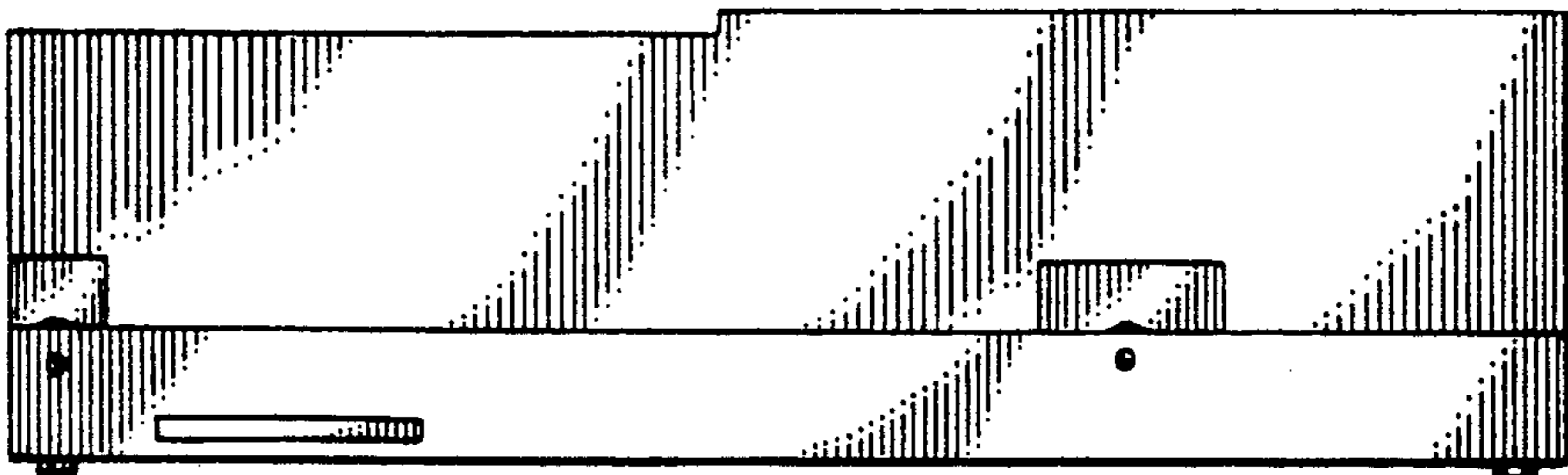


FIG. -2

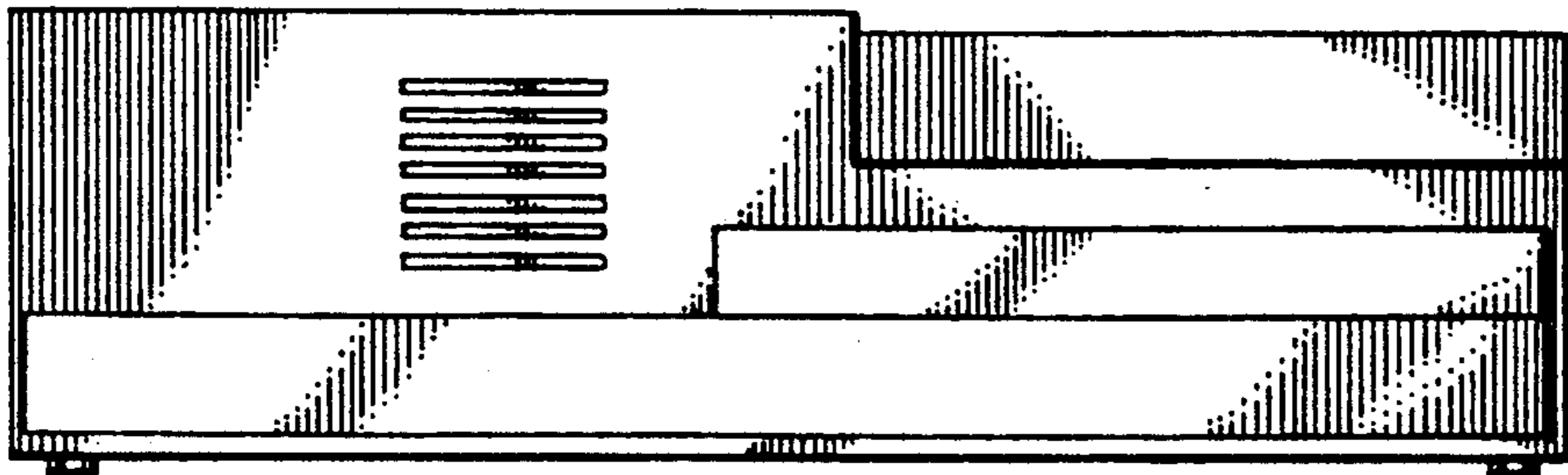


FIG. -3

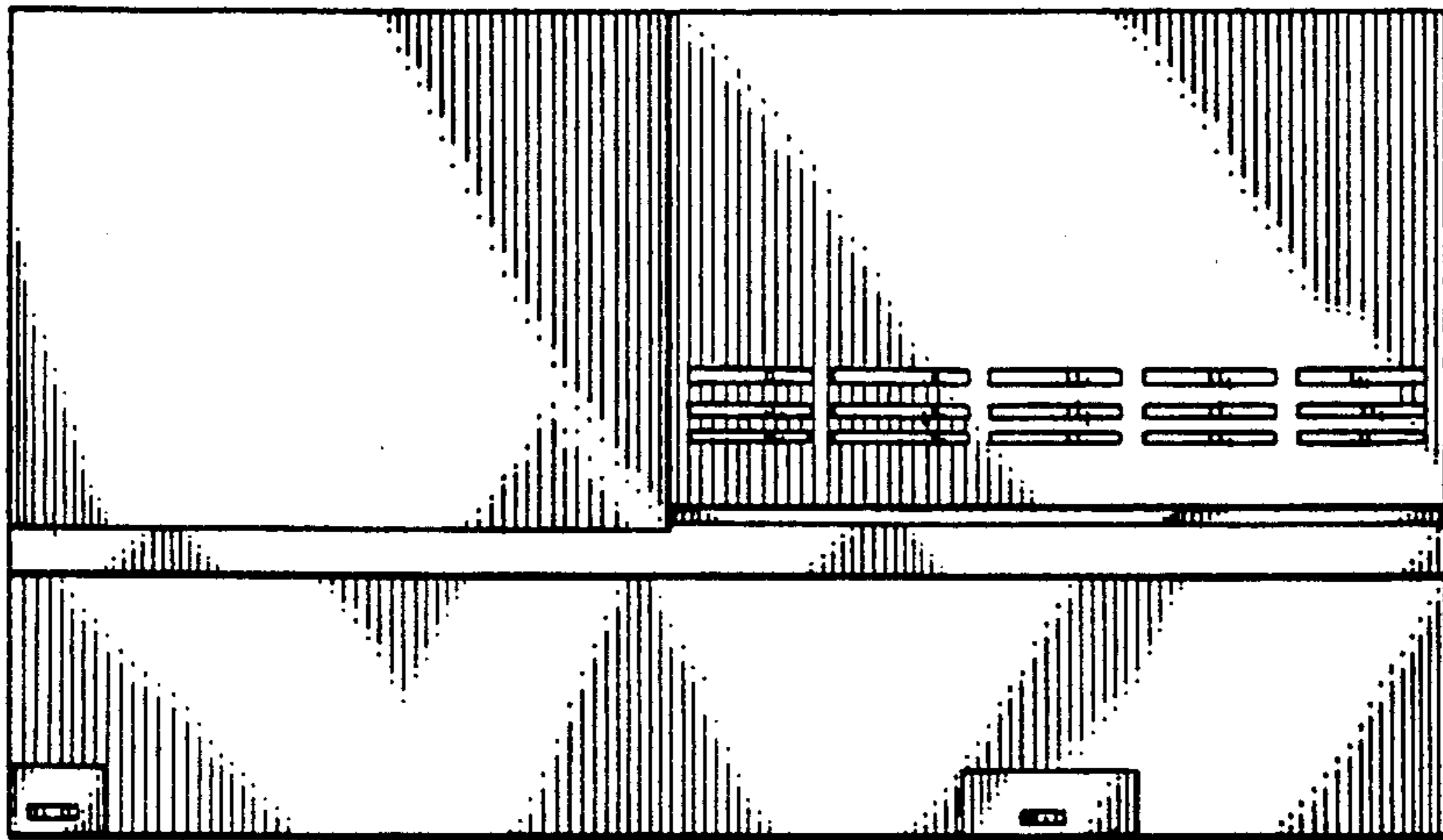


FIG.-4

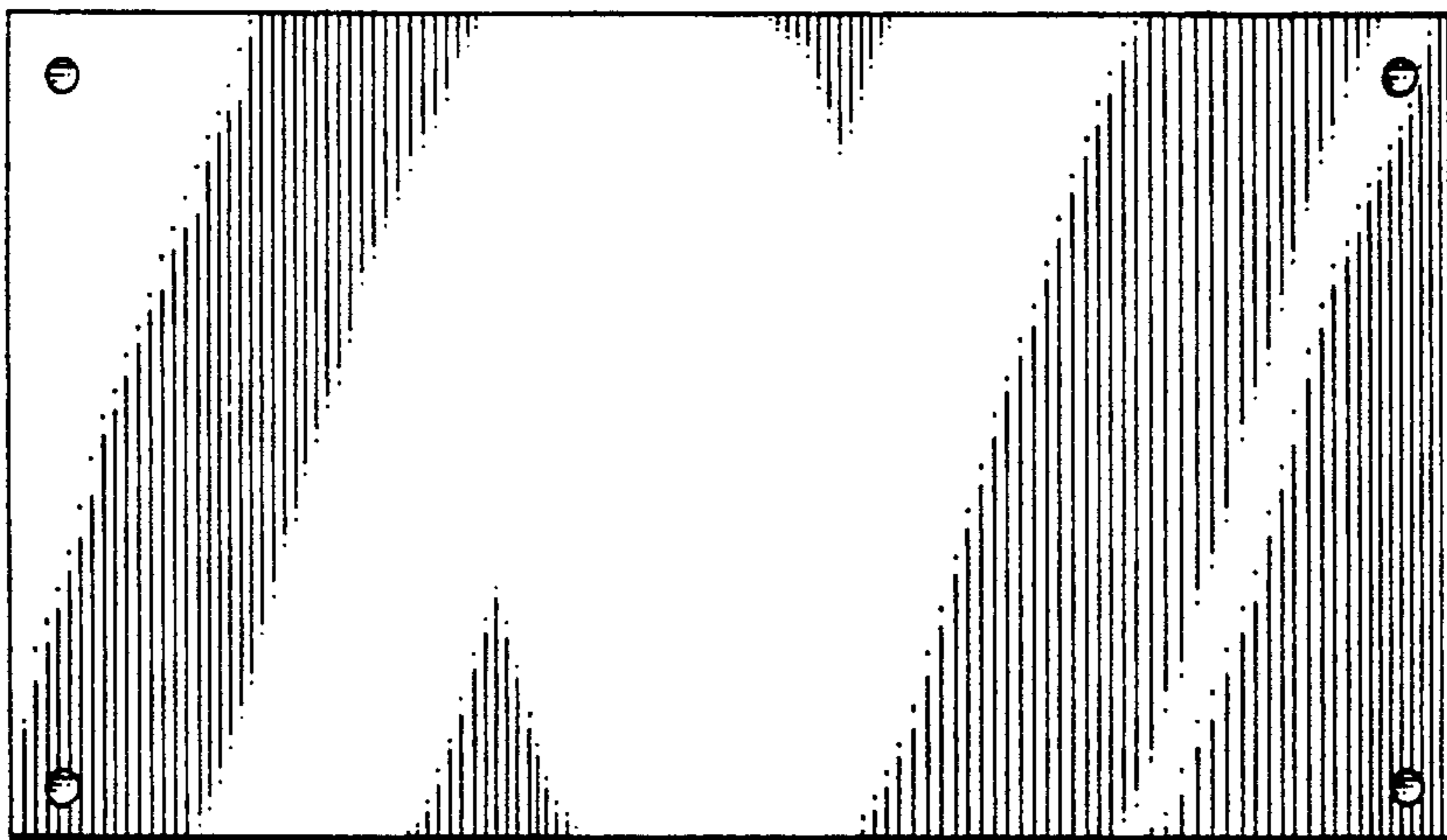


FIG.-5

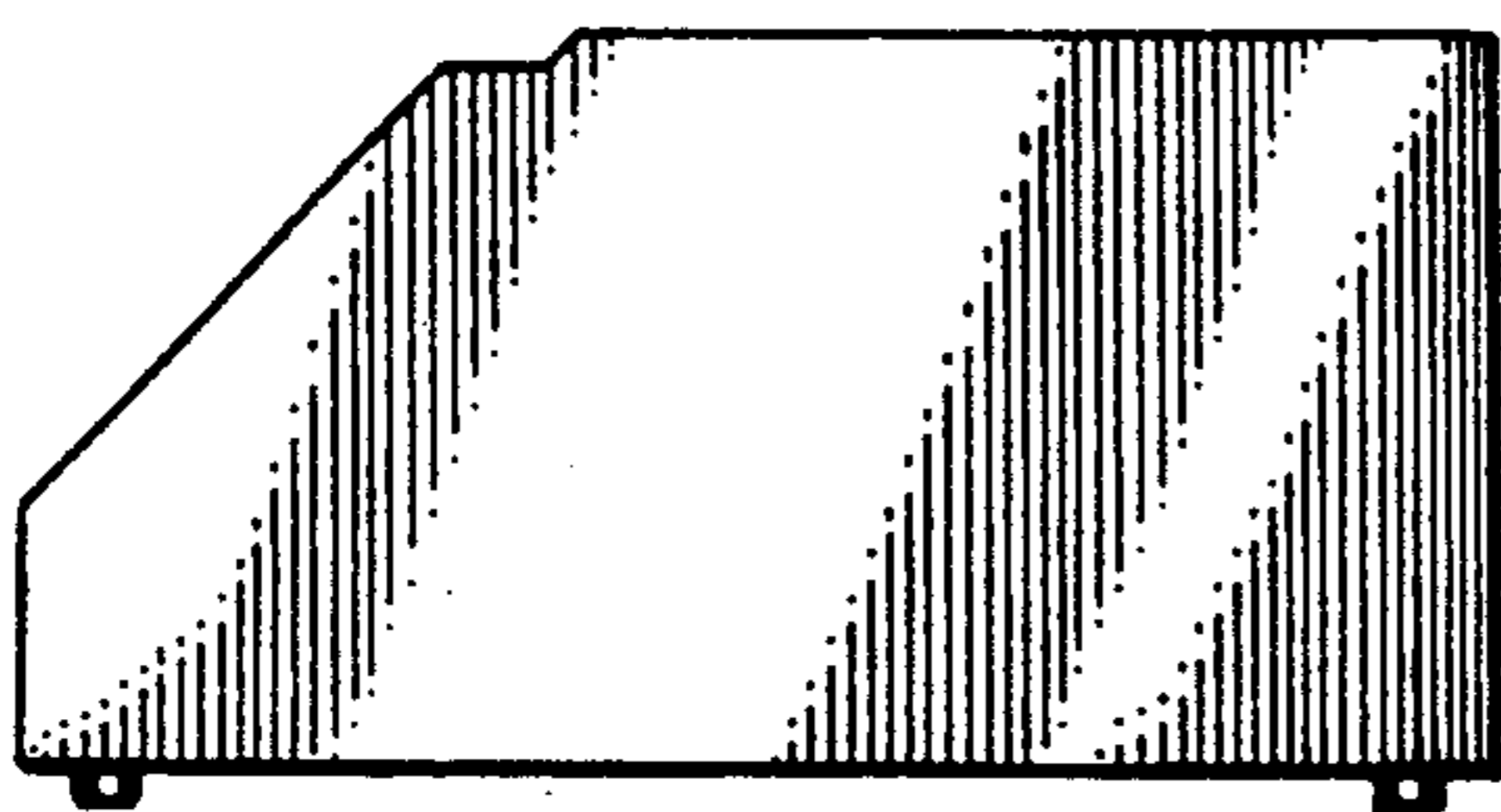


FIG.-6

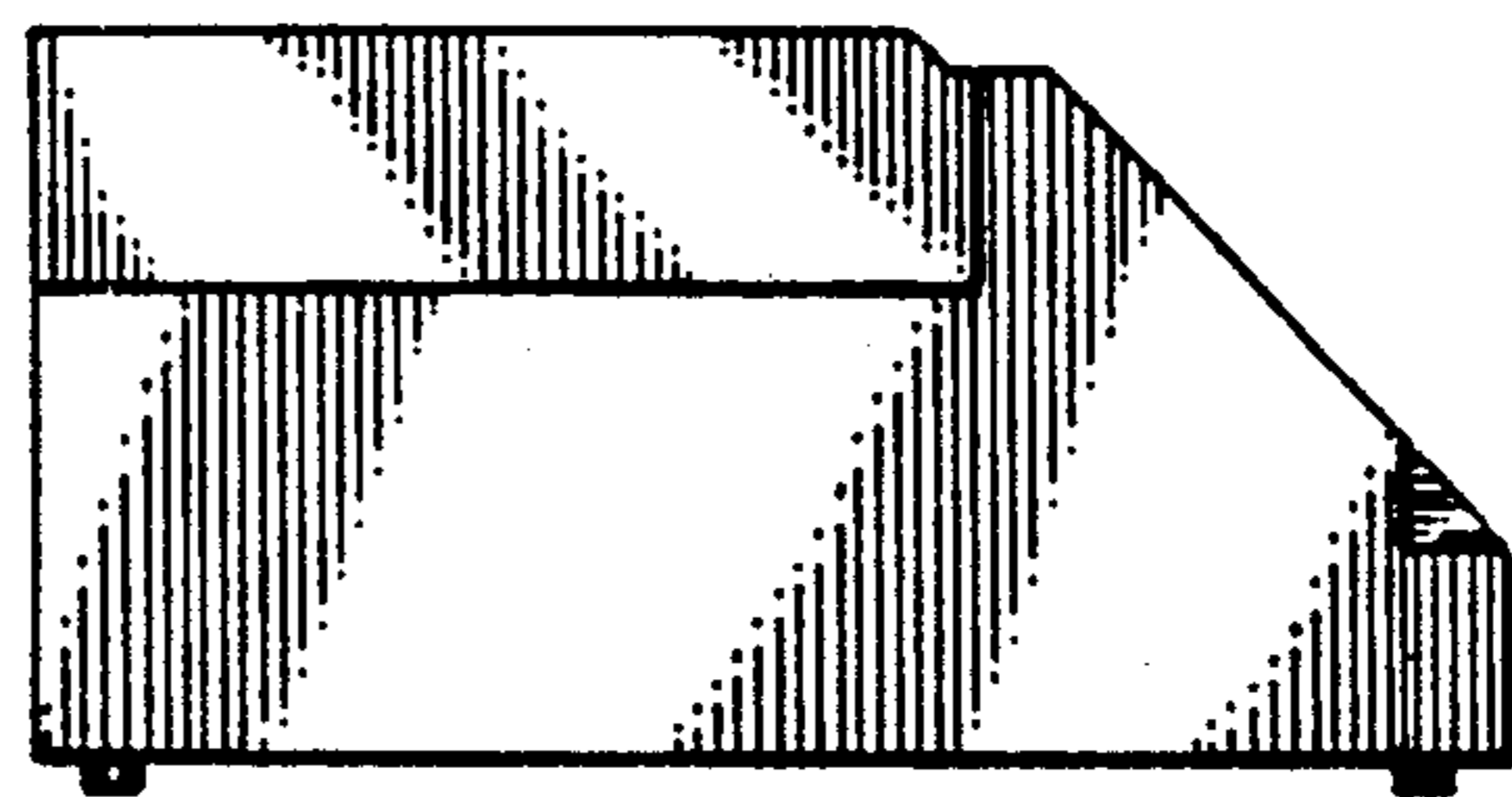


FIG.-7