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United States Patent [19]

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Ishida

[45] Date of Patent: **** Sep. 29, 1992**

[54] INPUT/OUTPUT UNIT OF SEQUENCE CONTROLLER

4,152,750	5/1979	Bremenour et al.	361/393 X
4,672,511	6/1987	Meusel et al.	361/380
4,920,453	4/1990	Onose et al.	361/394 X

[75] Inventor: **Katsuhiko Ishida, Osaka, Japan**

[73] Assignee: **Sharp Corporation, Osaka, Japan**

[**] Term: **14 Years**

[21] Appl. No.: **490,974**

[22] Filed: **Mar. 8, 1990**

[30] Foreign Application Priority Data

Sep. 13, 1989 [JP] Japan 1-33566

[52] U.S. Cl. **D13/162**

[58] Field of Search D13/123, 162, 164;
361/380, 392, 393, 394, 395

[56] References Cited

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D. 292,394	10/1987	Boucher	D13/162
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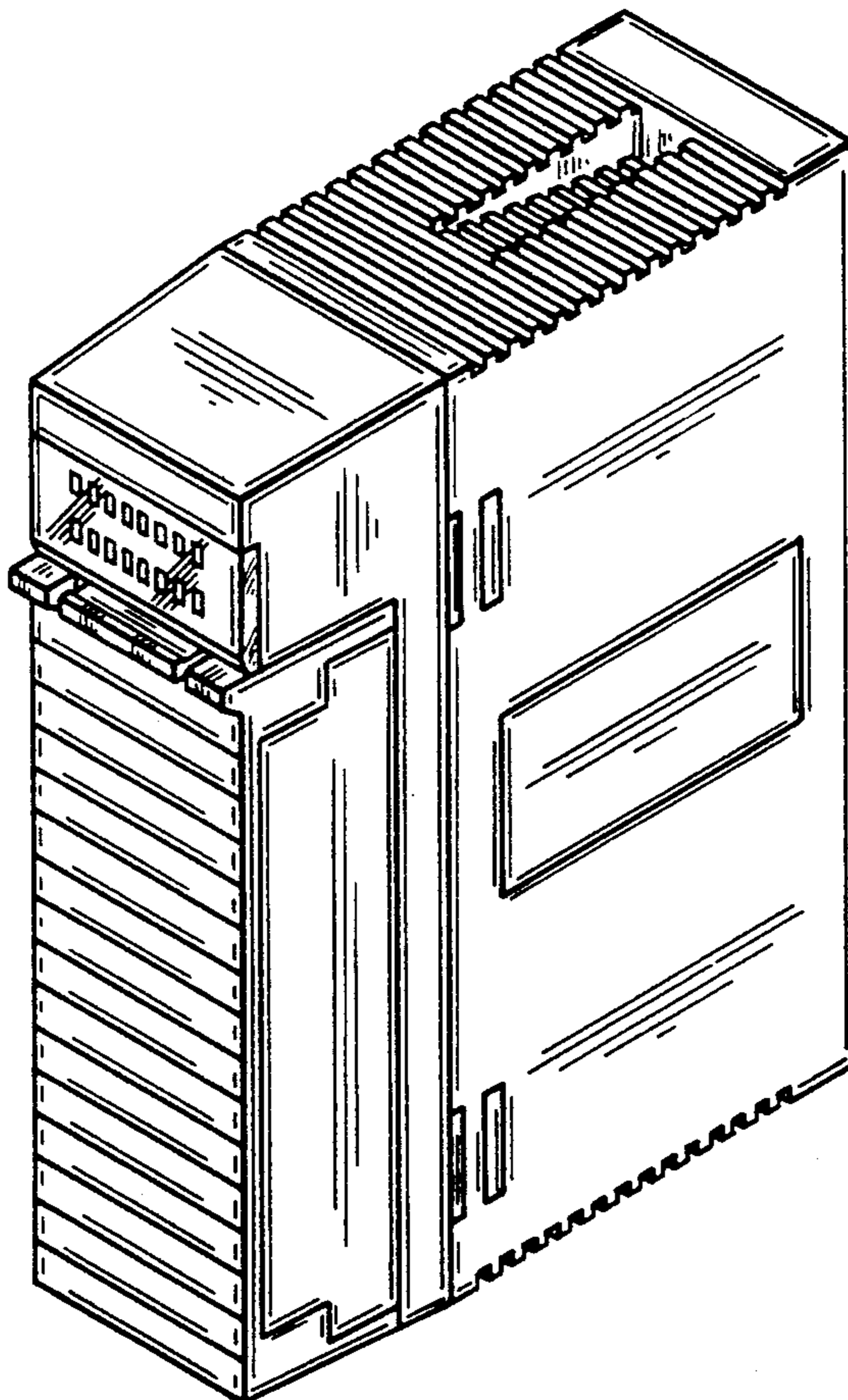
Primary Examiner—Wallace R. Burke
Assistant Examiner—Joel Sincavage
Attorney, Agent, or Firm—Flehr, Hohbach, Test, Albritton & Herbert

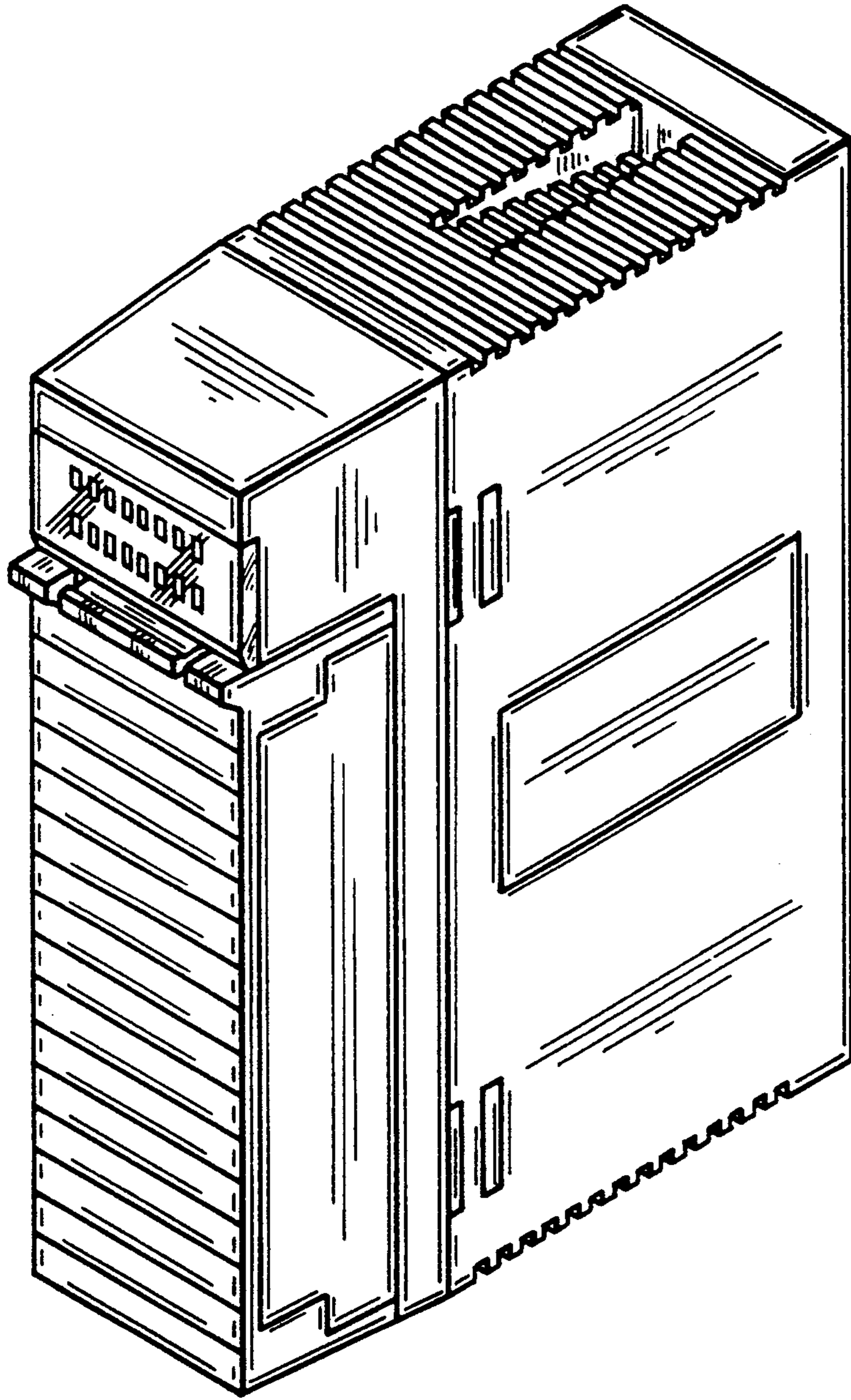
[57] CLAIM

The ornamental design for an input/output unit of sequence controller, as shown and described.

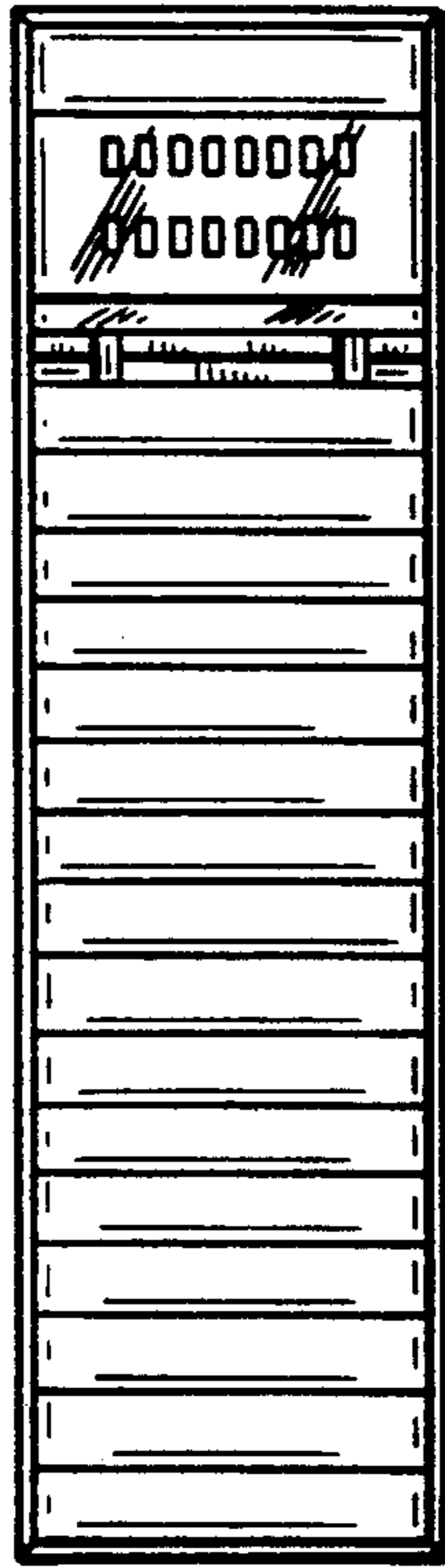
DESCRIPTION

FIG. 1 is a perspective view of an input/output unit of sequence controller showing the new design; FIG. 2 is a front elevational view thereof; FIG. 3 is a rear elevational view thereof; FIG. 4 is a top plan view thereof; FIG. 5 is a bottom plan view thereof; FIG. 6 is a right side elevational view thereof; and, FIG. 7 is a left side elevational view thereof.

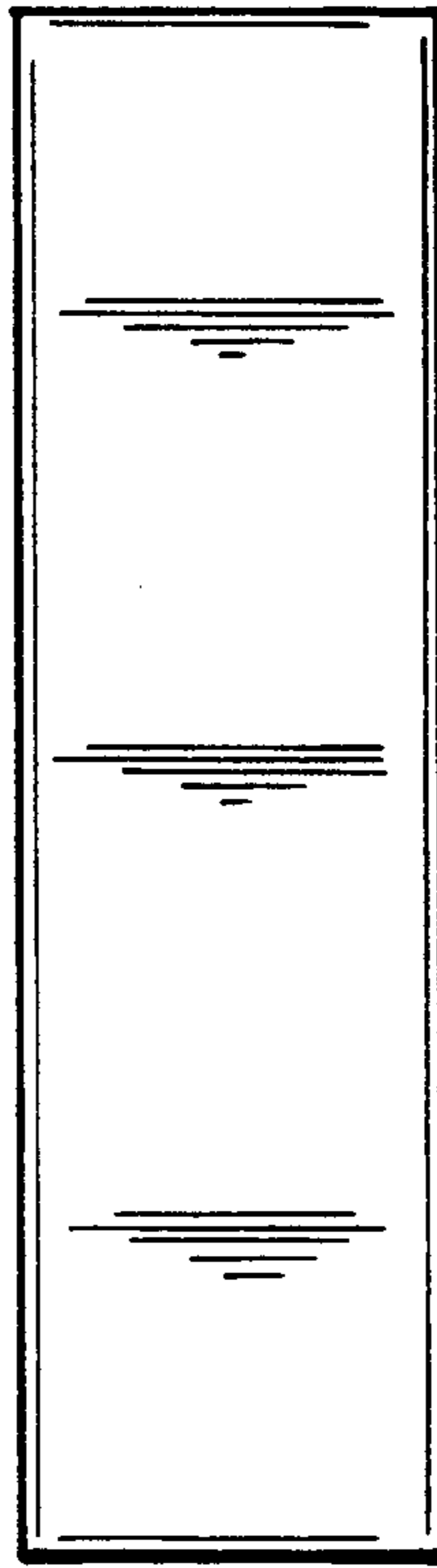




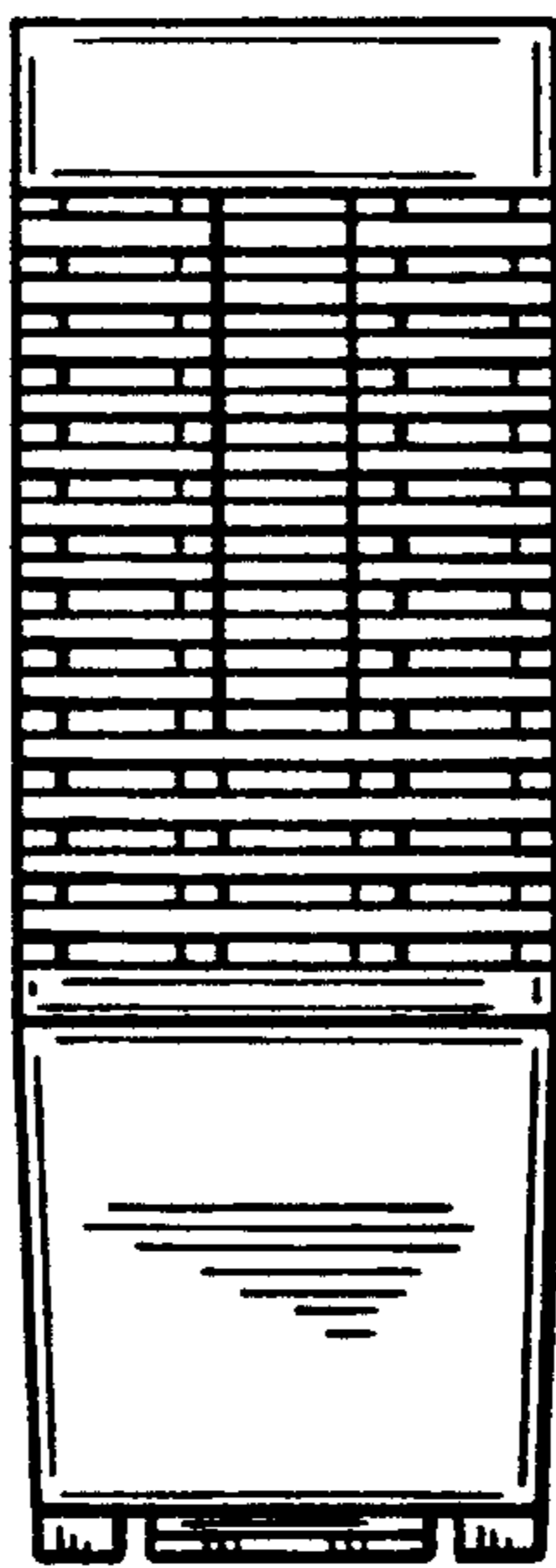
FIG_1



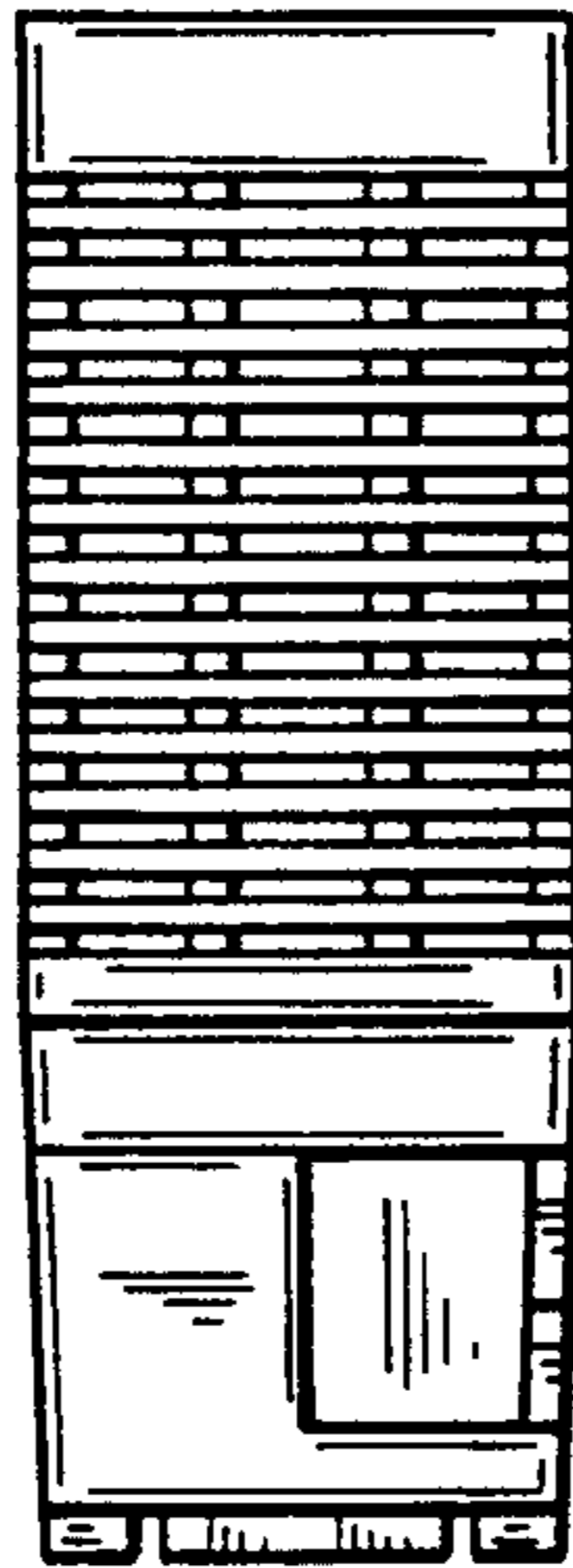
FIG_2



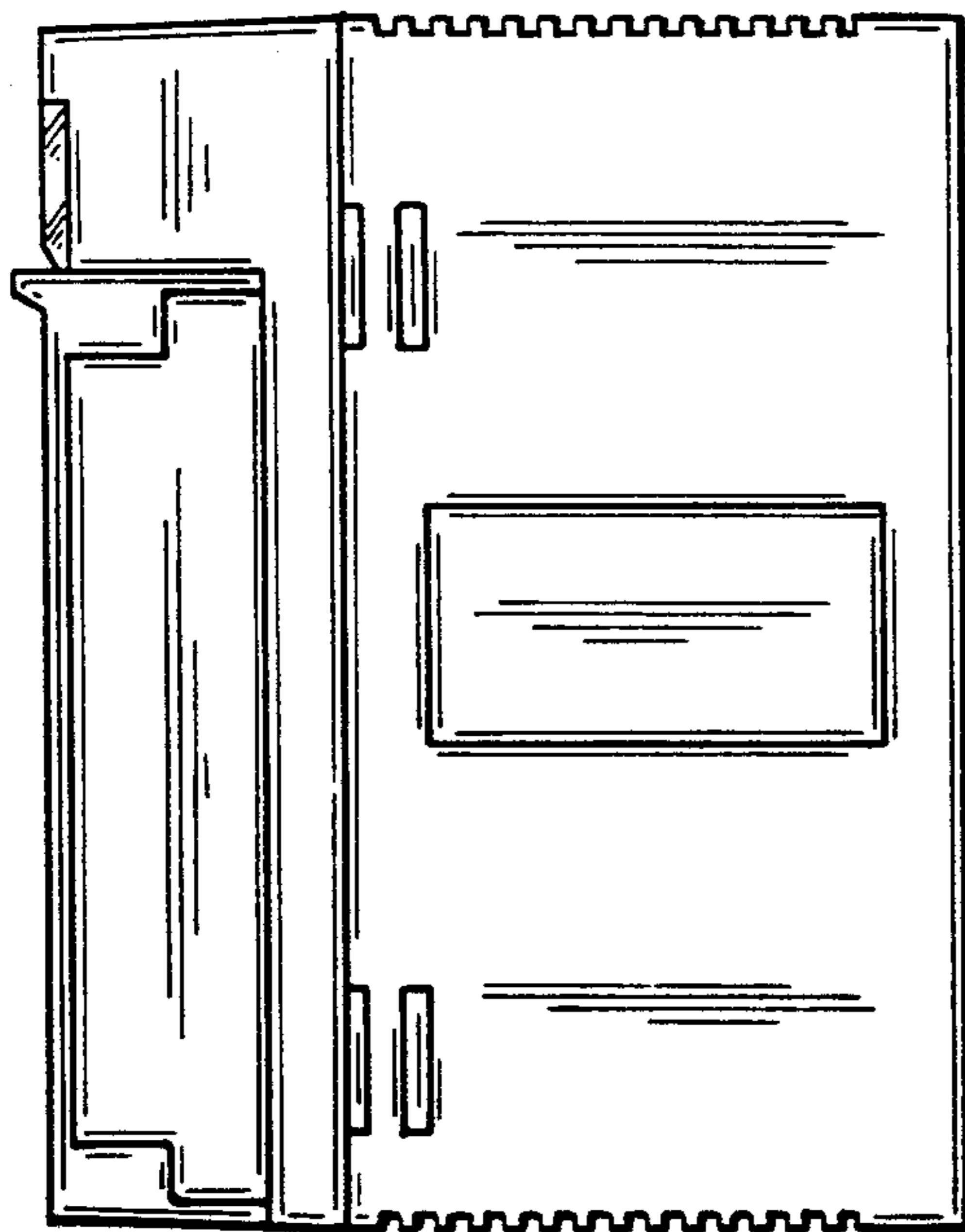
FIG_3



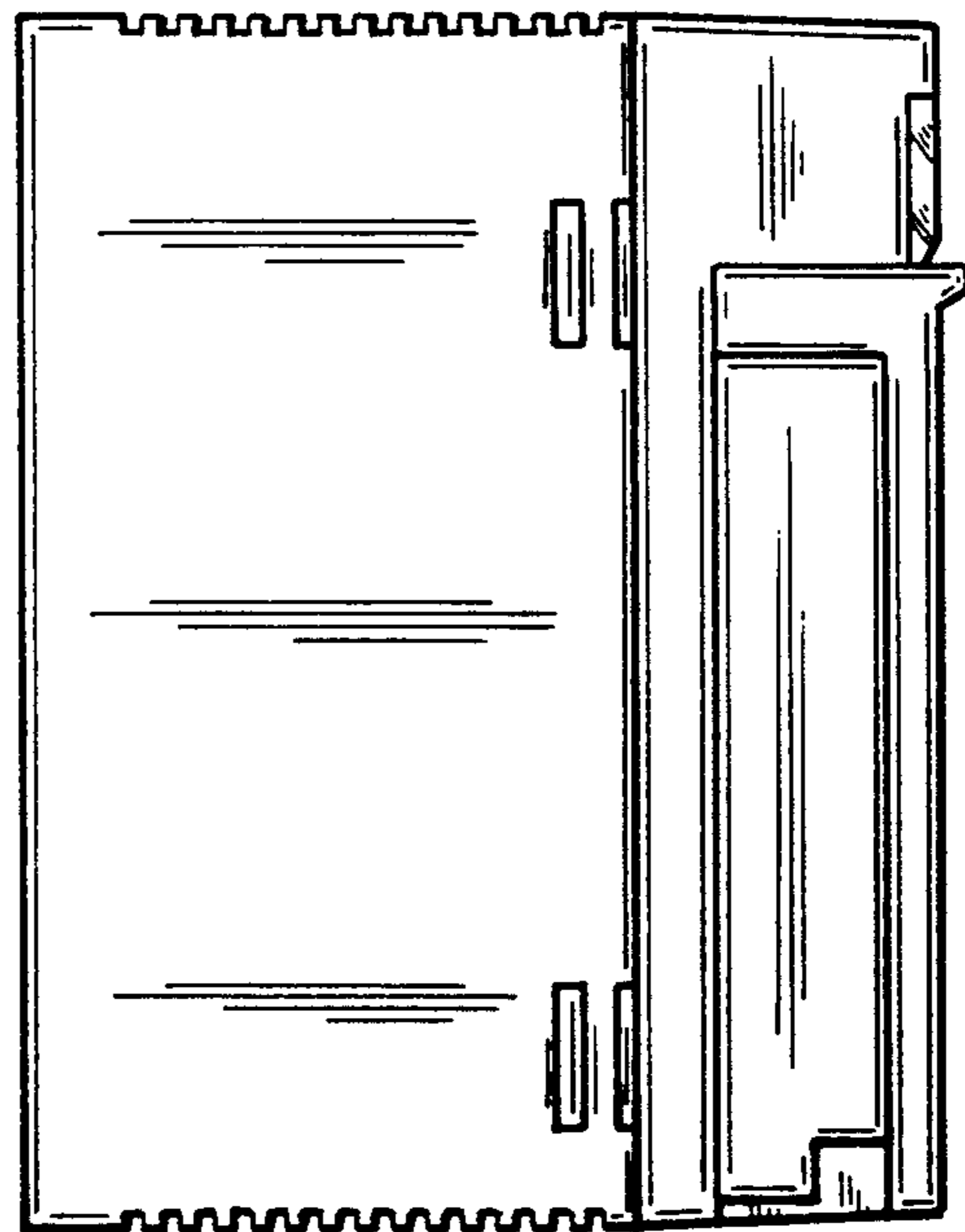
FIG_4



FIG_5



FIG_6



FIG_7