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United States Patent [19]

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Gloton

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[54] **CONNECTING TERMINAL FOR CHIP CARDS**

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4,777,563	10/1988	Teraoka et al.	361/395
4,990,759	2/1991	Gloton et al.	235/492
5,003,520	3/1991	Grieu et al.	235/380 X
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[**] Term: **14 Years**

[21] Appl. No.: **565,101**

[22] Filed: **Aug. 8, 1990**

[30] **Foreign Application Priority Data**

Feb. 9, 1990 [FR] France 90 0903

[52] U.S. Cl. **D14/117; D13/182**

[58] Field of Search D14/100, 114, 117; D13/182; 283/83; 29/840; 437/193; 380/23, 25; 235/379, 380, 441, 492; 174/6, 51, 59; 200/51 R; 361/392, 393, 395, 399; 439/43, 52, 64; 357/71, 72

[56] **References Cited**

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[57] CLAIM

The ornamental design for a connecting terminal for chip cards, as shown and described.

DESCRIPTION

The FIGURE is a top plan view of a connecting terminal for chip cards showing my new design, the undisclosed bottom plan view being a mirror image. The terminal shown is of no substantial thickness. The reduced card is shown in broken lines for illustrative purposes only and forms no part of the claimed design.



