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United States Patent [19]

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Ando

[45] Date of Patent: **** Apr. 21, 1992**

[54] **DIGITAL ELECTRONIC BRANCH EXCHANGE UNIT**

D. 294,028 2/1988 Duell et al. D14/102
D. 299,455 1/1989 Pushelberg et al. D14/240
D. 307,752 5/1990 Yoshida et al. D14/240

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[73] Assignee: **Kabushiki Kaisha Toshiba, Kawasaki, Japan**

[**] Term: **14 Years**

[21] Appl. No.: **481,381**

[22] Filed: **Feb. 20, 1990**

[30] **Foreign Application Priority Data**

Aug. 30, 1989 [JP] Japan 1-31311

[52] U.S. Cl. **D14/240; D14/107**

[58] Field of Search **D14/100, 102, 108, 109, D14/140, 240, 241; D13/162, 163, 164, 184, 199; 360/97.01-99.12; 361/331, 332, 380-385, 399, 415, 426, 391, 395; 379/329, 330, 399; 312/208, 291; 200/5 A, 5 R**

[56] **References Cited**

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D. 236,007 7/1975 Davis, Jr. et al. D14/102
D. 279,562 7/1985 Wood D14/140
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[57] **CLAIM**

The ornamental design for digital electronic branch exchange unit, as shown and described.

DESCRIPTION

FIG. 1 is a top front right side perspective view of a digital electronic branch exchange unit showing my new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a top plan view thereof;

FIG. 4 is a right side elevational view thereof, a left side elevational view being a mirror image of the right;

FIG. 5 is a rear elevational view thereof; and,

FIG. 6 is a bottom plan view thereof.

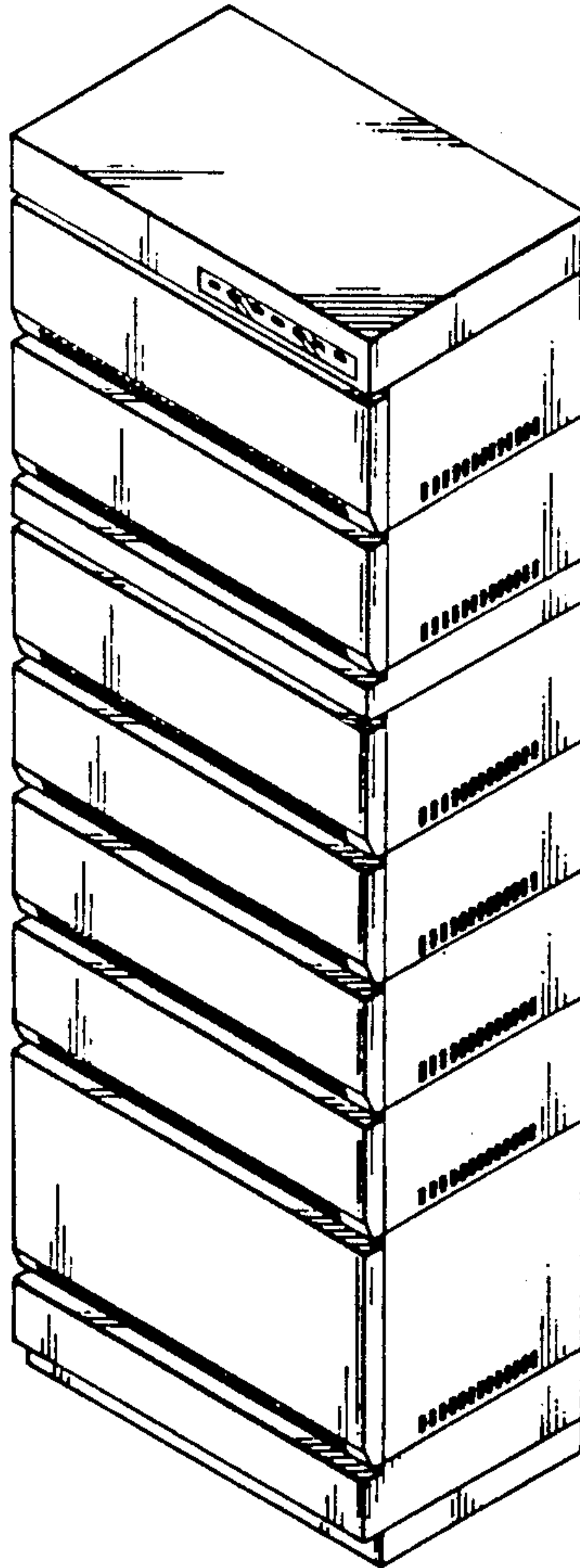


FIG. 1

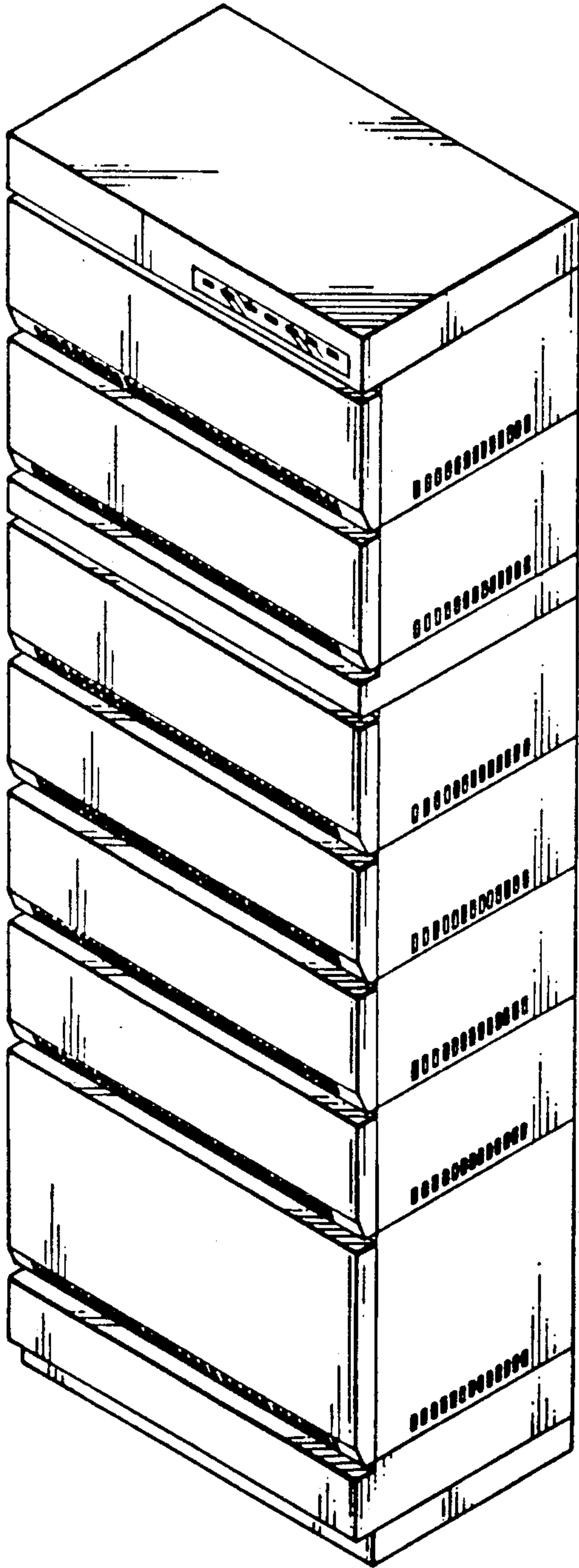


FIG. 2

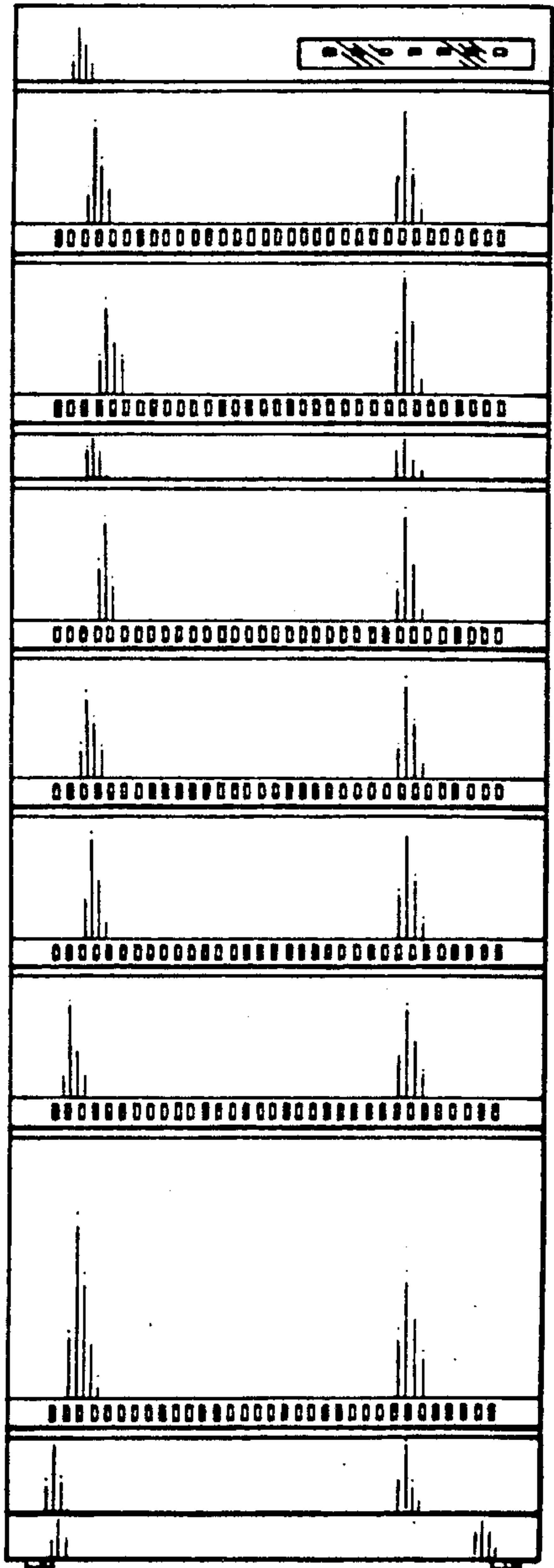


FIG. 3

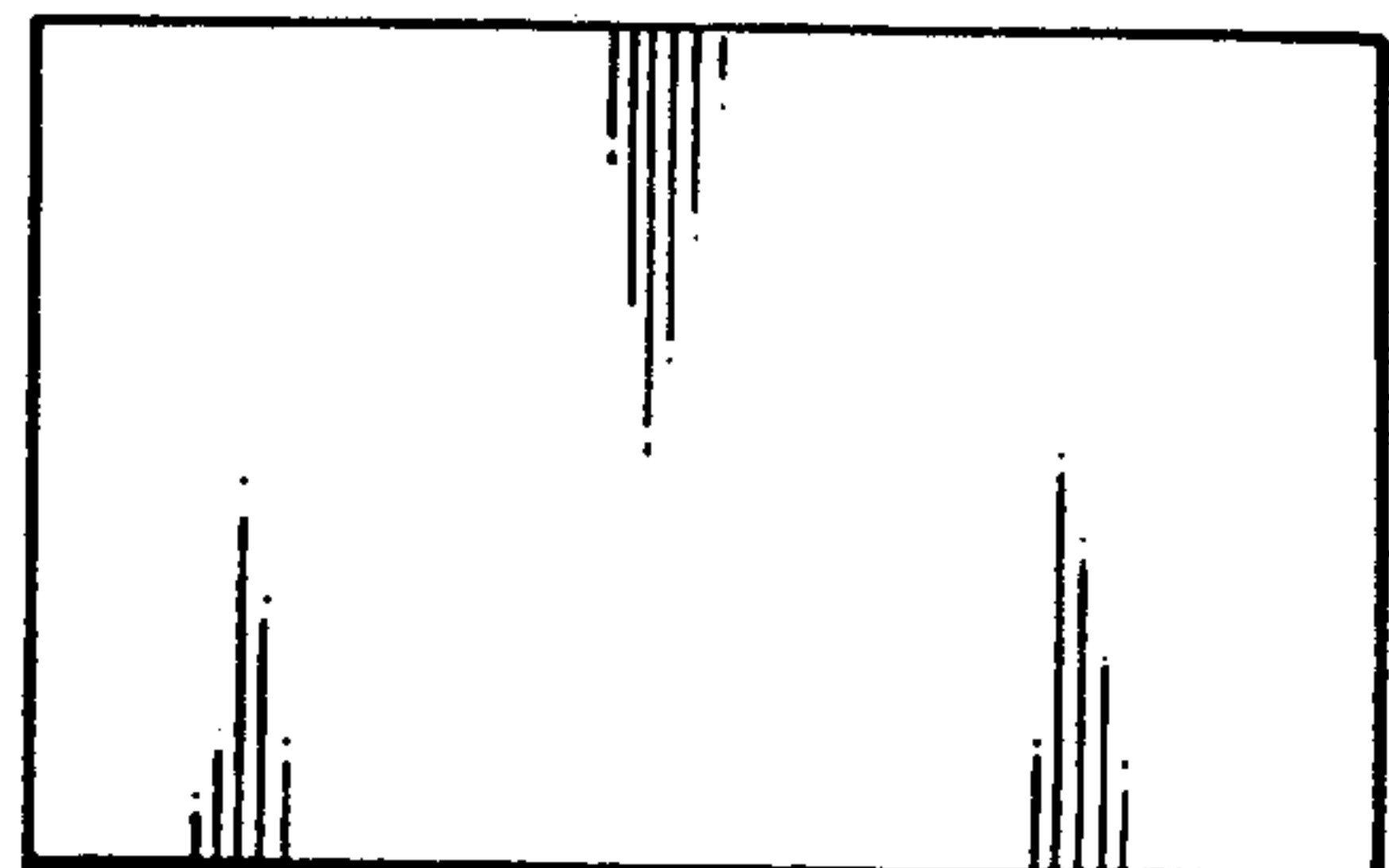


FIG. 4

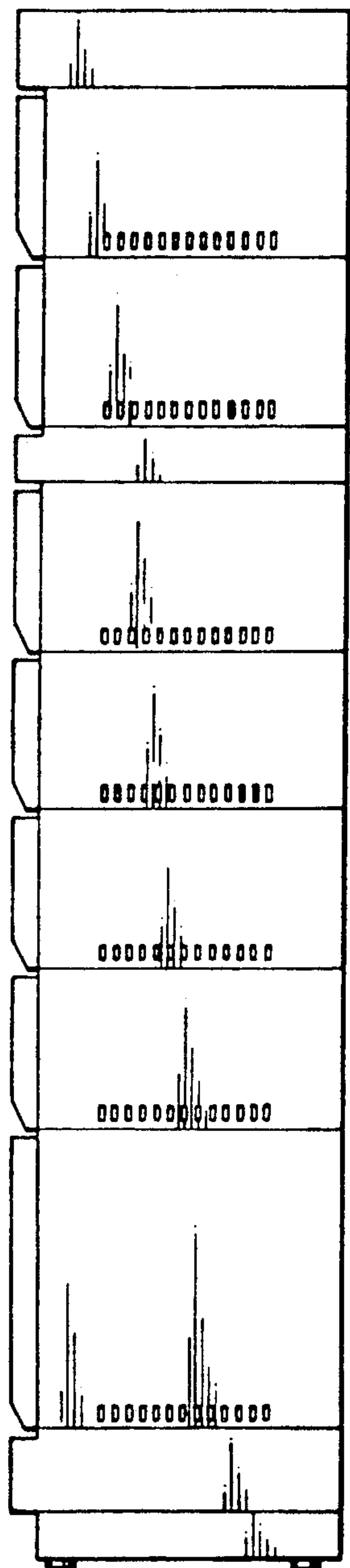


FIG. 5

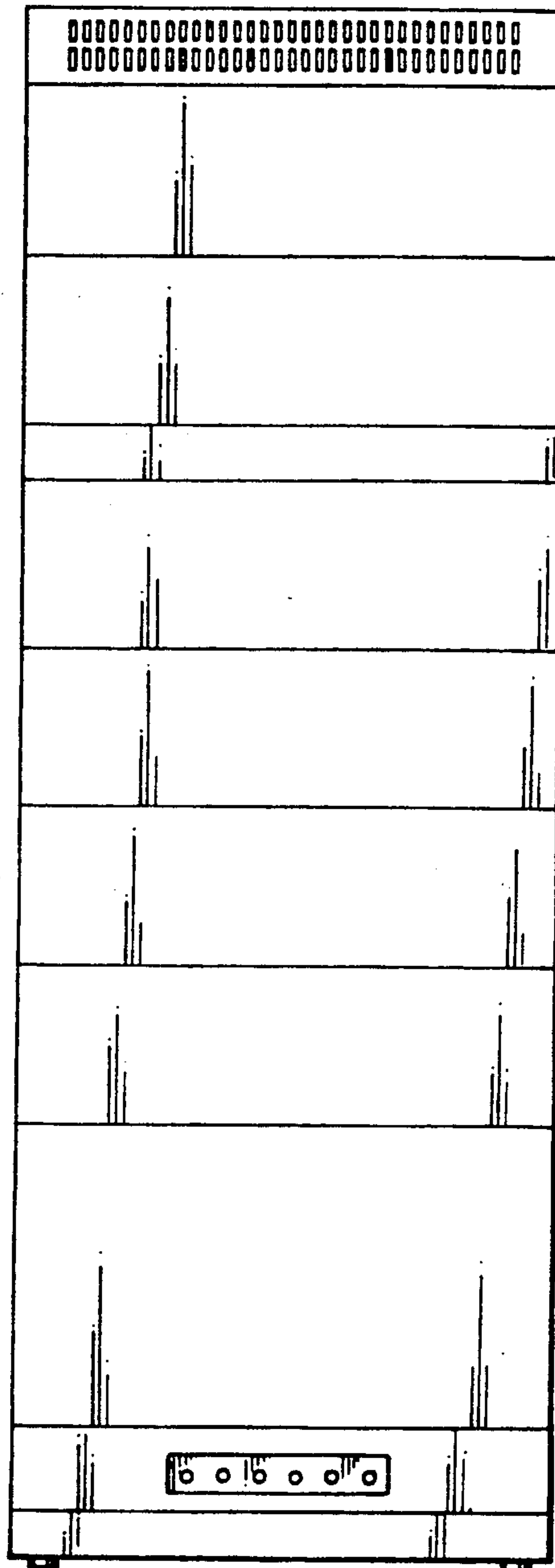


FIG. 6

