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United States Patent [19]

Ishii et al.

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[45] Date of Patent: **** Feb. 18, 1992**

[54] **INSTRUMENT FOR HOLDING BOATS FOR THERMAL TREATMENT OF SEMICONDUCTOR WAFERS**

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[73] Assignee: **Tel Sagami Limited, Kanagawa, Japan**

[**] Term: **14 Years**

[21] Appl. No.: **301,993**

[22] Filed: **Jan. 25, 1989**

[30] **Foreign Application Priority Data**

Jul. 25, 1988 [JP] Japan 1-29833

[52] U.S. Cl. **D15/144.1; D15/199**

[58] Field of Search D15/144, 199; 219/390, 219/411; 414/222, 225, 226, 433; 432/6, 33, 153, 239, 253, 258, 260, 264

[56] **References Cited**

U.S. PATENT DOCUMENTS

D. 199,694 12/1964 Belke D15/144 X

D. 272,743 2/1984 Knox D15/144

4,344,729 8/1982 Orsinger et al. 414/226

4,787,805 11/1988 Kosikowski 414/226 X

FOREIGN PATENT DOCUMENTS

28151 2/1987 Japan 414/226

727731 3/1980 U.S.S.R. 414/226

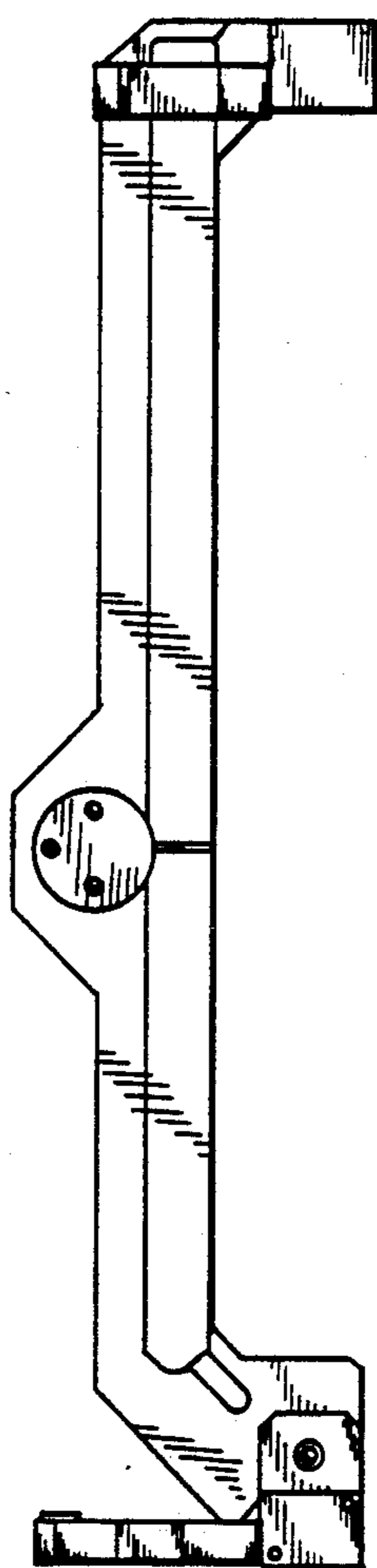
Primary Examiner—Bruce W. Dunkins
Assistant Examiner—Antoine D. Davis

[57] **CLAIM**

The ornamental design for an instrument for holding boats for thermal treatment of semiconductor wafers, as shown.

DESCRIPTION

FIG. 1 is a front elevational view of an instrument for holding boats for thermal treatment of semiconductor wafers showing our new design; FIG. 2 is a left side elevational view; FIG. 3 is a right elevational view; FIG. 4 is a top plan view; FIG. 5 is a bottom plan view; and FIG. 6 is a rear elevational view thereof.



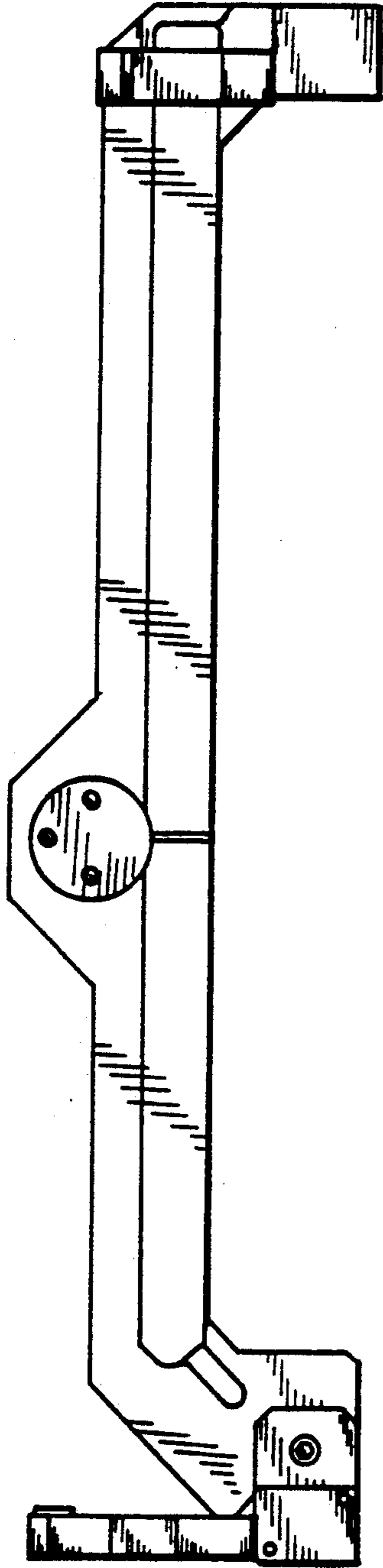


FIG. 1



FIG. 2

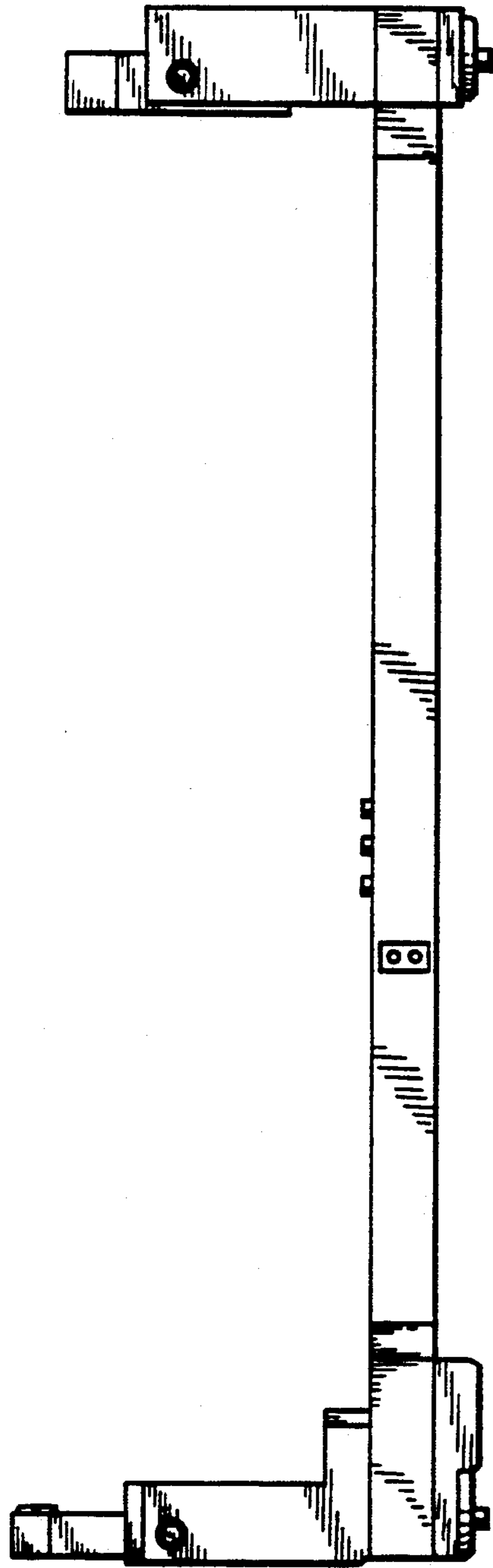


FIG. 3

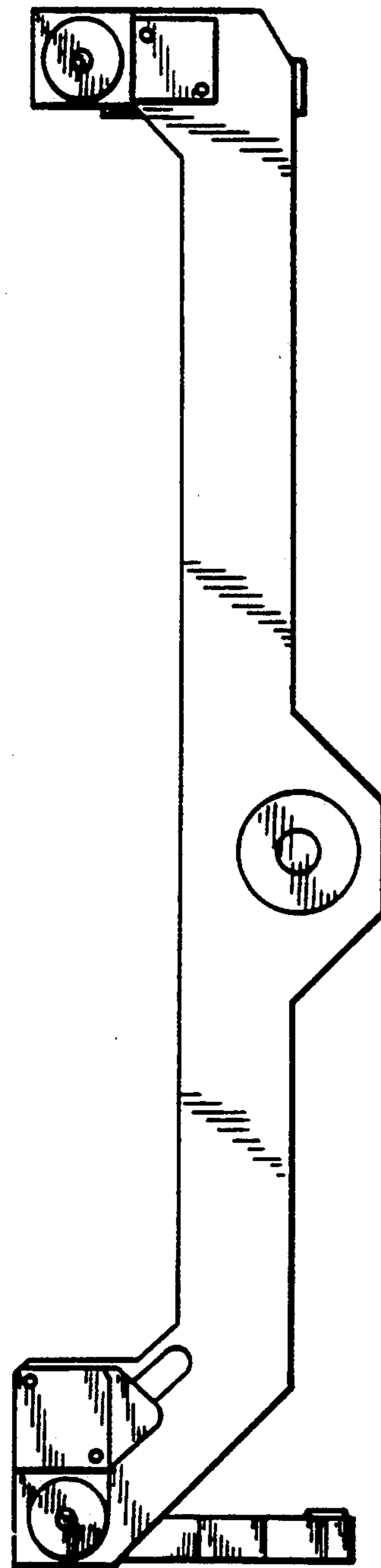


FIG. 6

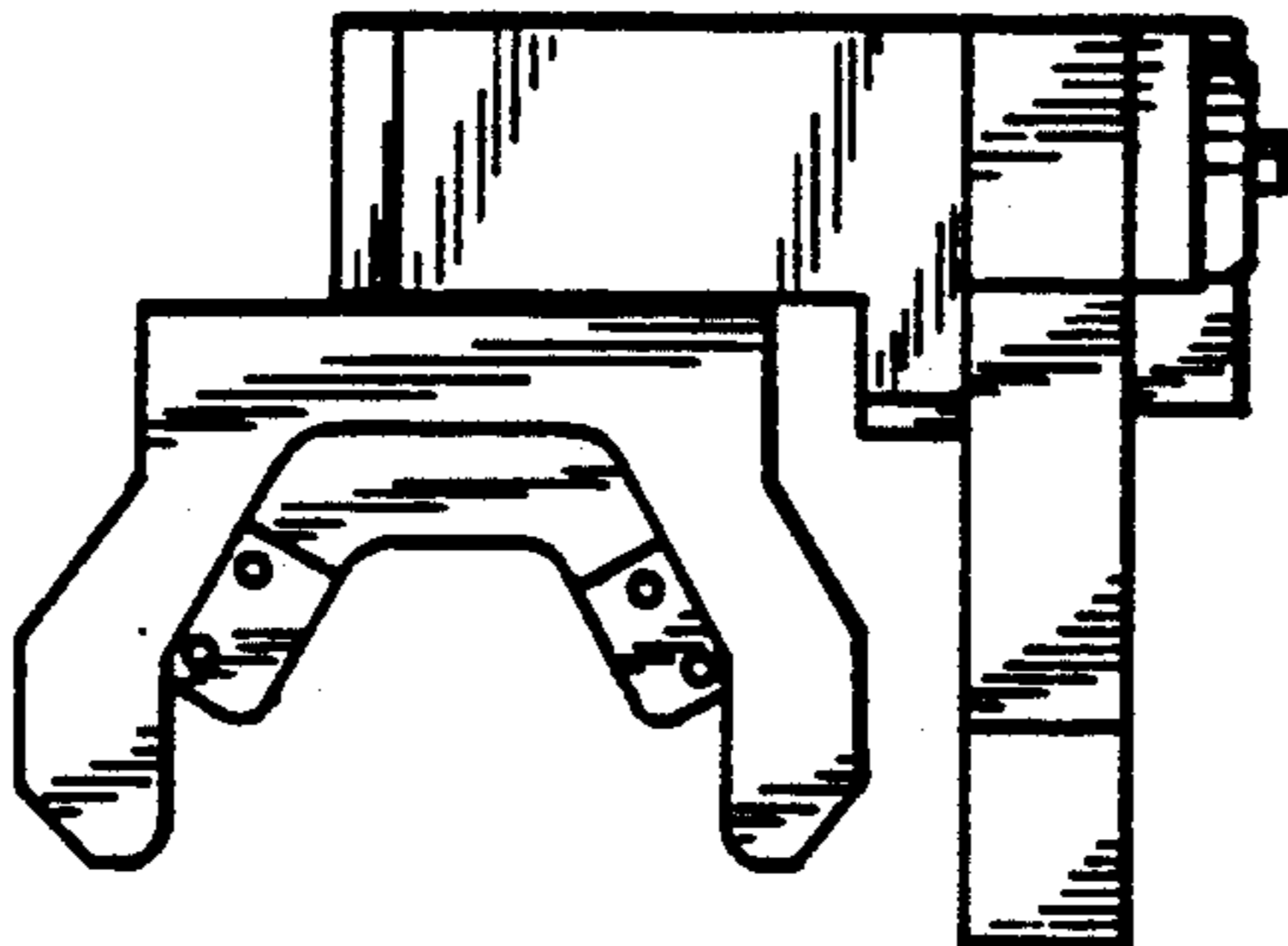


FIG. 4

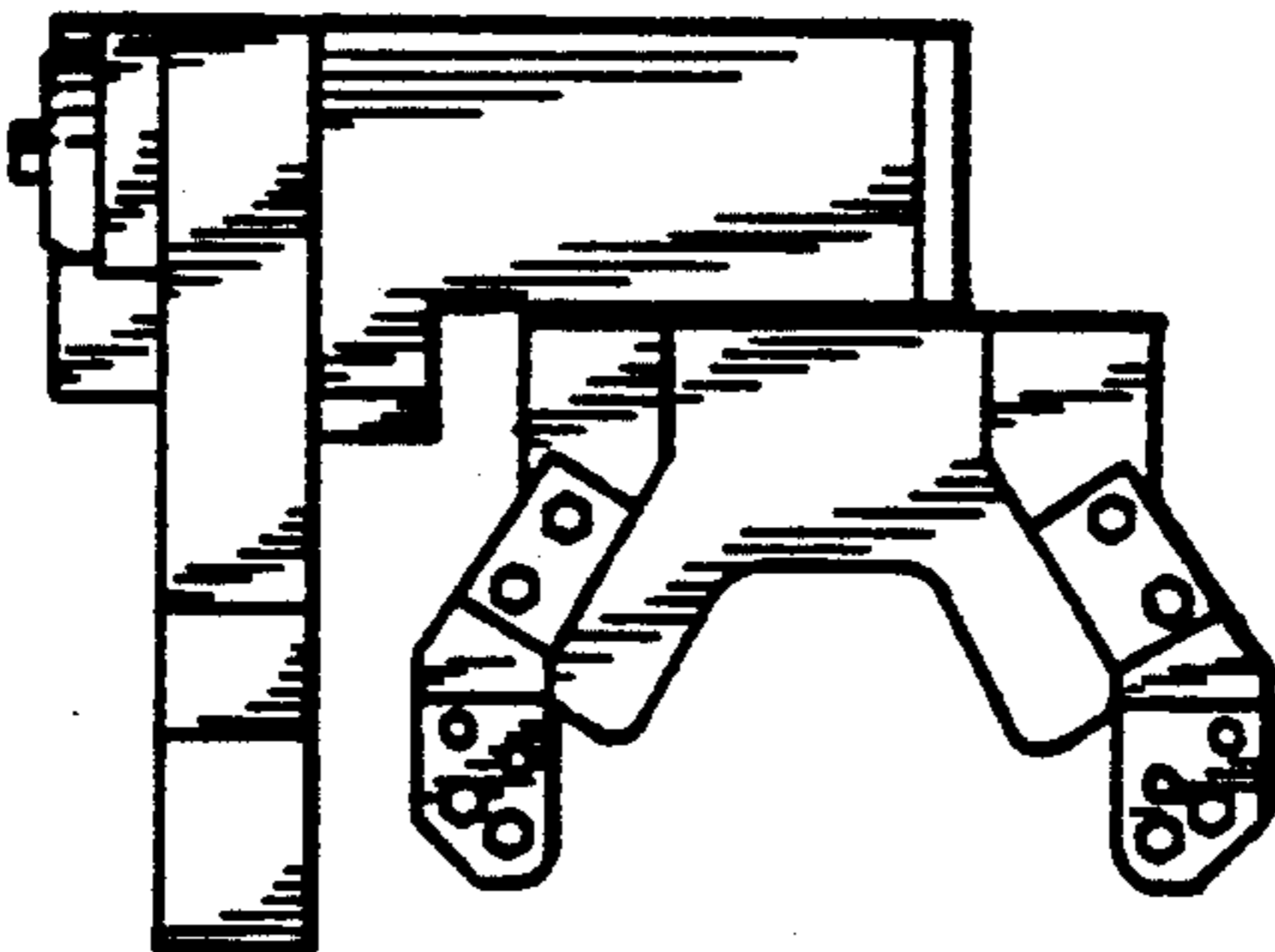


FIG. 5