

[54] SEMICONDUCTOR SUBSTRATE WITH CONDUCTING PATTERN

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[73] Assignee: Ibiden Co., Ltd., Ogaki, Japan

[*] Notice: The portion of the term of this patent subsequent to Jul. 23, 2005 has been disclaimed.

[**] Term: 14 Years

[21] Appl. No.: 181,253

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[52] U.S. Cl. D13/182

[58] Field of Search D13/12, 20, 99; 361/401, 403, 404, 405; 357/70, 72, 74, 80; 174/52.4; 437/209

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Assistant Examiner—Joel Sincavage
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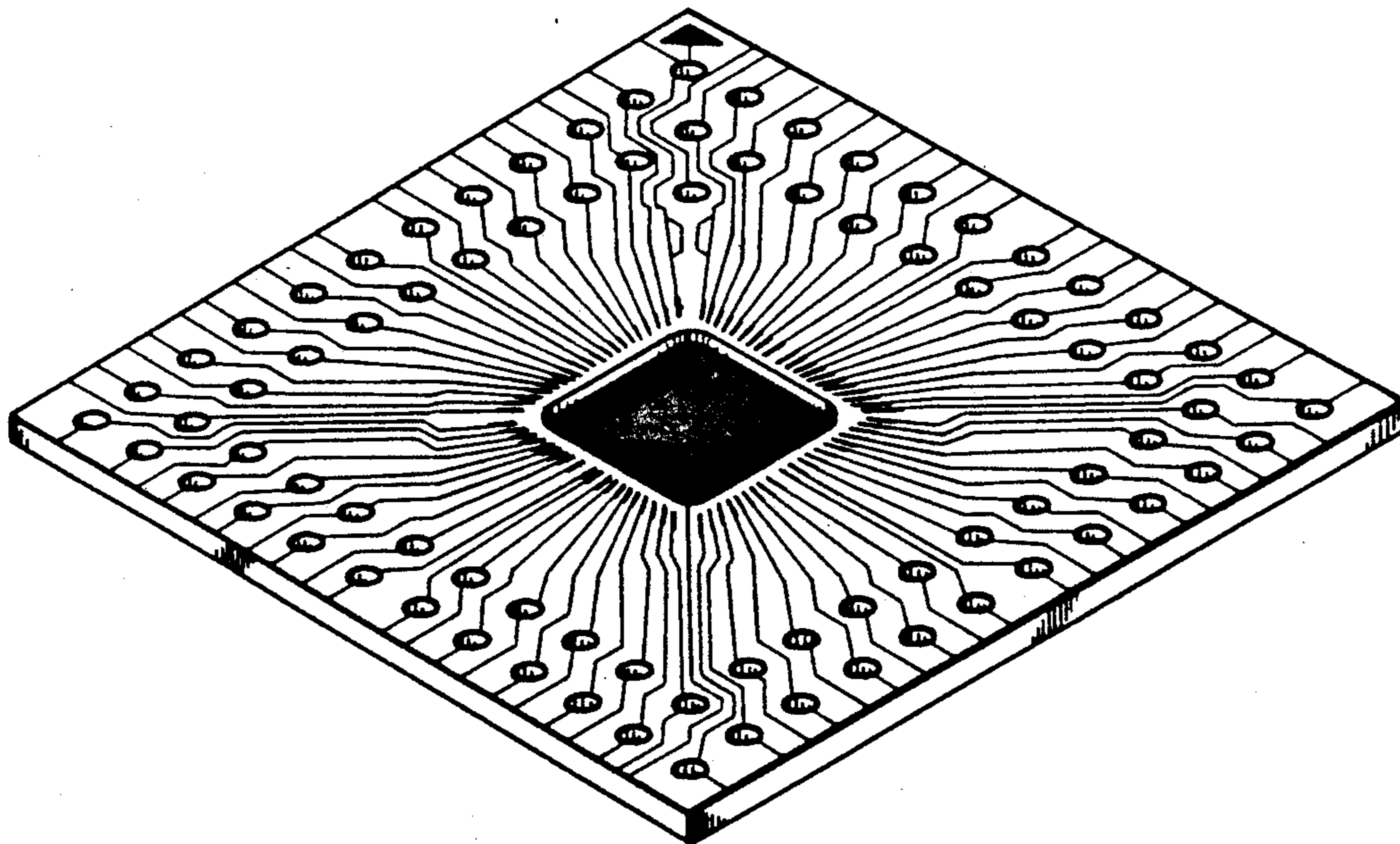
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[57] **CLAIM**

The ornamental design for a semiconductor substrate with conducting pattern, as shown.

DESCRIPTION

FIG. 1 is a top perspective view of a semi-conductor mounting device showing our new design; FIG. 2 is a bottom perspective view thereof; FIG. 3 is a right side elevational view thereof; FIG. 4 is a left side elevational view thereof; FIG. 5 is a rear elevational view thereof; and FIG. 6 is a front elevational view thereof; FIG. 7 is a top plan view thereof; and FIG. 8 is a bottom plan view thereof.



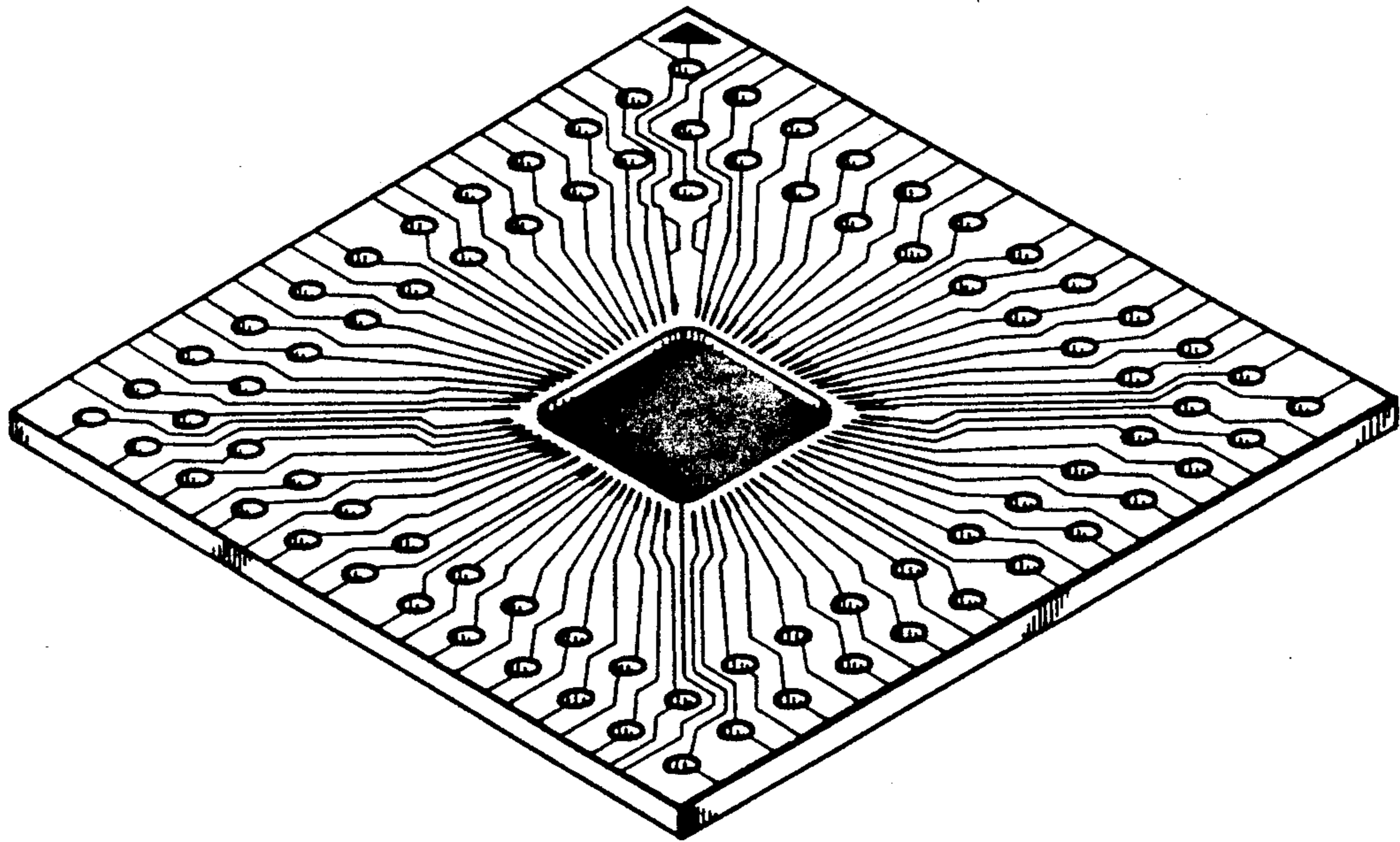


FIG. 1

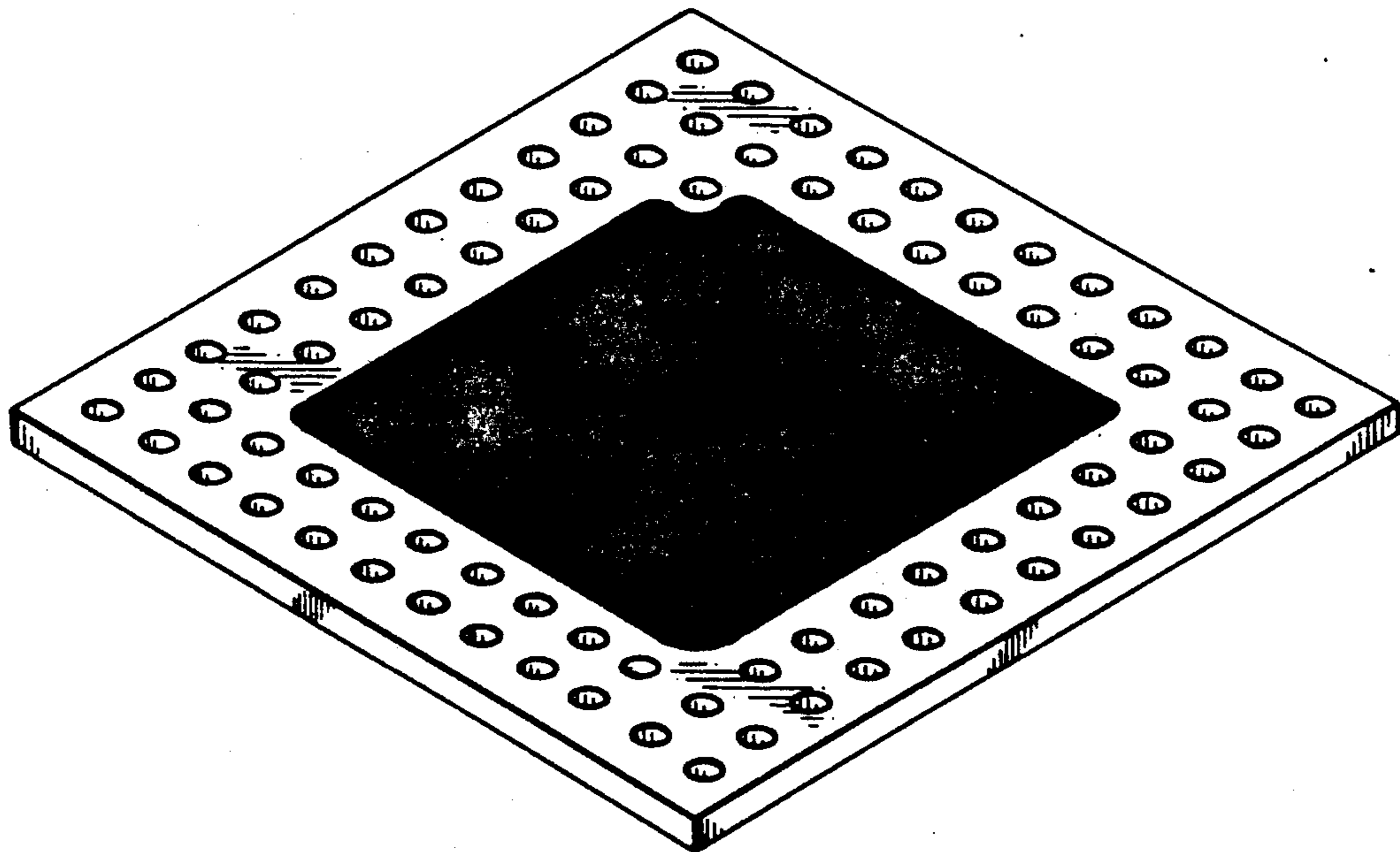


FIG. 2



FIG. 3



FIG. 4



FIG. 5

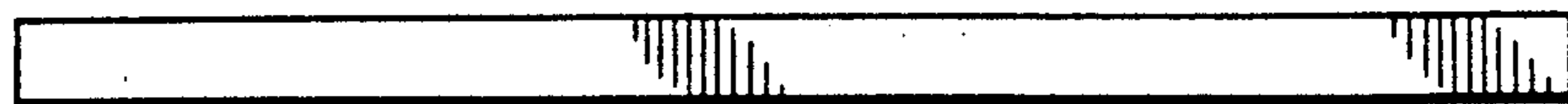


FIG. 6

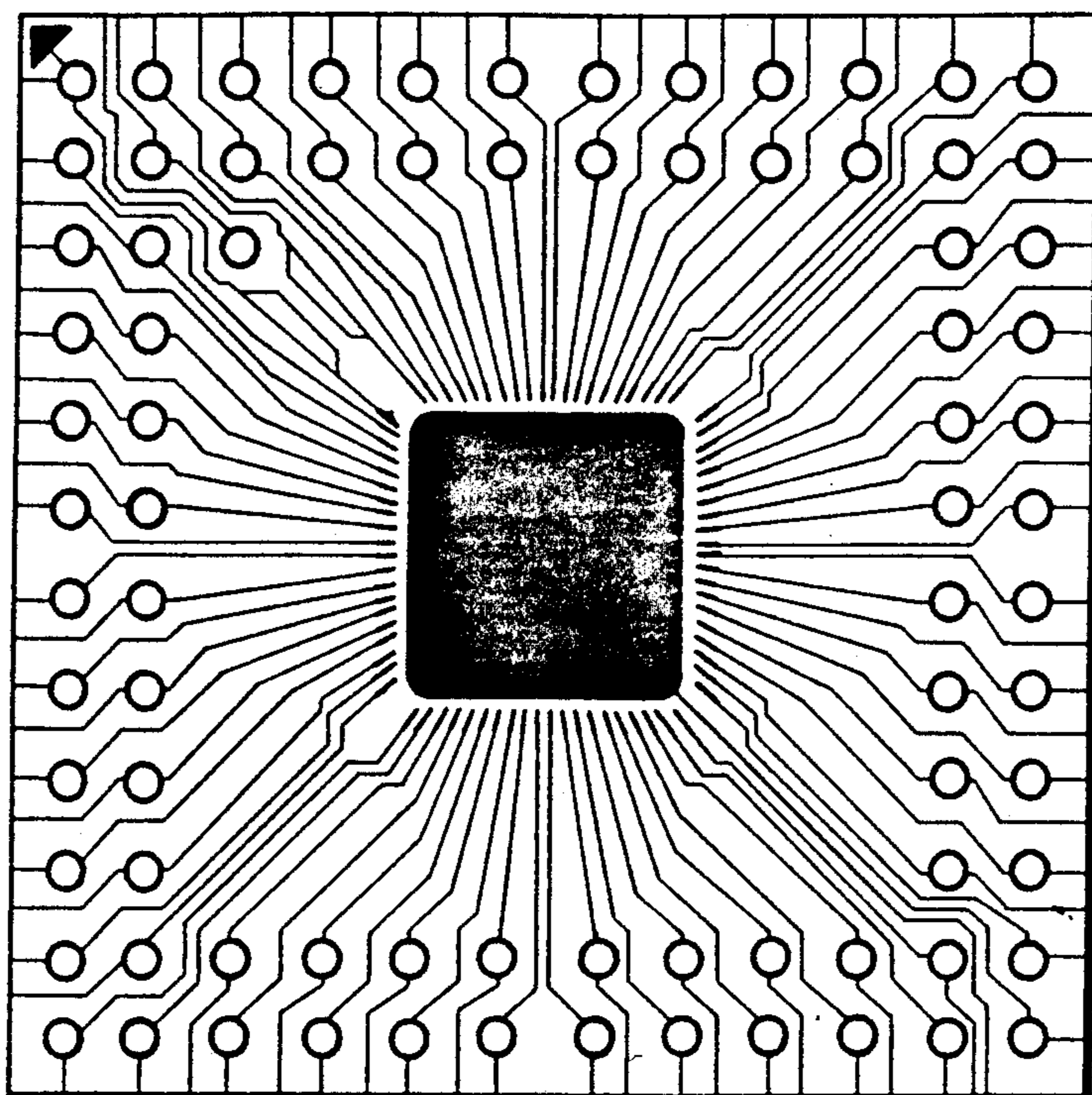


FIG. 7

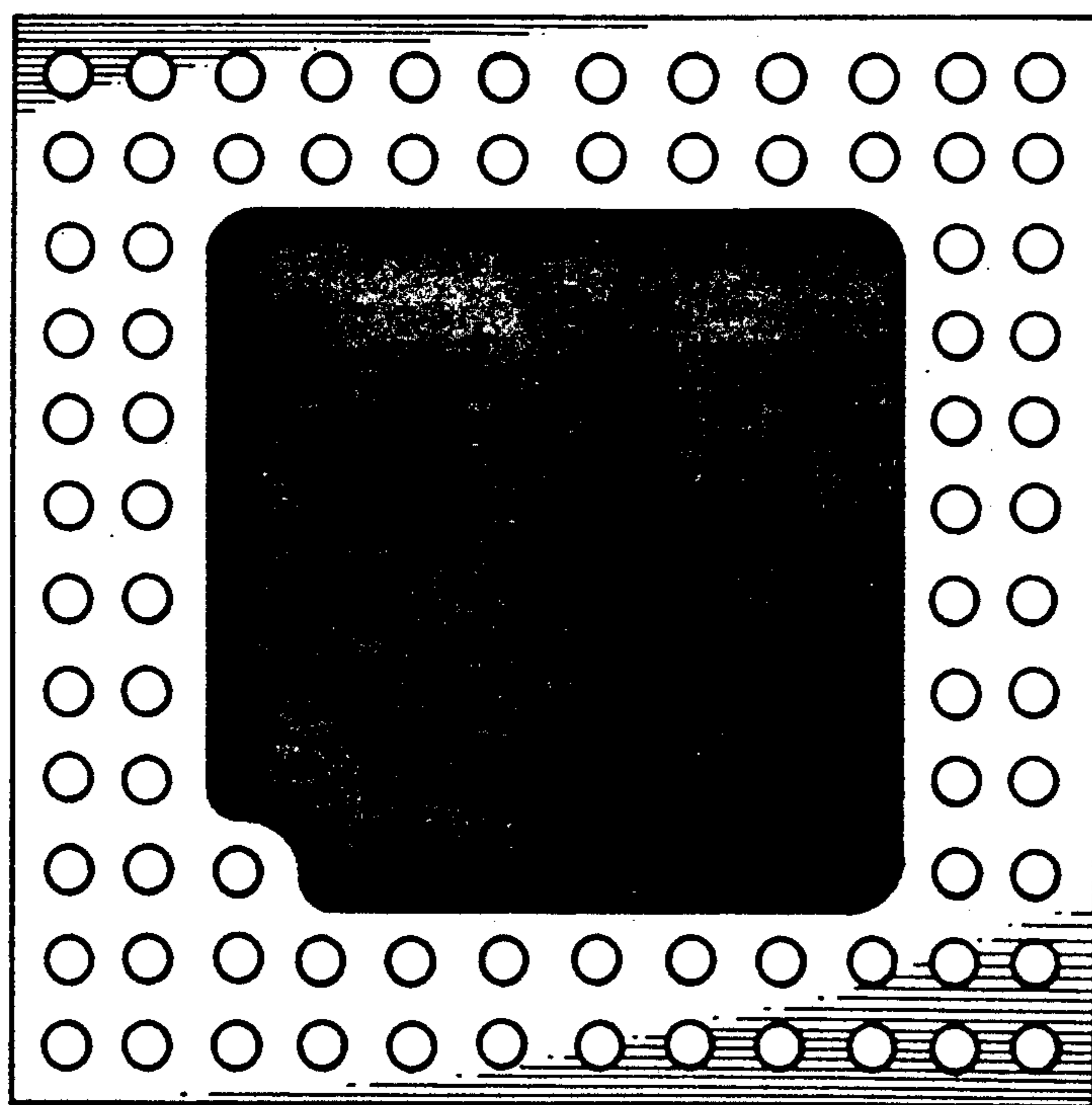


FIG. 8