

[54] **IMPEDANCE ADAPTER FOR ELECTRONIC CIRCUITRY**

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[73] **Assignee: Mux Lab Inc., Ville St-Laurent, Canada**

[**] **Term: 14 Years**

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[52] **U.S. Cl. D13/133**

[58] **Field of Search D13/133, 146, 147; D14/256; 439/344, 367, 381, 391, 460, 490, 573, 578, 620, 675**

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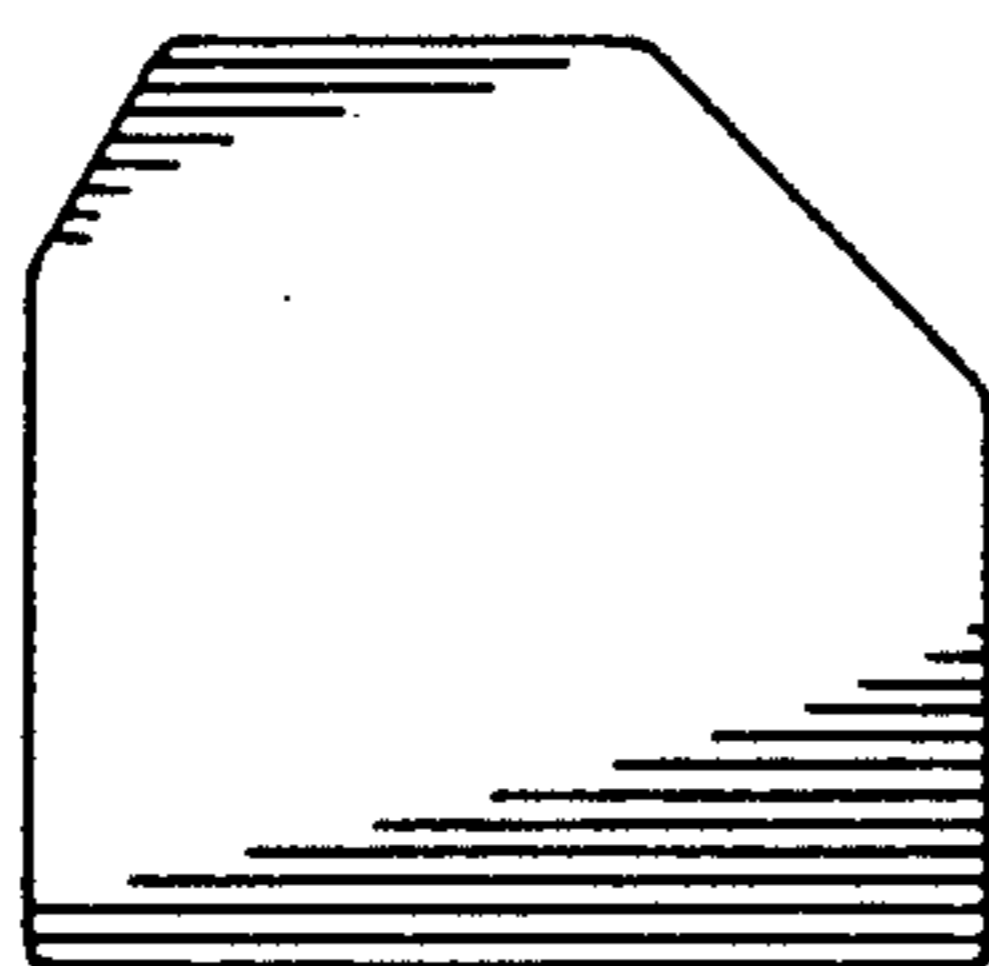
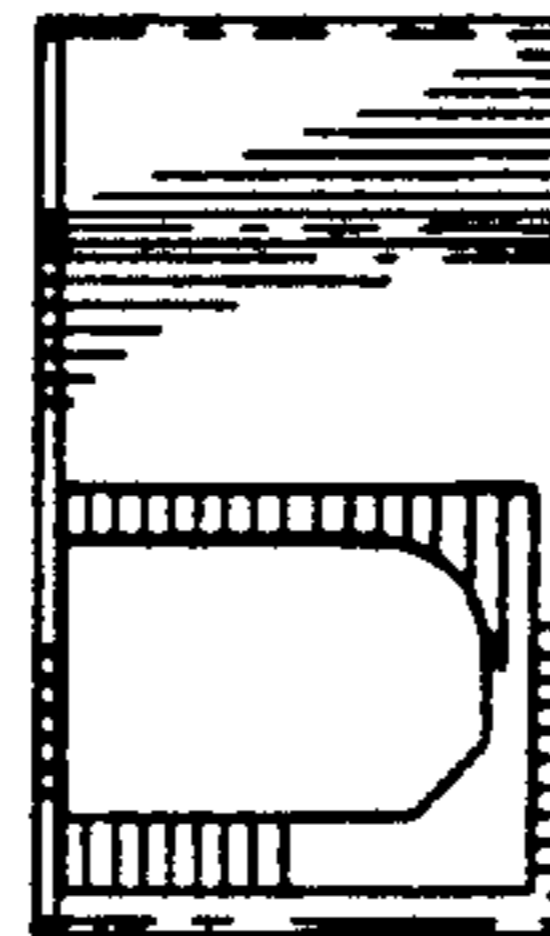
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[57] **CLAIM**

The ornamental design for an impedance adapter for electronic circuitry, as shown and described.

DESCRIPTION

FIG. 1 is a left side elevational view of an impedance adapter for electronic circuitry showing my new design;
 FIG. 2 is a front elevational view thereof;
 FIG. 3 is a right side elevational view thereof;
 FIG. 4 is a rear elevational view thereof;
 FIG. 5 is a top plan view thereof;
 FIG. 6 is a bottom plan view thereof.



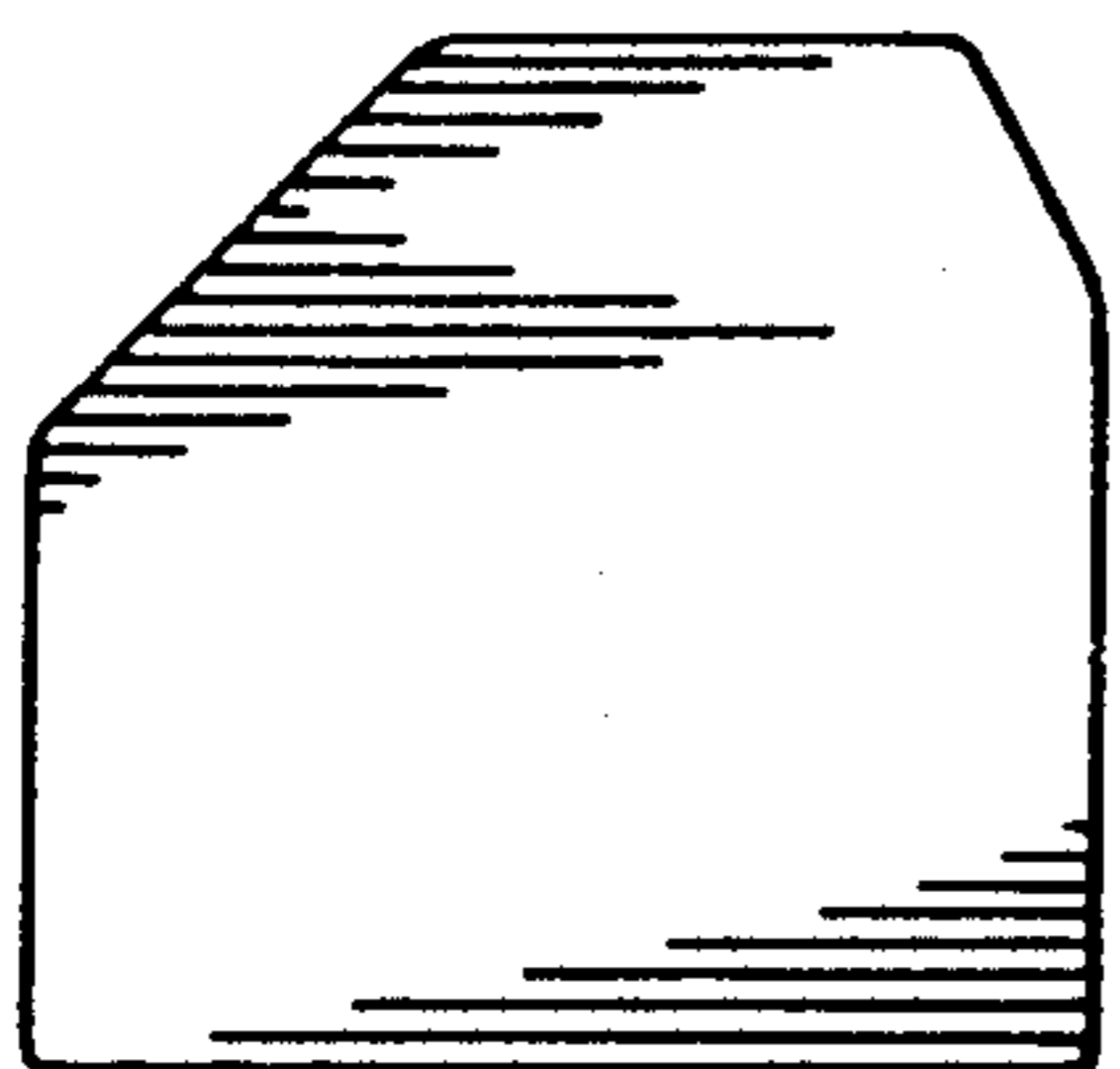


FIG. 1

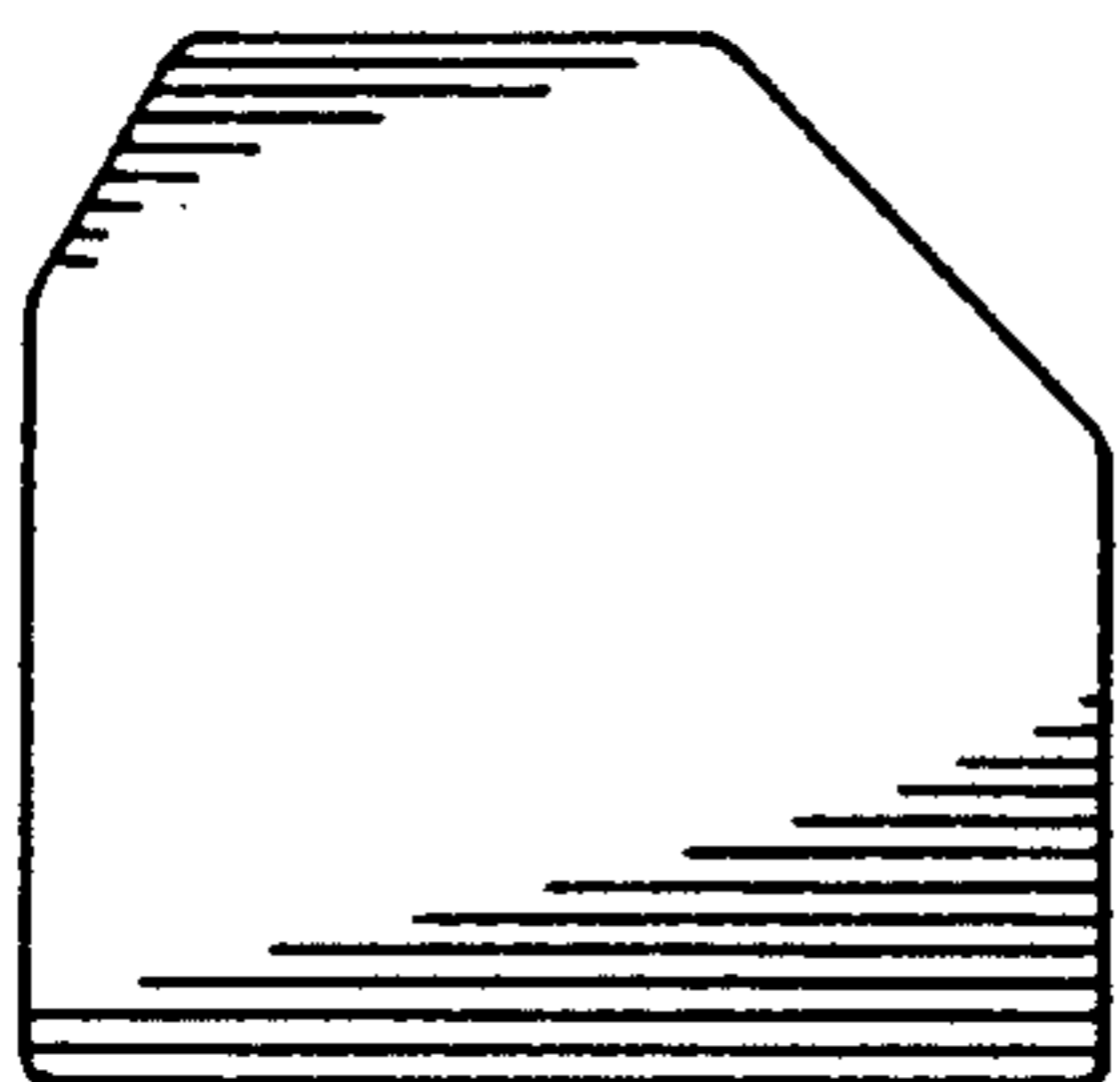


FIG. 3



FIG. 5

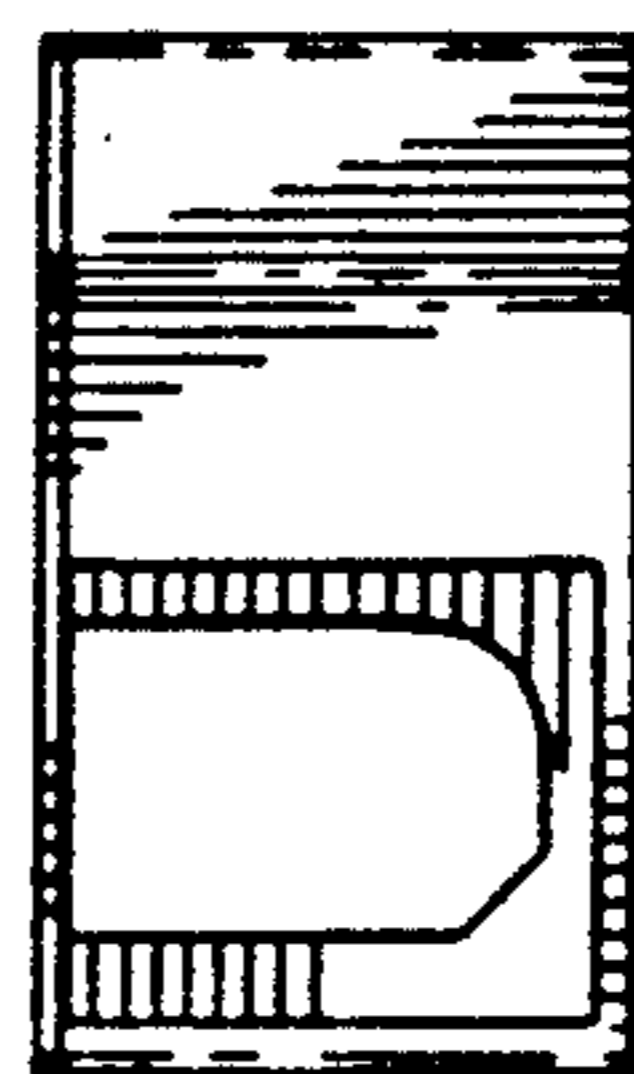


FIG. 2

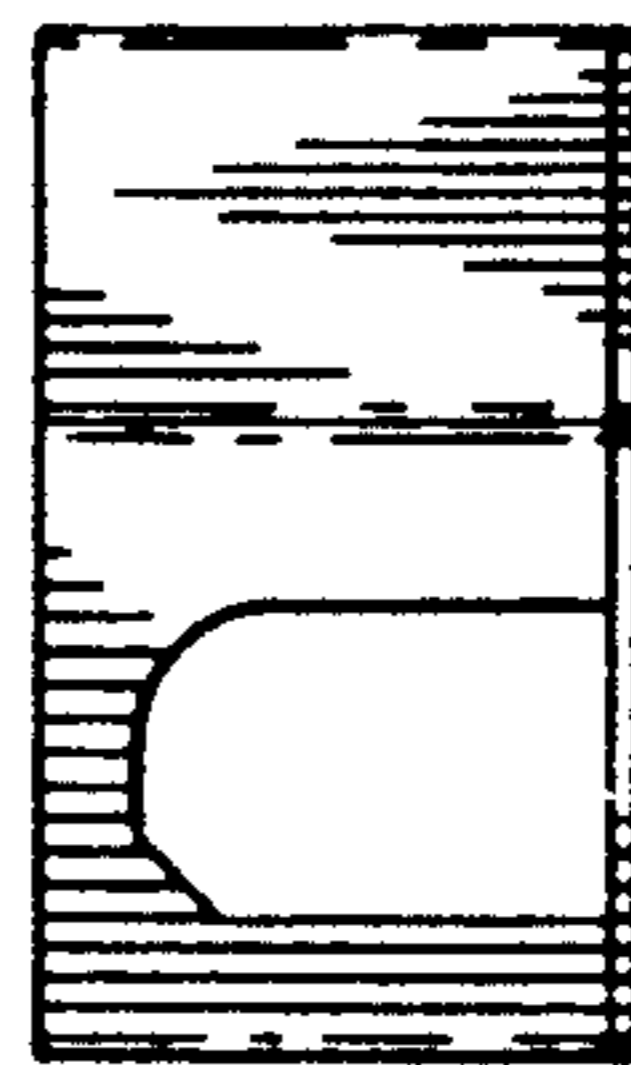


FIG. 4



FIG. 6