

[54] ELECTRICAL CONNECTOR RECEPTACLE FOR A PRINTED CIRCUIT BOARD

4,690,479 9/1987 Hollyday et al. 439/620 X
4,938,704 7/1990 Fujiura 439/607 X

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FOREIGN PATENT DOCUMENTS

[73] Assignee: Hirose Electric Co., Ltd., Tokyo, Japan

0124264 11/1984 European Pat. Off. 439/620
0180284 5/1986 European Pat. Off. 439/92

[**] Term: 14 Years

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[21] Appl. No.: 299,823

[57] CLAIM

[22] Filed: Jan. 11, 1989

The ornamental design for an electrical connector receptacle for a printed circuit board, as shown and described.

[30] Foreign Application Priority Data

Jul. 11, 1988 [JP] Japan 63-27584

DESCRIPTION

[52] U.S. Cl. D13/147

[58] Field of Search D13/133; 439/92, 344,
439/354, 631, 634, 636, 637, 651, 856, 857

FIG. 1 is a top plan view of an electrical connector receptacle for a printed circuit board shown an embodiment of my new design;

[56] References Cited

U.S. PATENT DOCUMENTS

D. 295,971 3/1988 Kikuta D13/25
D. 313,007 12/1990 Nakamura D13/147
4,469,387 9/1984 McHugh 439/140
4,518,209 5/1985 Negley 439/92
4,521,062 6/1985 Kurbikoff et al. 439/92
4,679,883 7/1987 Assini et al. 439/95 X

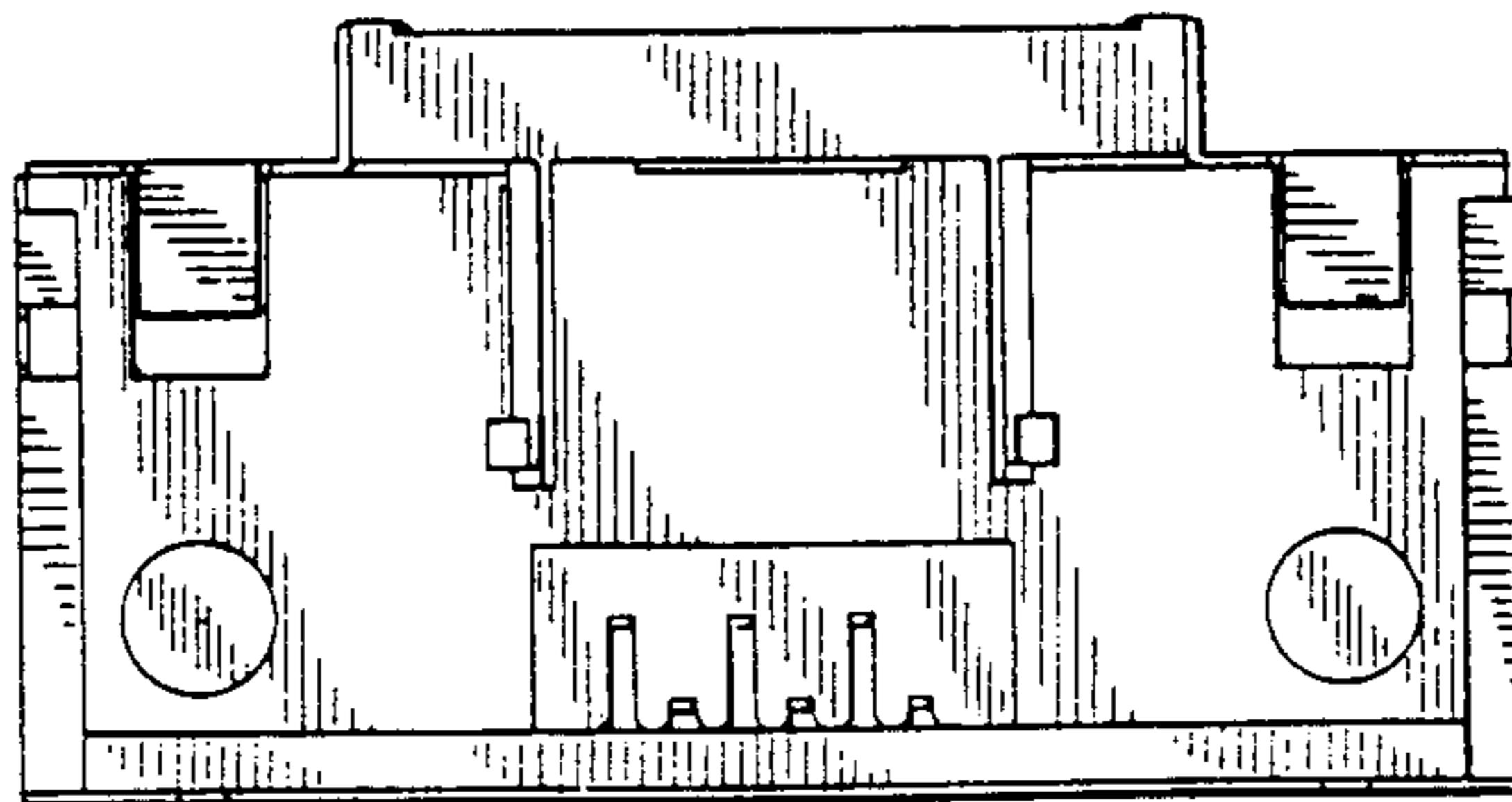
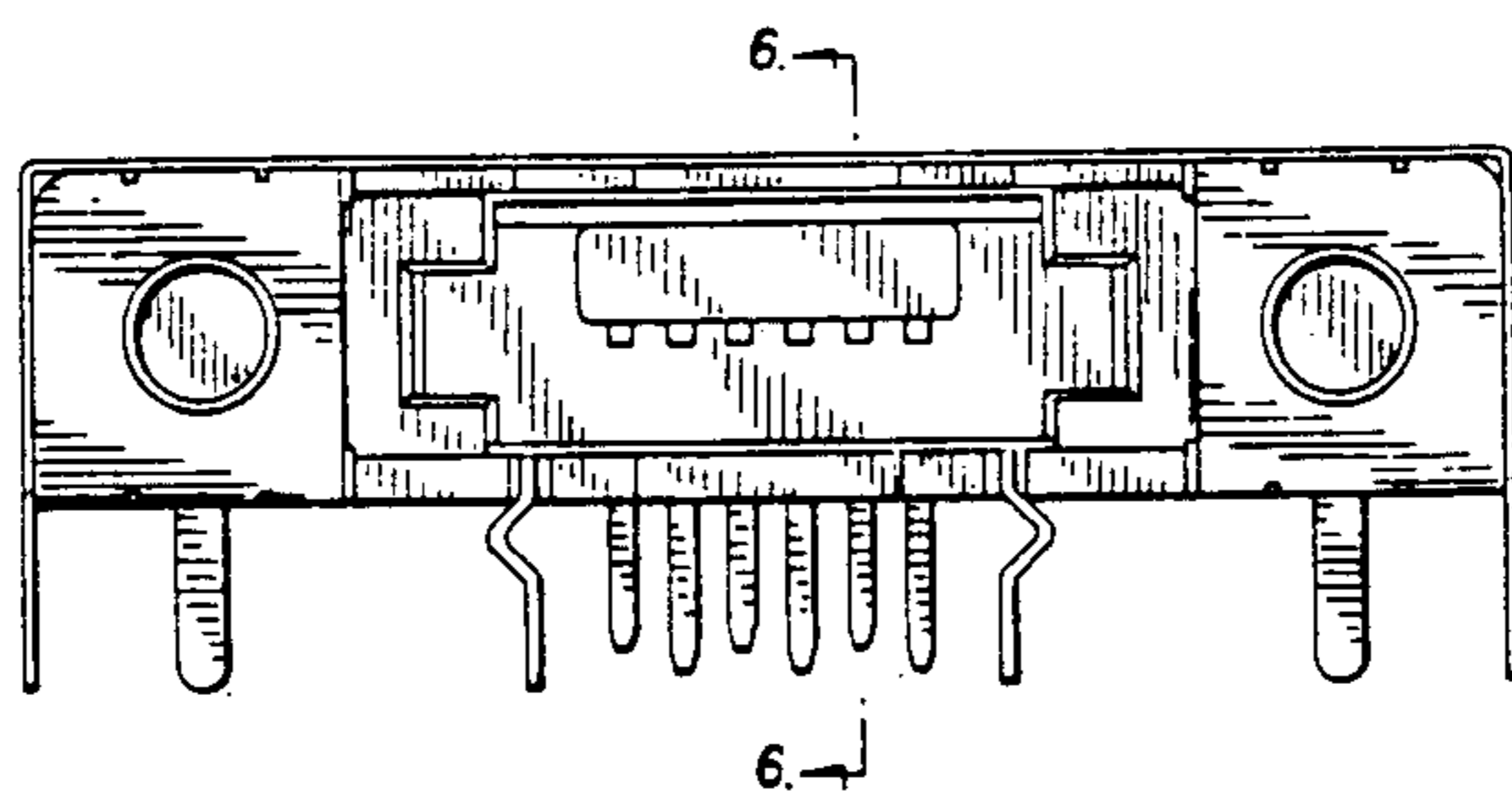
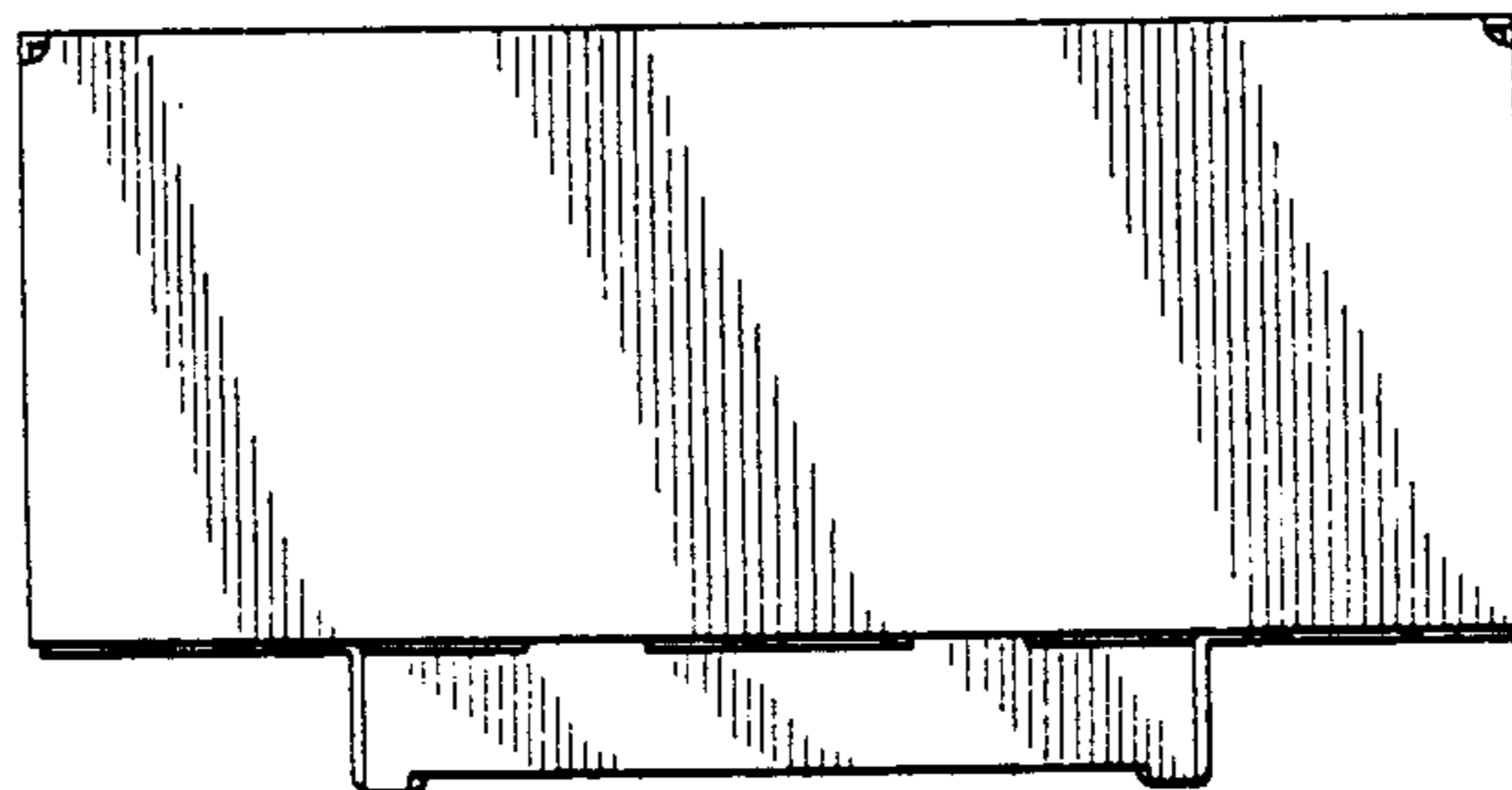
FIG. 2 is a front elevational view thereof;

FIG. 3 is a bottom plan view thereof;

FIG. 4 is a right side elevational view thereof, the left side elevational view thereof being a mirror image;

FIG. 5 is a rear elevational view thereof; and

FIG. 6 is a cross-sectional view thereof, taken along the line 5—5 in FIG. 2.



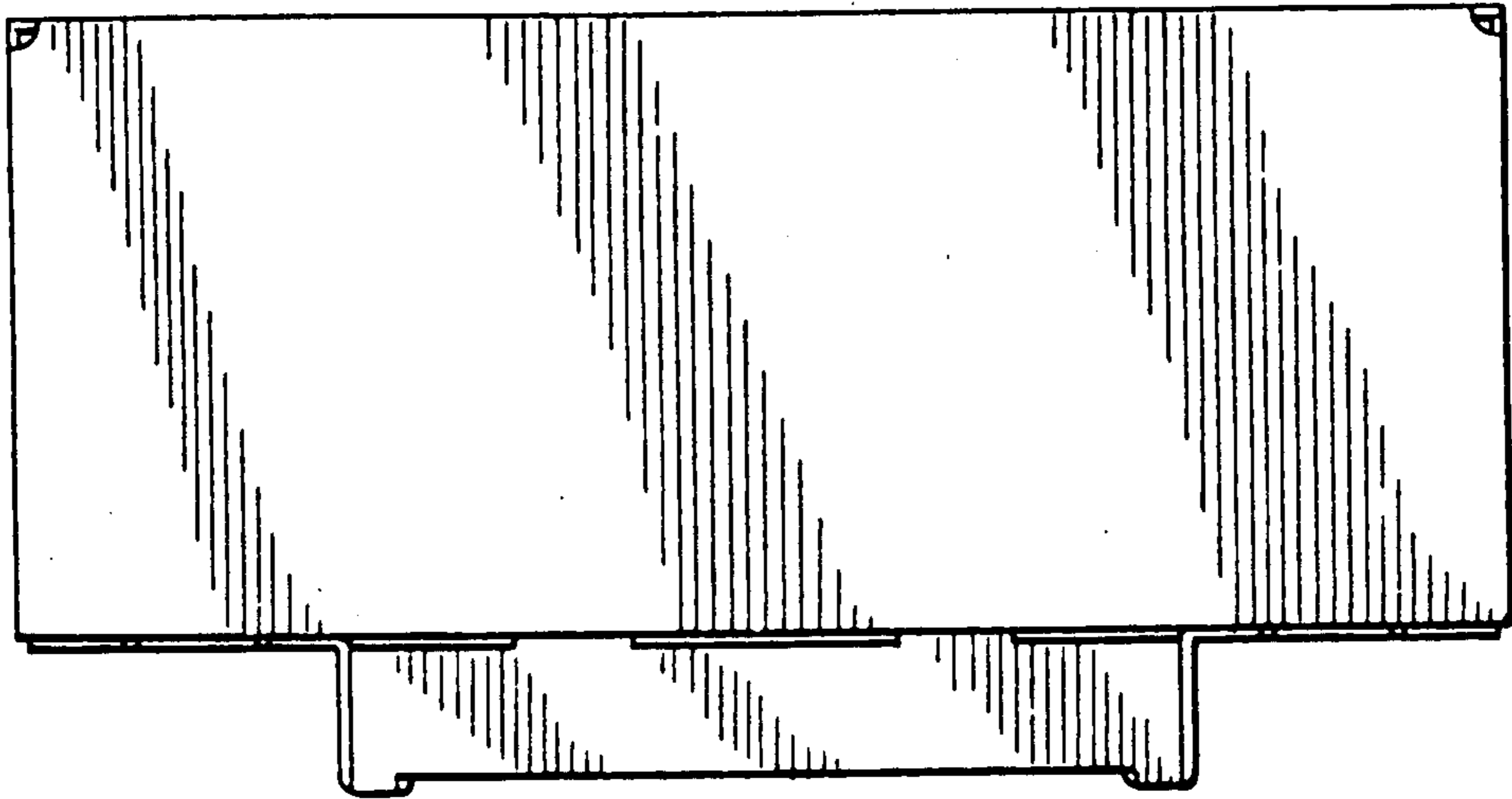


FIG. 1

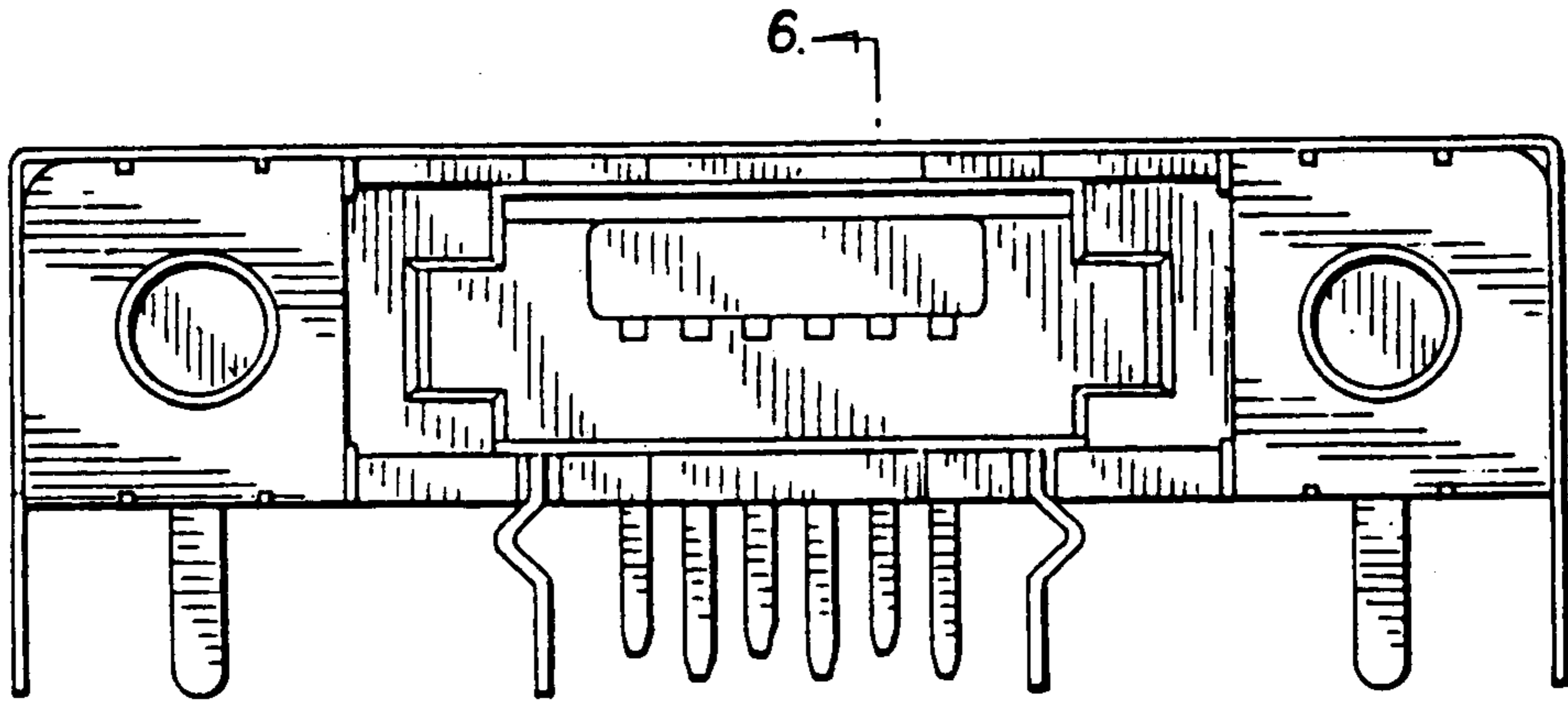


FIG. 2

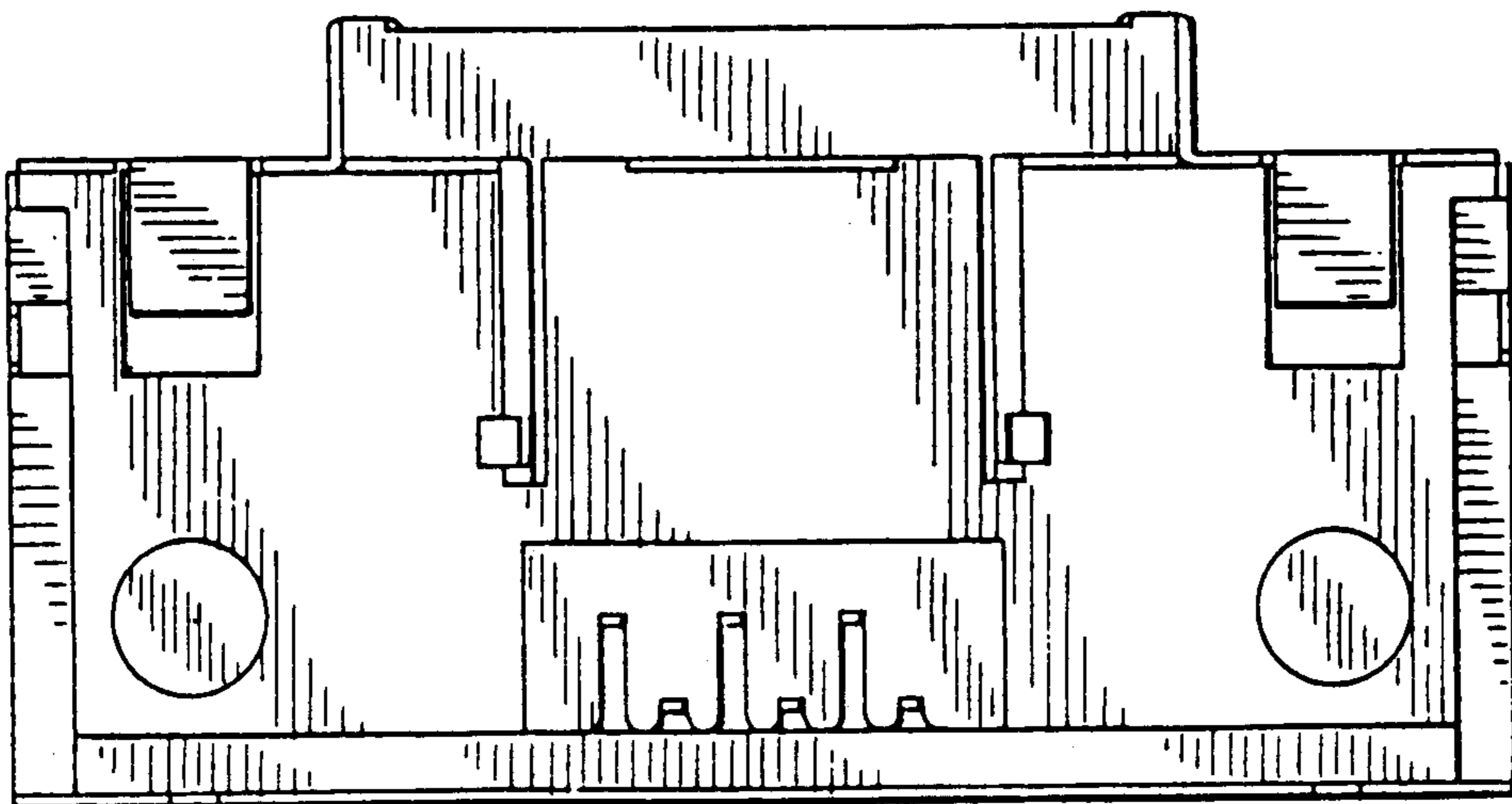


FIG. 3

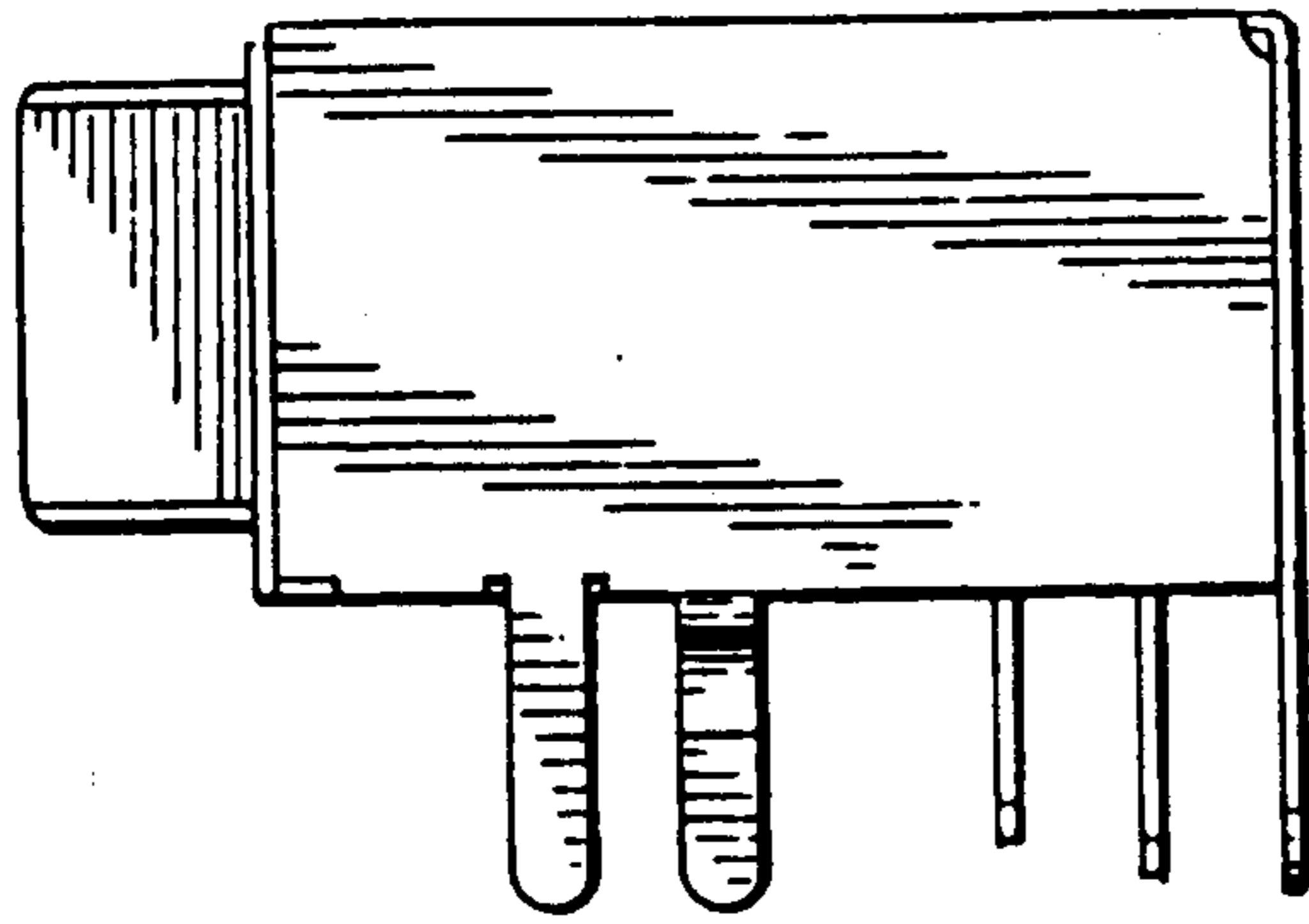


FIG. 4

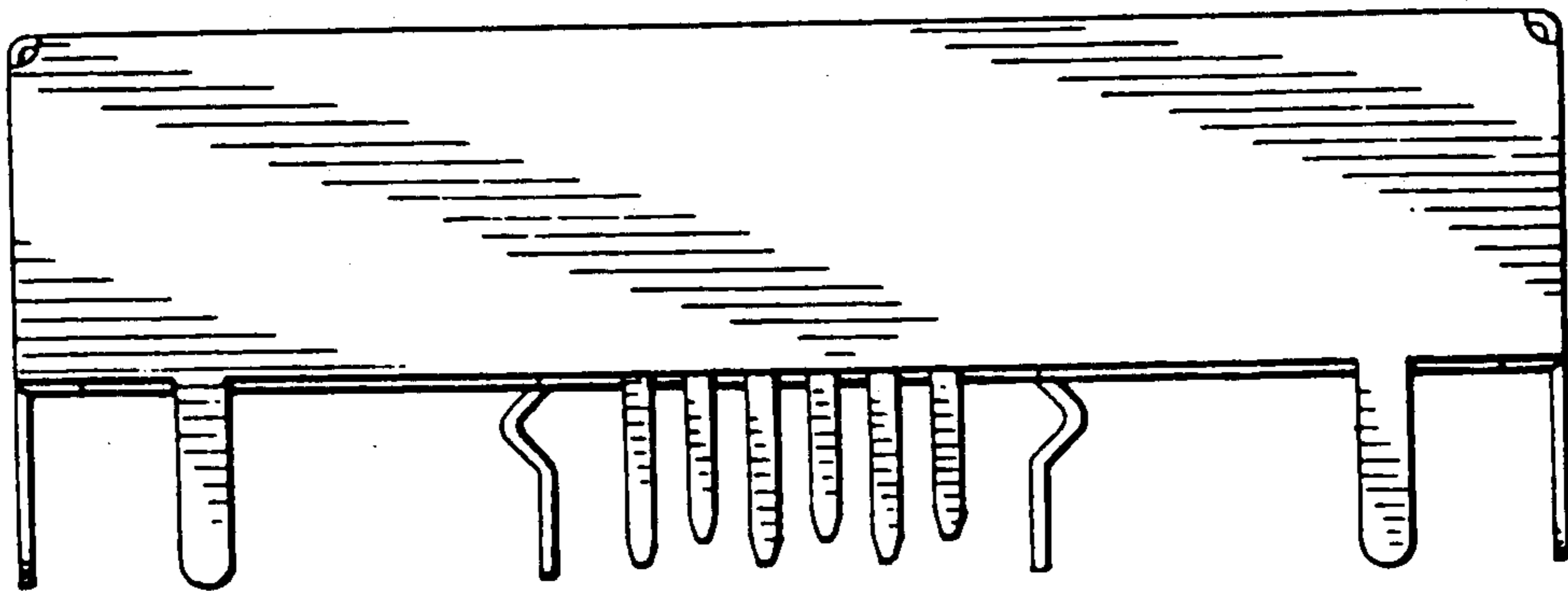


FIG. 5

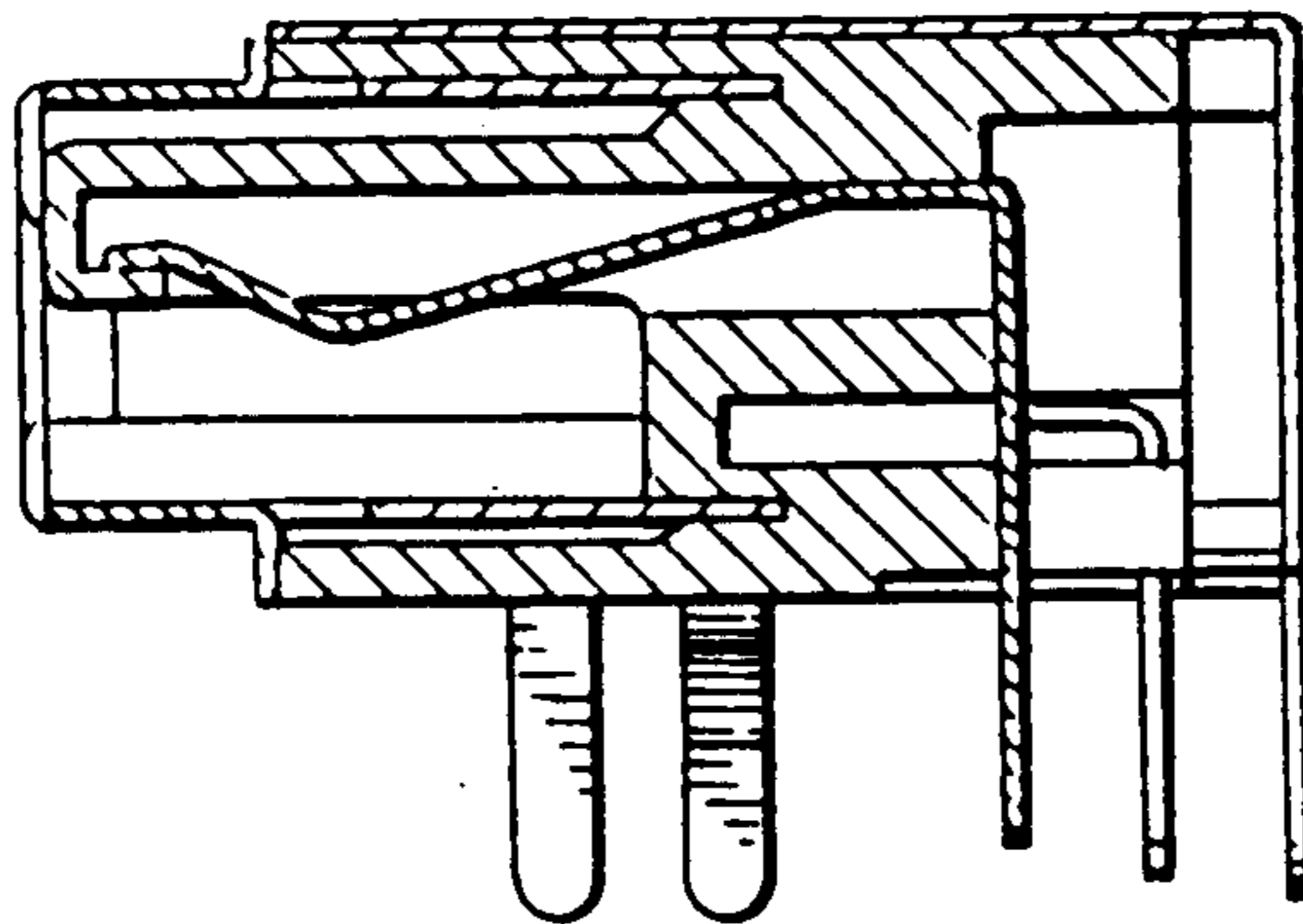


FIG. 6