

United States Patent [19]**Fujioka**[11] **Patent Number:** **Des. 318,045**[45] **Date of Patent:** **** Jul. 9, 1991**[54] **ABSORBER OF ELECTRICAL NOISE**[75] **Inventor:** **Akio Fujioka, Ichikawa, Japan**[73] **Assignee:** **Kitagawa Industries Co., Ltd., Aichi, Japan**[**] **Term:** **14 Years**[21] **Appl. No.:** **405,351**[22] **Filed:** **Sep. 11, 1989**[30] **Foreign Application Priority Data**

Mar. 20, 1989 [JP] Japan 1-10055

[52] **U.S. Cl. D13/199**[58] **Field of Search D13/156, 157, 118, 133, D13/147; D8/356, 396; 174/72 A, 74 A, 74 R, 52.1, 92, 138 F; 336/65, 92**[56] **References Cited****U.S. PATENT DOCUMENTS**

D. 269,064 5/1983 Ericsson D8/356
 D. 308,017 5/1990 Fujioka D8/396
 2,540,101 2/1951 Coleman 174/92 X

3,499,570 3/1970 Button 220/3.8 X
 3,576,937 5/1971 Eldridge, Jr. 174/92
 4,882,561 11/1989 Fujioka 336/65

Primary Examiner—Susan J. Lucas
Assistant Examiner—J. Sincavage
Attorney, Agent, or Firm—Oliff & Berridge

[57] **CLAIM**

The ornamental design for absorber of electric noise, as shown and described.

DESCRIPTION

FIG. 1 is a front elevational view of an absorber of electrical noise showing my new design, the rear elevational view being a mirror image;
 FIG. 2 is an end elevational view thereof, the opposite end elevational view being a mirror image;
 FIG. 3 is a top plan view thereof.
 FIG. 4 is a bottom plan view thereof.
 FIG. 5 is a perspective view thereof. The broken line showing of the conductors is included for the purpose of illustrating environmental elements only and forms no part of the claimed design.

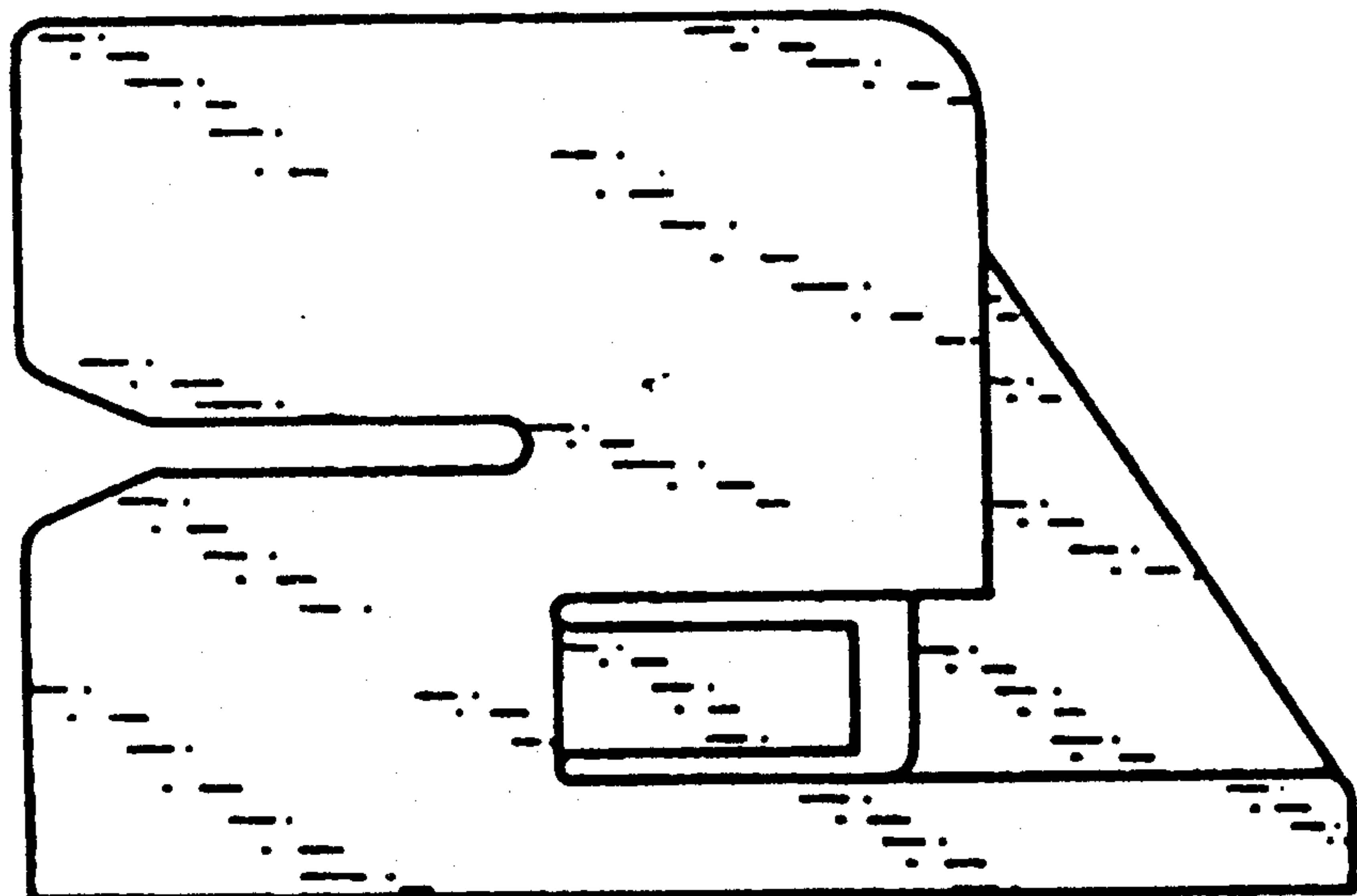


FIG. 1

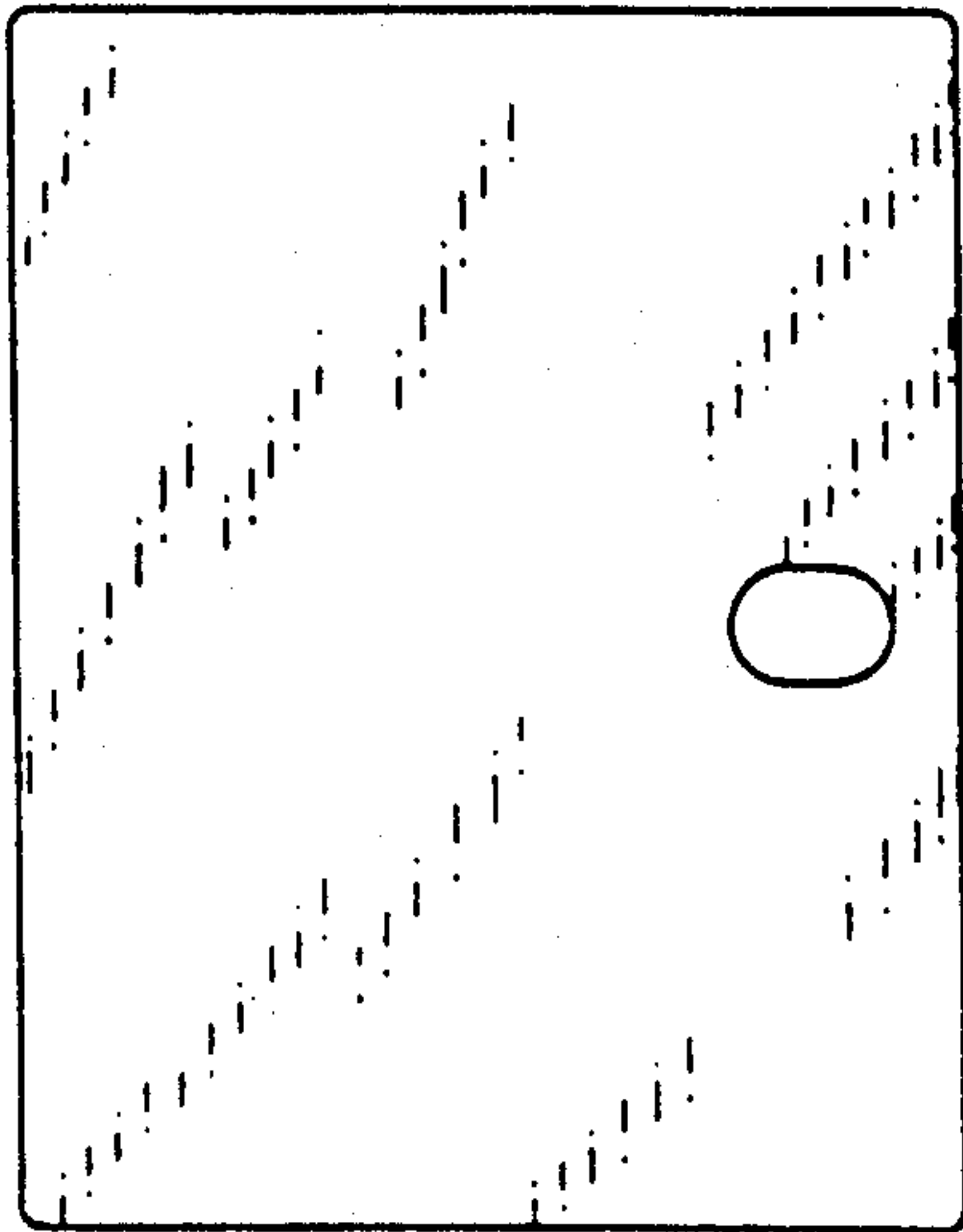


FIG. 2

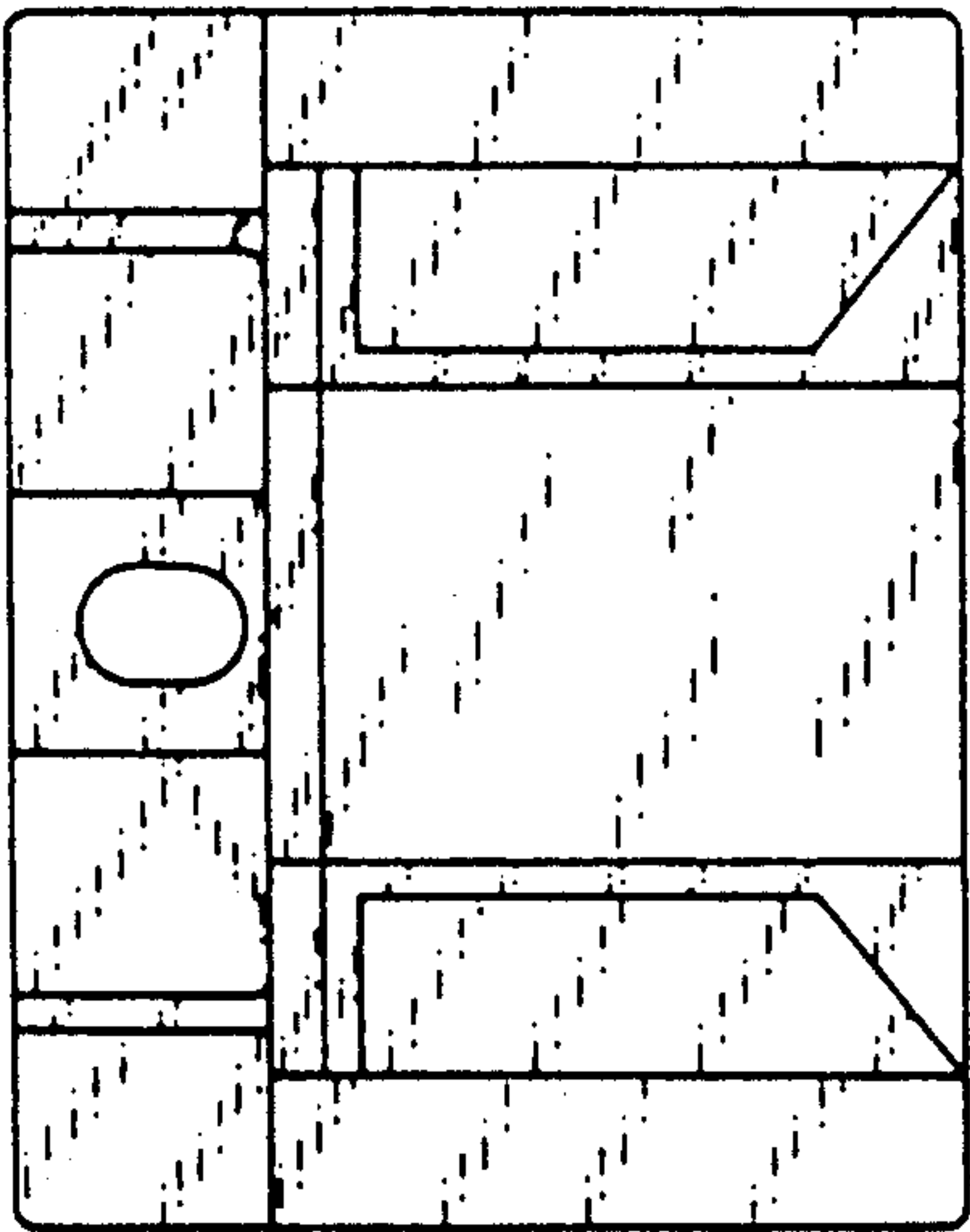


FIG. 3

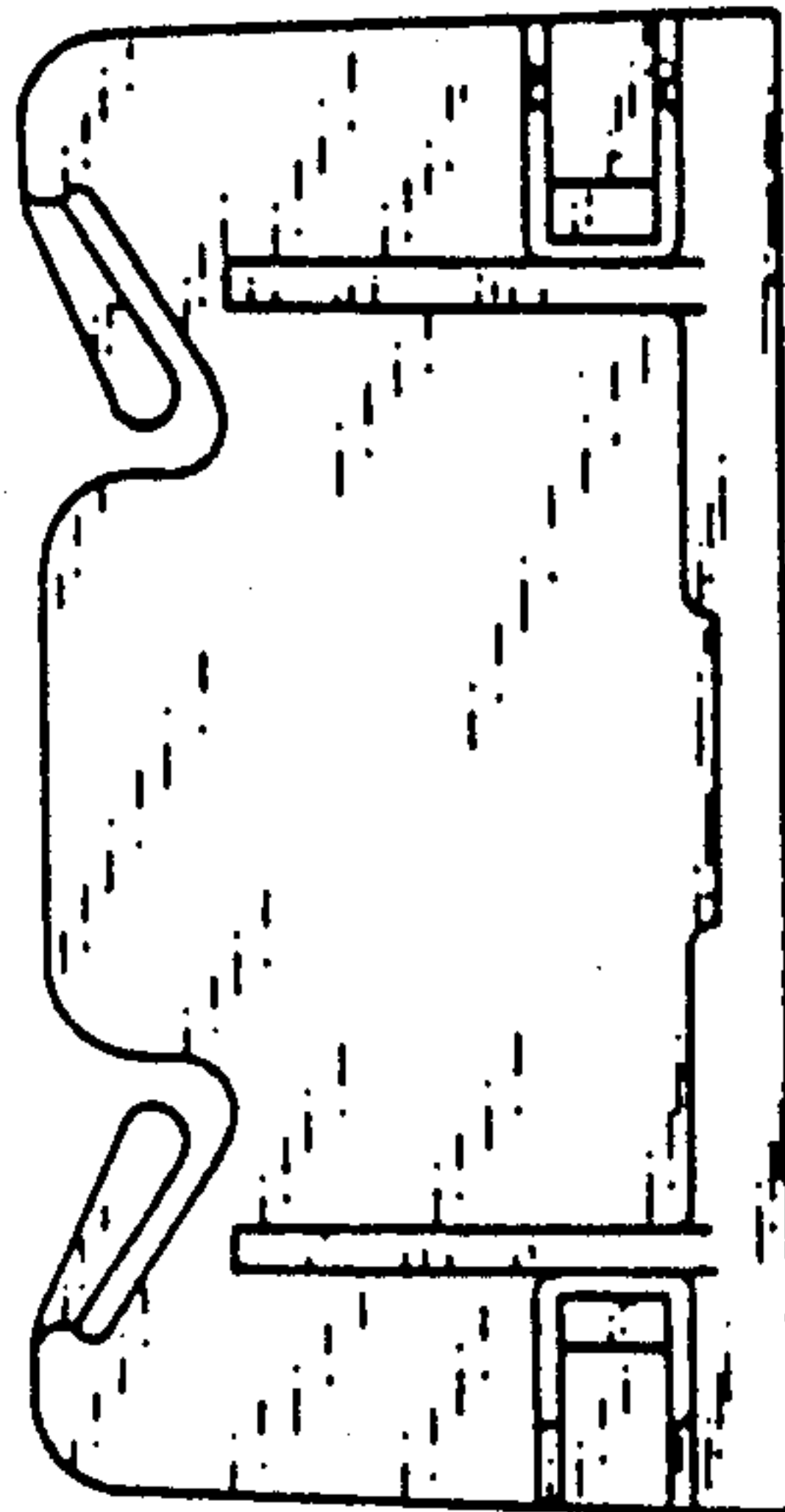


FIG. 4

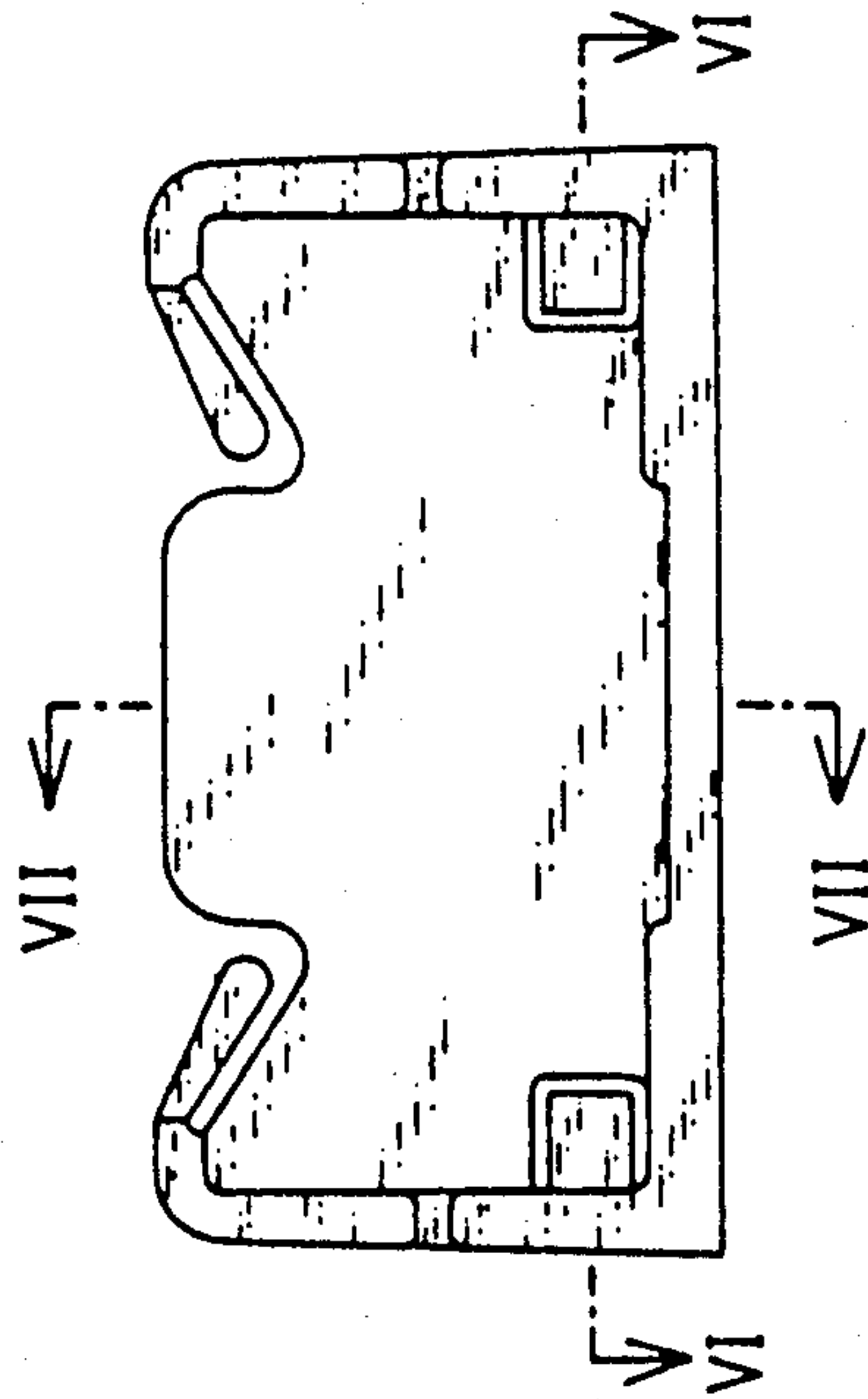


FIG. 5

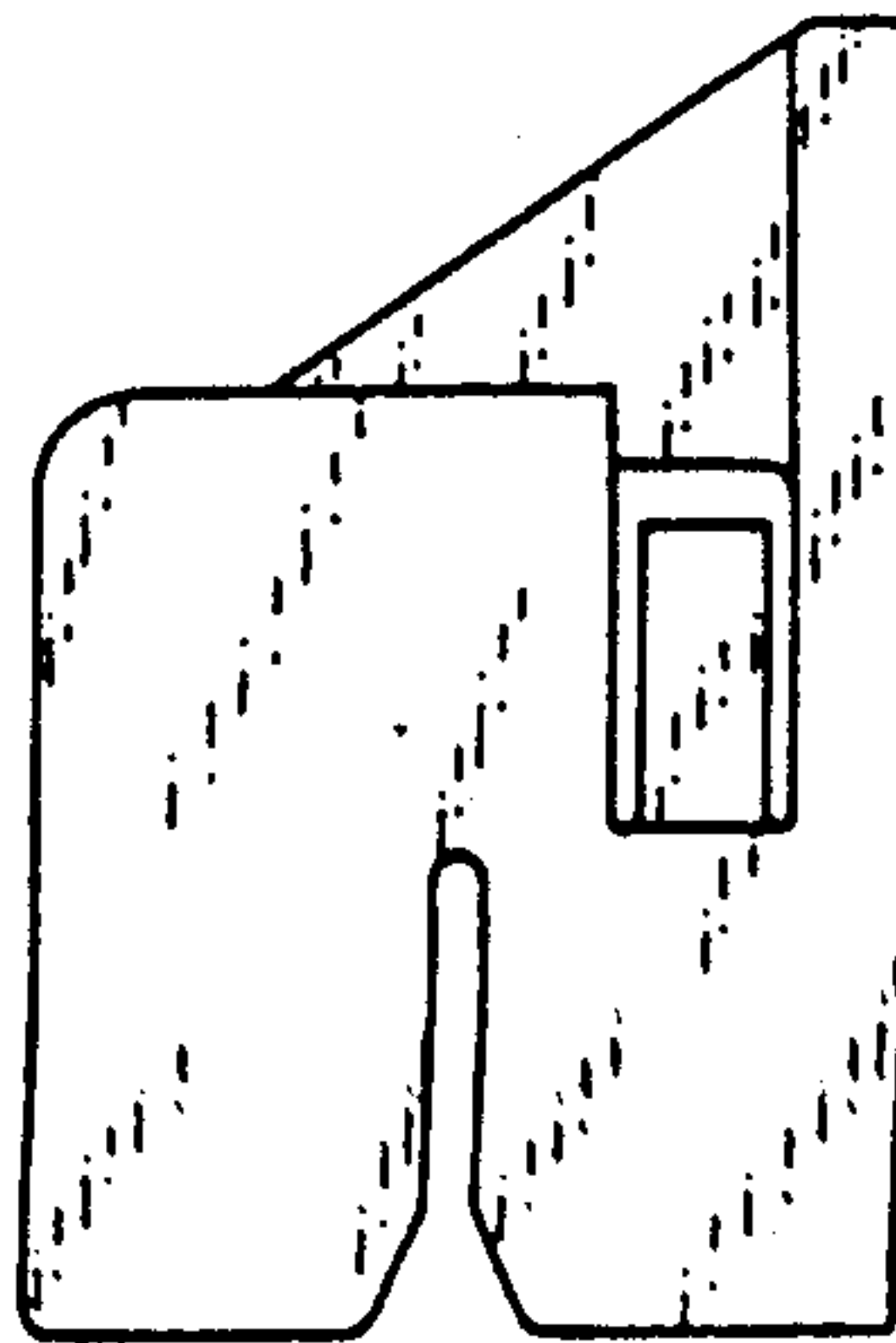


FIG. 7

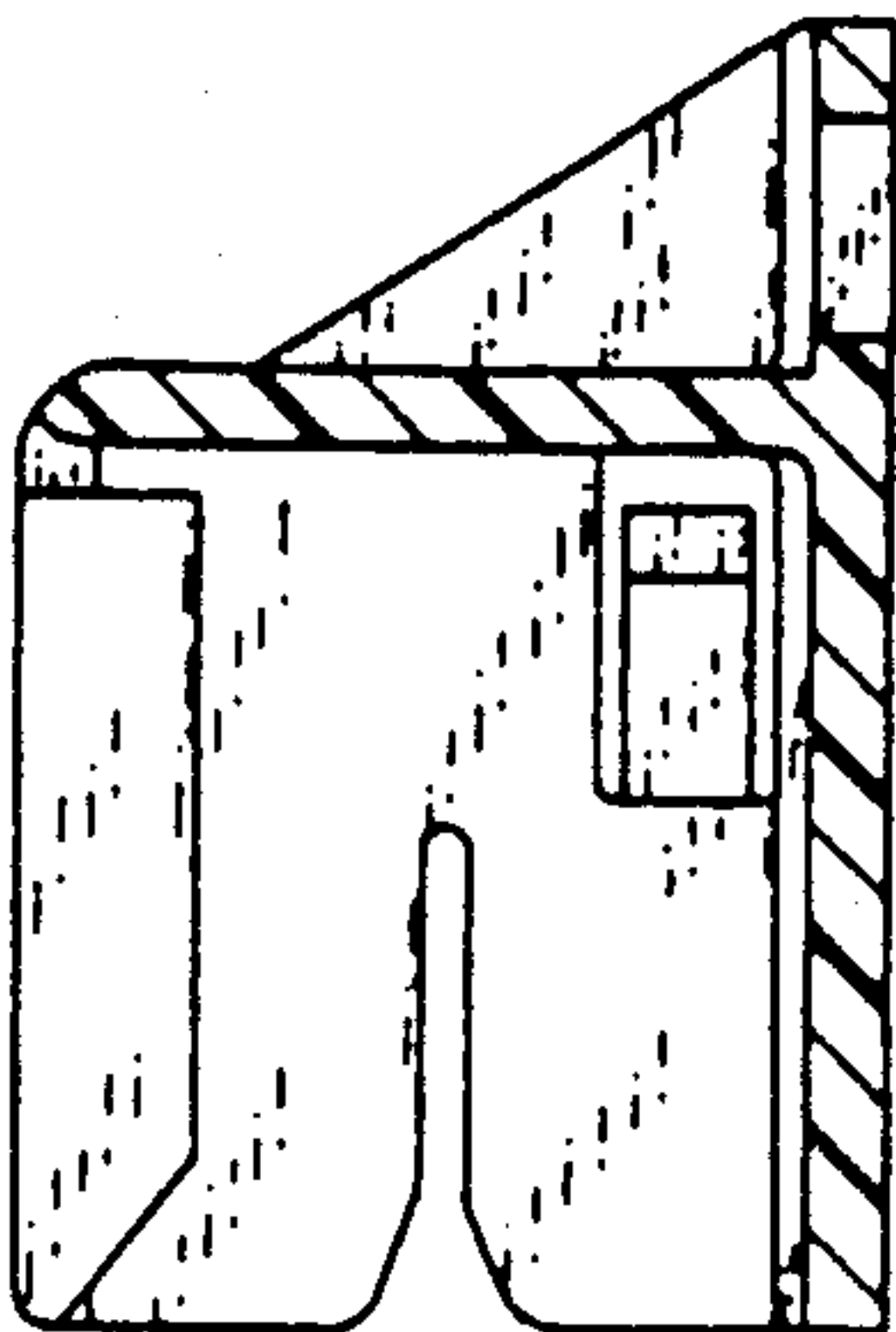


FIG. 8

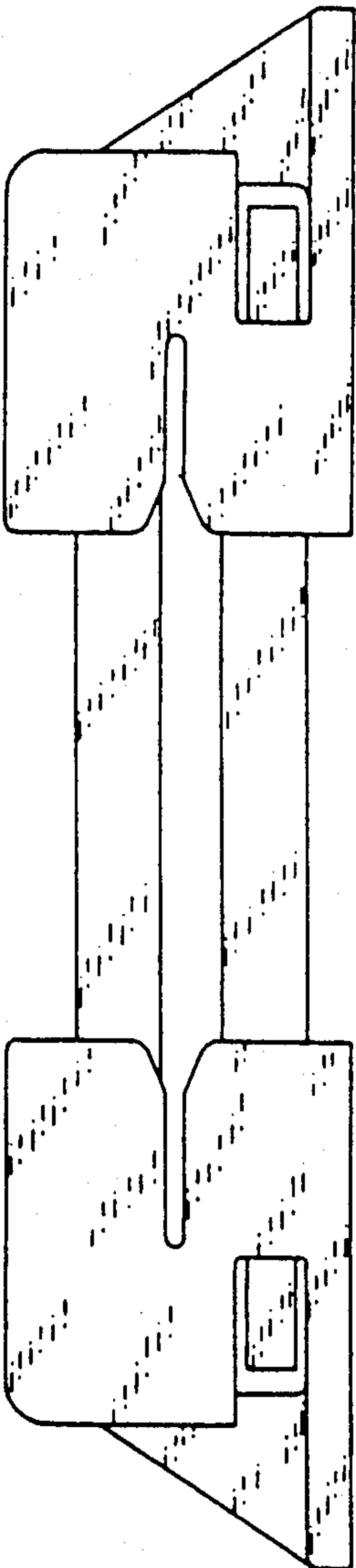


FIG. 6

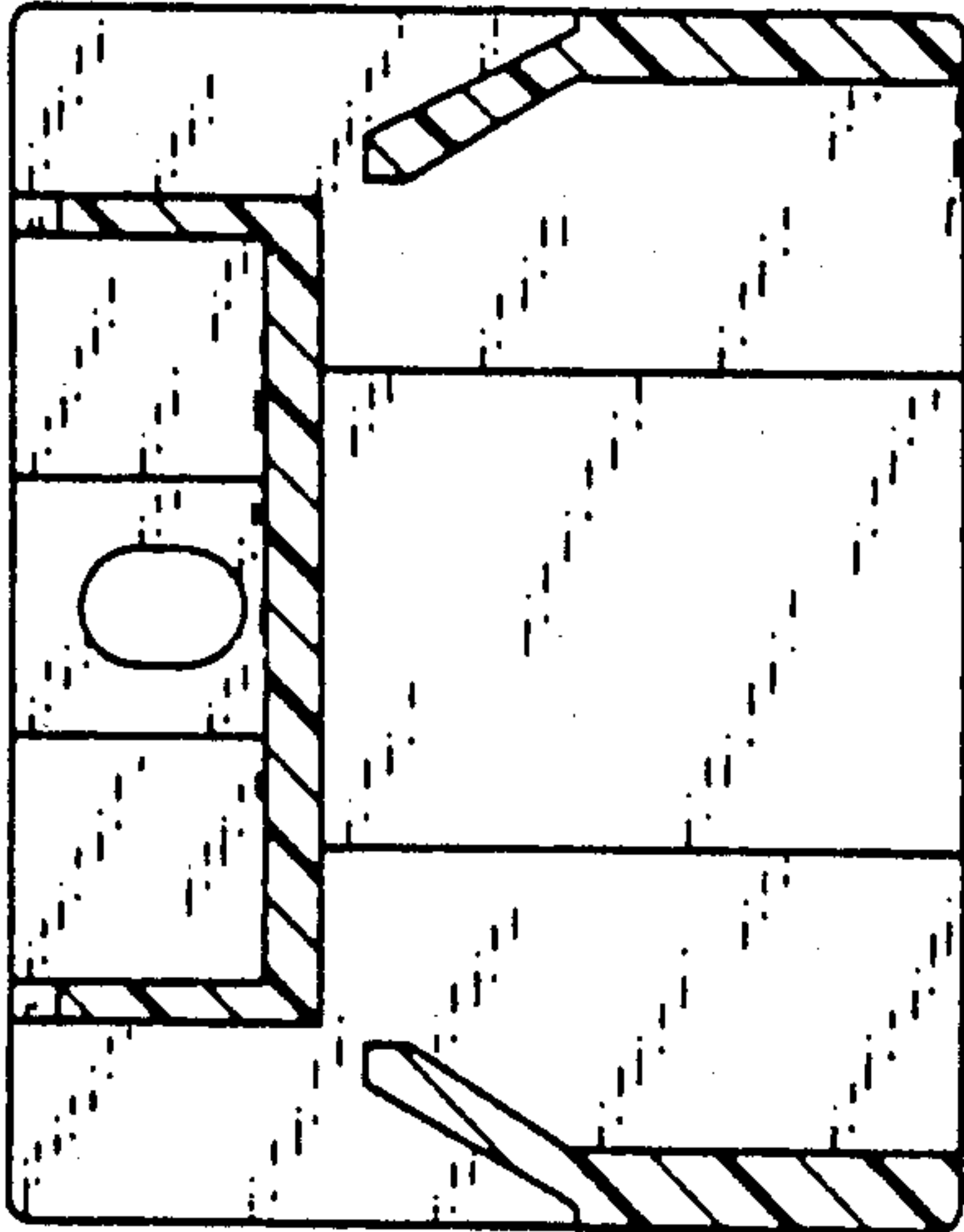


FIG. 9

